# Lithium-Ion Battery Charge Control for AC Charger Monolithic IC MM1707 Series

### **Outline**

This IC is a one-cell lithium ion battery charge control IC for AC chargers.

The charging current and charging voltage can be optimally controlled by controlling the primary side through a photocoupler.

It incorporates a charging over timer, delay circuit for misoperation prevention, and overvoltage/overcurrent detection functions.

It includes a 2-channel LED display circuit, which allows displaying the charge status.

### **Features**

- 1. Various charge control functions
- 2. Battery temperature detection function
- 3. Recharge detection function Accuracy ±50mV
- 4. Full-charge detection function Accuracy ±3.4mV
- 5. Voltage control function for the primary side of an AC adaptor
- 6. Abnormal battery detection function
  - (Overdischarge/overcharge voltage detection functions, battery temperature detection function)
- 7. Abnormal charge prevention function with a built-in timer
- 8. 2-channel LED display function
- 9. Misoperation prevention function with a built-in delay circuit

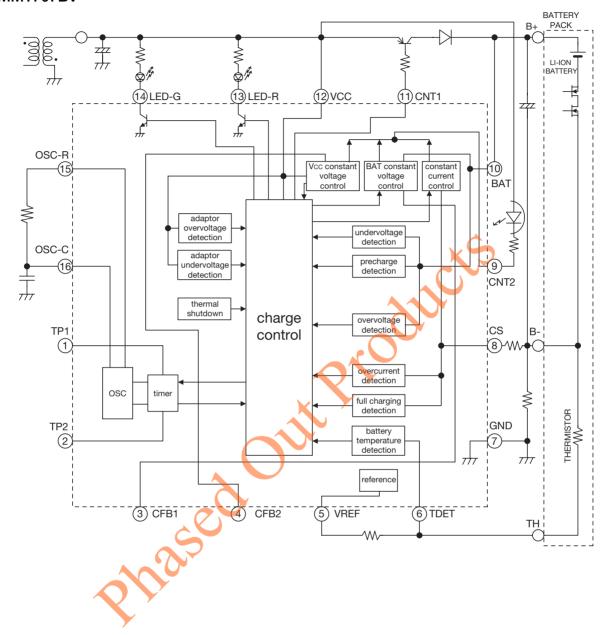
### **Package**

TSOP-16A, TSOP-16B

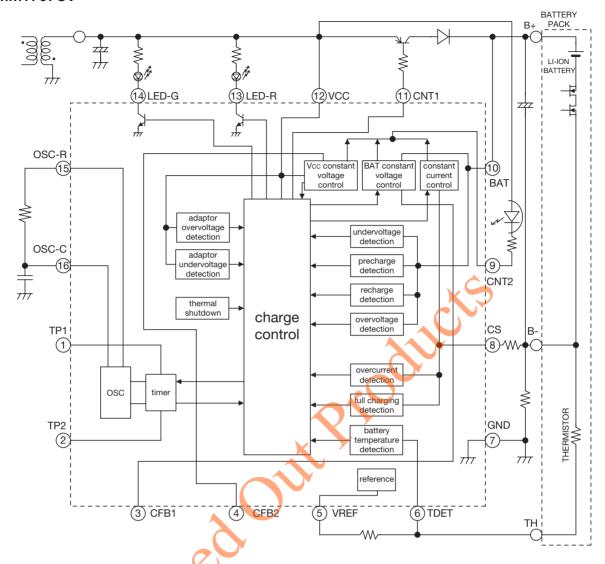
### **Applications**

- 1. Cell phones
- 2. Portable music players
- 3. Digital still cameras
- 4. Portable game devices
- 5. PDA

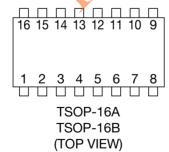
### Block Diagram



#### MM1707CV



### Pin Assignment



1	TP1	9	CNT2
2	TP2	10	BAT
3	CFB1	11	CNT1
4	CFB2	12	Vcc
5	VREF	13	LED-R
6	TDET	14	LED-G
7	GND	15	OSC-R
8	CS	16	OSC-C

### Pin Description

Pin no.	Pin name	I/O	Function
1	TP1	OUTPUT	TEST output pin. Pre-charge timer test pin. Inverts while couting and output to TP1, to permit monitoring. Also, TP1 output signal is invered again inside the IC and inputs to the next stage FF.
2	TP2	OUTPUT	TEST output pin. Full-charge timer test pin. Same structure as TP1.
3	CFB1	INPUT	Rated BAT voltage control phase compensation pin. Oscillation is improved by connecting an external capacitor between CFB1 and CNT2 for phase compensation.
4	CFB2	INPUT	Rated Vcc voltage control phase compensation pin. Oscillation is improved by connecting an external capacitor between CFB2 and CNT2 for phase compensation.
5	VREF	OUTPUT	Reference power supply output pin. Outputs about 1.2Volts typ. reference voltage. Used for temperature detection reference power supply .
6	TDET	INPUT	Temperature detection input pin.  Apply potential resistance divided by external resistor and thermistor from reference voltage when using. Reset state will exist if TDET pin does not reach the specified potential.
7	GND	INPUT	GND pin
8	CS	INPUT	Current detection input pin.  Detects charging current by external resistor (between CS and GND) voltage drop and cintrols charging current.  The full charge current is set using following formula; $I_{chg}=VL1(V)/R1(\Omega)$
9	CNT2	OUTPUT	Photo diode drive pin of photo coupler for Vcc and BAT constant voltage and constant current control.  Connect to cathode of diode.
10	BAT	INPUT	Battery voltage output pin.  Detect battery voltage and cntrol charging.
11	CNT1	OUTPUT	Charging control output pin. Controls external PNP-Tr.
12	Vcc	INPUT	Power supply input pin.
13	LED-R	OUTPUT	LED-R output pin. NPN-Tr open collector output.
14	LED-G	OUTPUT	LED-G output pin. NPN-Tr open collector output.
15	OSC-R	INPUT	Oscillator output pin Timer setting time changes according to oscillation cycle. Oscillation cycle is determined by an external resistor and capacitor.
16	OSC-C	OUTPUT	Oscillator inverted input pin.

### Pin Description2

Pin no.	Pin name	Equivalent circuit diagram	Pin no.	Pin name	Equivalent circuit diagram
2	TP1	1.2V ————————————————————————————————————	6	TDET	Vcc // W VREF
			8	CS	Vcc /// VREF
3	CFB1	BAT Vcc WREF CFB1 VREF	9	CNT2	GND m  CNT2
4	CFB2	Vcc Vcc VREF VREF	10	BAT	BAT Vcc
5	VREF	VCC  VREF  VREF  WCS	11	CNT1	Vcc //// CNT1

Pin no.	Pin name	Equivalent circuit diagram	Pin no.	Pin name	Equivalent circuit diagram
13	LED-R	Vcc //// //// //// //// /// /// /// ///	15	OSC-R	1.2V OSC-R GND
14	LED-G	Vcc ## LED-G	16	OSC-C	OSC-C W M GND

### Absolute Maximum Ratings (Ta=25°C)

Item	Symbol	Ratings	Units
Storage temperature	Tsrg	-55~+150	°C
Operating temperature	Topr	-40~+85	°C
Supply voltage	V <sub>DD</sub> max.	-0.3~+12	V
Allowable loss	Pd	300	mW

### Recommended Operating Conditions

Item	Symbol	Ratings	Units
Operating temperature	Topr	-40~+85	°C
Supply voltage	Vopr	2.7~5.9	V

Electrical Characteristics (Except where noted otherwise Ta=25°C, Vcc=5V)

Item	Symbol	Measurement conditions	Min.	Тур.	Max.	Units
Consumption current	Idd	Vcc=5V		6	8.4	mA
Reference voltage	Vref	Vcc=5V		1.238		V
Vcc detection voltage L	Vvccl	Vcc=H→L	2.35	2.45	2.55	V
Vcc detection voltage L	Vvcclw		50	100	150	mV
hysteresis voltage width	V VCCLW		30	100	150	111 V
Vcc detection voltage H	Vvcch	Vcc=L→H	6.1	6.3	6.5	V
Vcc detection voltage H	Vvcchw		50	100	150	mV
hysteresis voltage width	V VCCHW		50	100	150	
Vcc control voltage	Vvcc		4.1	4.2	4.3	V
BAT pin leakage current	<b>I</b> BAT	Vcc=5V, BAT=3.5V			1	μA
BAT pin output voltage	VBAT	Vcc=5V, Ta=0~+50°C	4.170	4.200	4.230	V
Current limit 1	V <sub>L</sub> 1	Full charge	148	154	160	mV
- Garrone miner	, r.	BAT=3.5V	10	101	100	111 4
Current limit 2	V <sub>L</sub> 2	Pre-charge	10.0	15.0	20.0	mV
	BAT=2.5V					
Full charge detection voltage	$V_{\mathrm{F}}$		15.0	20.0	25.0	mV
Overcurrent detection voltage	Voc		170	200	220	mV
Low voltage detection voltage	$V_{LV}$	VBAT=L→H	1.90	2.00	2.10	V
Low voltage detection voltage	$V_{ m LVW}$	Y	25	50	100	mV
hysteresis voltage width						
Pre-charge detection voltage	$V_{P}$	VBAT=L→H	2.80	2.90	3.00	V
Pre-charge detection voltage	$V_{\mathrm{PW}}$		25	50	100	mV
hysteresis voltage width						
Overvoltage detection voltage	Vov	VBAT=L→H	4.30	4.35	4.40	V
Battery temperature	V <sub>TH</sub>	Low temperature –3°C±3°C	0.919	0.950	0.981	V
detection voltage H		detection (rising threshold)				
Battery temperature	V <sub>TL1</sub>	High temperature 53°C±3°C	0.310	0.335	0.360	V
detection voltage L1		detection (falling threshold)				
Battery temperature detection	$V_{\mathrm{TL}2}$	High temperature 43°C±3°C	0.394	0.423	0.452	V
voltageL2 (abnomal reset)	T	detection (rising threshold)		20	150	A
TDET pin input current	ITDET	T 10A		30	150	nA
LED R pin output voltage	VLED R	ILED R=10mA ILED R=10mA			0.4	V
LED G pin output voltage CNT1 pin output voltage	VLED G VCNT1	ILED R=10mA ICNT1=10mA			0.4	V
	VCNT1 VCNT2	ICNT1=10mA ICNT2=5mA			0.4	V
CNT2 pin output voltage	V CNT2	Not including external deviation			0.4	V
Oscillation cycle	Tosc	Not including external deviation $R=130k\Omega$ , $C=0.\mu F$	16.47	18.3	20.13	ms
		K=130K <b>12</b> , C=0.µF				

<sup>\*</sup>Current limits 1, 2 and full charge detection are specified at current detection resistor voltage drop.

<sup>\*</sup>If the IC is damaged and control is no longer possible, its safety can not be guaranteed. Please protect with something other than this IC.

<sup>\*</sup>Temperature detection is the setting value at B constant 3435(10KC15-1608 made by Ishizuka Denshi).

<sup>\*</sup>Use a capacitor with good temperature characteristics in the oscillator. Capacitor deviation will contribute to

<sup>\*</sup>The standard value of Timer error time is Pre- charge, Full charge.

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Item	Symbol	Measurement conditions	Min.	Тур.	Max.	Units
Consumption current	$\mathbf{I}_{\mathrm{DD}}$	Vcc=5V		6	8.4	mA
Reference voltage	$V_{ m REF}$	Vcc=5V		1.238		V
Vcc detection voltage L	Vvccl	Vcc=H→L	2.35	2.45	2.55	V
Vcc detection voltage L	V		ΕO	100	150	V
hysteresis voltage width	Vvcclw		50	100	150	mV
Vcc detection voltage H	Vvcch	Vcc=L→H	6.1	6.3	6.5	V
Vcc detection voltage H	<b>1</b> 7		Ε0.	100	150	
hysteresis voltage width	Vvcchw		50	100	150	mV
Vcc control voltage	Vvcc		4.1	4.2	4.3	V
BAT pin leakage current	<b>I</b> BAT	Vcc=5V, BAT=3.5V			1	μА
BAT pin output voltage	VBAT	Vcc=5V, Ta=0~+50°C	4.170	4.200	4.230	V
0	77.4	Full charge	1.10	151	1.00	7.7
Current limit 1	$V_{L}1$	BAT=3.5V	148	154	160	mV
0	17.0	Pre-charge V	24.0	00.0	000	7.7
Current limit 2	$V_{L2}$	BAT=2.5V	24.0	30.0	36.0	mV
Full charge detection voltage	$V_{\mathrm{F}}$	40	16.8	21.0	25.2	mV
Overcurrent detection voltage	Voc		170	200	220	mV
Low voltage detection voltage	$V_{LV}$	VBAT=L→H		2.00	2.10	V
Low voltage detection voltage	3.7			F0	100	7.7
hysteresis voltage width	$V_{LVW}$			50	100	mV
Pre-charge detection voltage	$V_P$	VBAT=L→H	2.80	2.90	3.00	V
Pre-charge detection voltage	17	<b>V</b>	O.E.	Ε0	100	
hysteresis voltage width	$ m V_{PW}$		25	50	100	mV
Re-charge detection voltage	$V_R$	VBAT=H→L	3.85	3.90	3.95	V
Overvoltage detection voltage	Vov	VBAT=L→H	4.30	4.35	4.40	V
Battery temperature	V	Low temperature –3°C±3°C	0.010	0.050	0.001	17
detection voltage H	V <sub>TH</sub>	detection (rising threshold)	0.919	0.950	0.981	V
Battery temperature	VTL1	High temperature 53°C±3°C	0.310	0.335	0.260	V
detection voltage L1	VILI	detection (falling threshold)		0.555	0.360	\ \ \ \ \ \
Battery temperature detection	V <sub>TL2</sub>	High temperature 43°C±3°C	0.204	0.492	0.459	v
voltage L2 (abnomal reset)	V11.2	detection (rising threshold)		0.423	0.432	\ \ \
TDET pin input current	Itdet			30	150	nA
LED R pin output voltage	VLED R	ILED R=10mA			0.4	V
LED G pin output voltage	VLED G	ILED R=10mA			0.4	V
CNT1 pin output voltage	Vcnt1	Icnti=10mA			0.4	V
CNT2 pin output voltage	V <sub>CNT2</sub>	Існт2=5mА			0.4	V
Oppillation such	Т.	Not including external deviation	16 47	10.0	90.19	404 =
Oscillation cycle	Tosc	R=130kΩ, C=0.1μF	16.47	18.3	20.13	ms
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- \*Current limits 1, 2 and full charge detection are specified at current detection resistor voltage drop.
- \*If the IC is damaged and control is no longer possible, its safety can not be guaranteed. Please protect with something other than this IC.
- \*Temperature detection is the setting value at B constant 3435(10KC15-1608 made by Ishizuka Denshi).
- \*Use a capacitor with good temperature characteristics in the oscillator. Capacitor deviation will contribute to timer error.
- \*When the battery overdischarge condition, it 3mA charge for 36 seconds, and then it does not switch to precharging during that interval, it means the IC has identified a battery abnormality.
- \*The standard value of Timer error time is Pre- charge, Full charge, 3mA charge, and Blinking cycle.

### Electrical Characteristics2 (OSC Capacitor setting Note)

### Oscillation cycle

Unit: s

R	<b>75k</b> Ω	<b>100k</b> Ω	<b>120k</b> Ω	130kΩ	150kΩ	<b>200k</b> Ω
0.047μF	4.9m	6.5m	7.8m	8.5m	9.8m	13.0m
0.082μF	8.7m	11.6m	13.9m	15.1m	17.4m	22.9m
0.1μF	10.7m	14.2m	16.9m	18.3m	21.1m	28.2m
0.15μF	16.0m	21.2m	25.4m	27.7m	31.8m	42.1m
0.22μF	23.2m	31.1m	37.5m	40.8m	46.5m	61.8m

### ■ Timer of each times

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H	Calculation	Examples of calculation		
Item	formula	(for C=0.01μF, R=130kΩ; T=18.3ms)		
Pre-charge timer (VBAT>2.0V)	T×2 <sup>16</sup>	1199s (19min 59s)		
		19189s		
Full charge timer	$T \times 2^{20}$	(5h 19min 49s)		
3mA charge timer	$T \times 2^{11}$	37.5s		
Full charge detection delay time	$T \times 2^3$	0.146s		
Overcurrent detection delay time	$T \times 2^5$	0.586s		
Overvoltage detection delay time	$T\times 2^5$	0.586s		
Battery temperature detection delay time	$T \times 2^1$	0.037s		

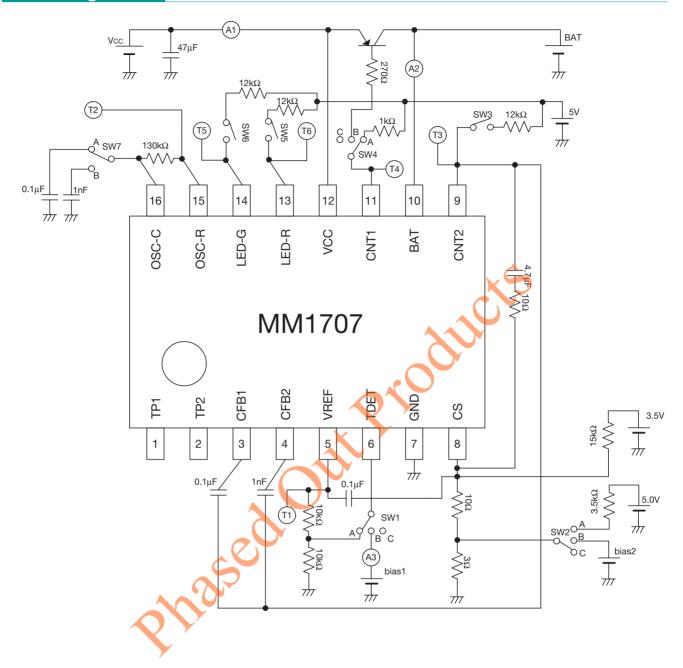
T; OSC oscillation cycle

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Itam A C	Calculation	Examples of calculation
Item	formula	(for C=0.01μF, R=130kΩ; T=18.3ms)
Pre-charge timer (VBAT>2.0V)	$T \times 2^{16}$	1199s
Tre-charge timer (VDA1>2.0V)	1 / 2	(19min 59s)
Full charge timer	$T \times 2^{20}$	19189s
i dii charge timei	1 ^2	(5h 19min 49s)
3mA charge timer	$T\times 2^{11}$	37.5s
Full charge detection delay time	$T \times 2^3$	0.146s
Overcurrent detection delay time	$T \times 2^5$	0.586s
Overvoltage detection delay time	$T \times 2^5$	0.586s
Re-charge detection delay time	$T \times 2^2$	0.073s
Battery temperature detection delay time	$T \times 2^1$	0.037s
LED-R blinking cycle	$T \times 2^7$	2.342s

T; OSC oscillation cycle

### **Measuring Circuit**



Measuring Procedures (Except where noted otherwise Ta=25°C, SW1: A, SW31: ON, SW41: A, SW51: ON, SW6: ON, SW7: A)

Item	Measuring procedures
Compounding oursel	The A1 current value when SW1: B, SW2: C, SW3: OFF, SW4: B, SW5: OFF,
Consumption current	SW6: OFF, BAT=3.5V and bias1=1.25V is Icc.
Deference voltere	The T1 voltage when SW1: C, SW2: C, SW3: OFF, SW4: B, SW5: OFF, SW6:
Reference voltage	OFF and BAT=3.5V is V <sub>REF</sub> .
Ver detection voltage I	BAT=1.85V. Gradually decrease Vcc from 2.6V. The Vcc voltage when T2
Vcc detection voltage L	oscillation stops is Vccl.
Vcc detection voltage L	BAT=1.85V. Gradually raise Vcc from 2.3V. The Vcc voltage when T2 starts to
hysteresis voltage width	oscillate is Vcc12 and the difference between Vcc1 and Vcc12 is Vcc1w.
Vcc detection voltage H	BAT=2.2V. Gradually raise Vcc from 6.05V. The Vcc voltage when T2 oscillation
vec detection voltage n	stops is Vcch.
Vcc detection voltage H	BAT=2.2V. Gradually raise Vcc from 6.55V. The Vcc voltage when T2 starts to
hysteresis voltage width	oscillate is Vccн2 and the difference between Vccң and Vccн2 is Vccнw.
Vcc control voltage	BAT=2.2V. Gradually raise Vcc from 4V. Vcc when T3 switches from 5V to 0V is
voc control voltage	Vvcc.
BAT pin leakage current	The A2 current value when Vcc=5V and BAT=3.5V is IBAT.
BAT pin output voltage	Vcc=5.0V, bias2=0.05V and SW2: B. Gradually decrease BAT from 4.05V. The
2711 piir Gaipar Tanaga	BAT voltage when T3 switches from 5V to 0V is VBAT1 and VBAT=VBAT1-0.05.
Current limit 1 (Full charge)	Vcc=5V, BAT=3.5V and SW2: B. Gradually raise bias2 from 0.14V. The bias2
( 1 1 3 7	voltage when T3 switches from 5V to 0V is V <sub>L1</sub> .
Current limit 2 (Pre-charge)	Vcc=5V, BAT=2.5V and SW2: B. Gradually raise bias2 from 0.01V. The bias2
, ,	voltage when T3 switches from 0V to 5V is V <sub>12</sub> .
Full charge detection voltage	Vcc=4.5V, BAT=4.15V and SW2: B. Gradually decrease Bias2 from 0.03V. The
	bias2 voltage when T5 switches from 0V to 5V is V <sub>F</sub> .
Overcurrent detection voltage	Vcc=4.5V, BAT=3.5V and SW2: B. Gradually raise Bias2 from 0.16V. The bias2
	voltage when T4 switches from 0V to 5V is Voc.
Low voltage detection voltage	Vcc=4.05V and gradually raise BAT from 1.85V. The BAT voltage when T4 switches from 5V to 0V is VLV.
Low voltage detection voltage	Vcc=4.05V and gradually decrease BAT from 2.2V. The BAT voltage when T4
hysteresis voltage width	switches from 0V to 5V is V <sub>LV2</sub> and the difference between V <sub>LV</sub> and V <sub>LV2</sub> is V <sub>LVW</sub> .
	Vcc=4.3V, bias2=0.027V, SW2: B and gradually raise BAT from 2.75V. The BAT
Pre-charge detection voltage	voltage when T3 switches from 5V to 0V is V <sub>P1</sub> and V <sub>P</sub> =V <sub>P1</sub> -0.027.
	Vcc=4.3V, bias2=0.027V, SW2: B and gradually decrease BAT from 3.2V. The
Pre-charge detection voltage	BAT voltage when T3 switches from 0V to 5V is V <sub>P2</sub> and the difference between
hysteresis voltage width	V <sub>P1</sub> and V <sub>P2</sub> is V <sub>PW</sub> .
	Vcc=4.5V, bias2=0.05V and SW2: B. Gradually raise BAT from 4.05V. The BAT
Overvoltage detection voltage	voltage when T6 switches from 0V to 5V is Vov1, and Vov=Vov1–0.05.
Battery temperature	Vcc=4.05V, BAT=2.5V and SW1: B. Gradually raise bias1 from 0.90V. The bias1
detection voltage H	voltage when T6 switches from 0V to 5V is V <sub>TH</sub> .
Battery temperature	Vcc=4.05V, BAT=2.5V and SW1: B. Gradually decrease bias1 from 0.40V. The
detection voltage L1	bias1 voltage when T6 switches from 0V to 5V is V <sub>TL1</sub> .
Battery temperature	Vcc=4.05V, BAT=2.5V and SW1: B. Gradually decrease bias1 from 0.30V. The
detection voltageL2	bias1 voltage when T6 switches from 5V to 0V is V <sub>TL2</sub> .
TDET pin input current	Vcc=4.5V, BAT=2.5V, bias1=1.2V, SW1: B and the current value A3 is ITDET.
LED R pin output voltage	Vcc=4.5V, BAT=2.5V, SW3: OFF, SW4: C, SW5: OFF, SW6: OFF and the T6
	voltage when 10mA flows on T6 is VLEDR.
LED G pin output voltage	Vcc=4.5V, BAT=2.5V, SW3: OFF, SW4: C, SW5: OFF, SW5: OFF and the T5
LLD a pin output voltage	voltage when 10mA flows on T5 is VLEDG.

Item	Measuring procedures
CNT1 pin output voltage	Vcc=4.5V, BAT=3.5V, SW3: OFF, SW4: C, SW5: OFF, SW5: OFF and the T4
	voltage when 10mA flows on T4 is Vcnt1.
CNT2 pin output voltage	Vcc=4.5V, BAT=2.5V, SW3: OFF, SW4: C, SW5: OFF, SW5: OFF and the T3
	voltage when 5mA flows on T3 is Vcnt2.
Oscillation cycle	The cycle of T2 signal when Vcc=4.5V and BAT=2.5V is Tosc.

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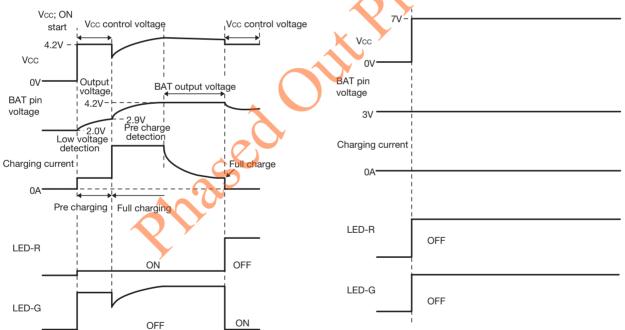
Item	Measuring procedures
Consumption current	The A1 current value when SW1: B, SW2: C, SW3: OFF, SW4: B, SW5: OFF,
	SW6: OFF, BAT=3.5V and bias1=1.25V is Icc.
Reference voltage	The T1 voltage when SW1: C, SW2: C, SW3: OFF, SW4: B, SW5: OFF, SW6:
	OFF and BAT=3.5V is VREF.
Vcc detection voltage L	BAT=1.85V. Gradually decrease Vcc from 2.6V. The Vcc voltage when T2
	oscillation stops is Vccl.
VCC detection voltage L	BAT=1.85V. Gradually raise Vcc from 2.3V. The Vcc voltage when T2 starts to
hysteresis voltage width	oscillate is Vcc12 and the difference between Vcc1 and Vcc12 is Vcc1w.
Vcc detection voltage H	BAT=2.2V. Gradually raise Vcc from 6.05V. The Vcc voltage when T2 oscillation
	stops is Vcch.
VCC detection voltage H	BAT=2.2V. Gradually raise Vcc from 6.55V. The Vcc voltage when T2 starts to
hysteresis voltage width	oscillate is VccH2 and the difference between VccH and VccH2 is VccHw.
Vcc control voltage	BAT=2.2V. Gradually raise Vcc from 4V. The Vcc when T3 switches from 5V to
	0V is Vvcc.
BAT pin leakage current	The A2 current value when Vcc=5V and BAT=3.5V is IBAT.
BAT pin output voltage	Vcc=5.0V, bias2=0.05V and SW2: B. Gradually decrease BAT from 4.05V. The
Ditt pill output voltage	BAT voltage when T3 switches from 5V to 0V is VBAT1 and VBAT=VBAT1-0.05.
Current limit 1 (Full charge)	Vcc=5V, BAT=3.5V and SW2: B. Gradually raise bias2 from 0.14V. The bias2
Carront mine i (i un onargo)	voltage when T3 switches from 5V to 0V is VL1.
Current limit 2 (Pre-charge)	Vcc=5V, BAT=2.5V and SW2: B. Gradually raise bias2 from 0.02V. The bias2
	voltage when T3 switches from 0V to 5V is V <sub>12</sub> .
Full charge detection voltage	Vcc=4.5V, BAT=4.15V and SW2: B. Gradually decrease Bias2 from 0.03V. The
	bias2 voltage when T6 switches from 0V to 5V is V <sub>F</sub> .
Overcurrent detection voltage	Vcc=4.5V, BAT=3.5V and SW2: B. Gradually raise Bias2 from 0.16V. The bias2
	voltage when T4 switches from 0V to 5V is Voc.
Low voltage detection voltage	Vcc=4.05V and gradually raise BAT from 1.85V. The BAT voltage when T4
	switches from 5V to 0V is V <sub>L</sub> V.  Vcc=4.05V and gradually decrease BAT from 2.2V. The BAT voltage when T4
Low voltage detection voltage hysteresis voltage width	switches from 0V to 5V is V <sub>LV2</sub> and the difference between V <sub>LV</sub> and V <sub>LV2</sub> is V <sub>LVW</sub> .
Trysteresis voltage width	Vcc=4.3V, bias2=0.027V, SW2: B and gradually raise BAT from 2.75V. The BAT
Pre-charge detection voltage	voltage when T3 switches from 5V to 0V is V <sub>P1</sub> and V <sub>P</sub> =V <sub>P1</sub> -0.027.
	Vcc=4.3V, bias2=0.027V, SW2: B and gradually decrease BAT from 3.2V. The
Pre-charge detection voltage	BAT voltage when T3 switches from 0V to 5V is V <sub>P2</sub> and the difference between
hysteresis voltage width	V <sub>P1</sub> and V <sub>P2</sub> is V <sub>PW</sub> .
	Vcc=4.35V, bias2=0.05V and SW2: B. Gradually decrease BAT from 4.05V. The
Re-charge detection voltage	BAT voltage when T6 switches from 5V to 0V is V <sub>R1</sub> and V <sub>R</sub> =V <sub>R1</sub> -0.05.
Overvoltage detection voltage	Vcc=4.5V, bias2=0.05V and SW2: B. Gradually raise BAT from 4.05V. The BAT
	voltage when T6 switches from 0V to 5V is Vov1, and Vov=Vov1–0.05.
Battery temperature	Vcc=4.05V, BAT=2.5V and SW1: B. Gradually raise bias1 from 0.90V. The bias1
detection voltage H	voltage when T6 switches from 0V to 5V is V <sub>TH</sub> .
	<u> </u>

Item	Measuring procedures
Battery temperature	Vcc=4.05V, BAT=2.5V and SW1: B. Gradually decrease bias1 from 0.40V. The
detection voltage L1	bias1 voltage when T6 switches from 0V to 5V is V <sub>TL1</sub> .
Battery temperature	Vcc=4.05V, BAT=2.5V and SW1: B. Gradually decrease bias1 from 0.30V. The
detection voltage L2	bias1 voltage when T6 switches from 5V to 0V is V <sub>TL2</sub> .
TDET pin input current	Vcc=4.5V, BAT=2.5V, bias1=1.2V, SW1: B and the current value A3 is ITDET.
LED R pin output voltage	Vcc=4.5V, BAT=2.5V, SW3: OFF, SW4: C, SW5: OFF, SW6: OFF and the T6
	voltage when 10mA flows on T6 is VLEDR.
LED G pin output voltage	Vcc=4.5V, BAT=2.5V, SW3: OFF, SW4: C, SW5: OFF, SW5: OFF and the T5
	voltage when 10mA flows on T5 is VLEDG.
CNT1 pin output voltage	Vcc=4.5V, BAT=3.5V, SW3: OFF, SW4: C, SW5: OFF, SW5: OFF and the T4
	voltage when 10mA flows on T4 is Vcnt1.
CNT2 pin output voltage	Vcc=4.5V, BAT=2.5V, SW3: OFF, SW4: C, SW5: OFF, SW5: OFF and the T3
	voltage when 5mA flows on T3 is VCNT2.
Oscillation cycle	The cycle of T2 signal when Vcc=4.5V and BAT=2.5V is Tosc.

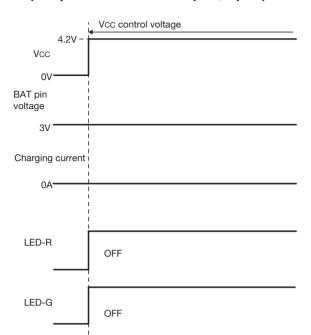
### **Timing Chart**

### MM1707BV

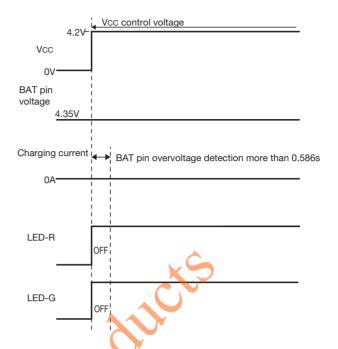
### ■ Case of normal charging ■ Case of connecting abnormal adapter



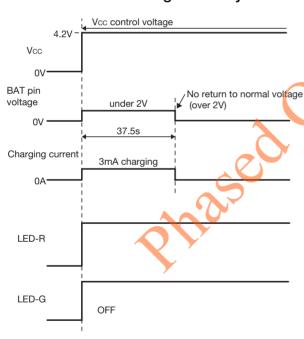
### Case of setting battery error (temparature detection pin; open)



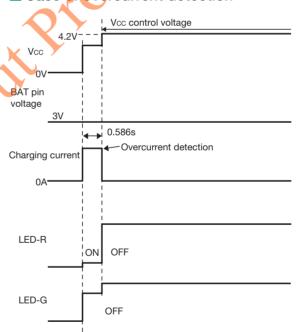
### Case of overcharged battery



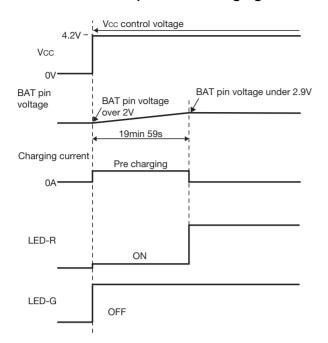
### Case of overdischarged battery



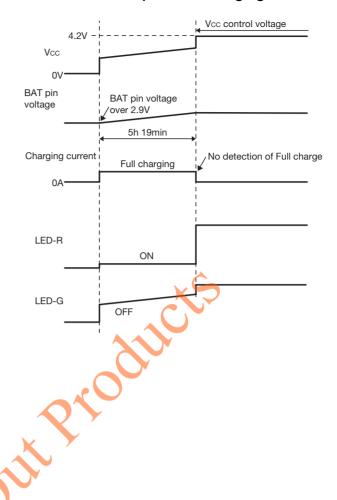
### Case of overcurrent detection



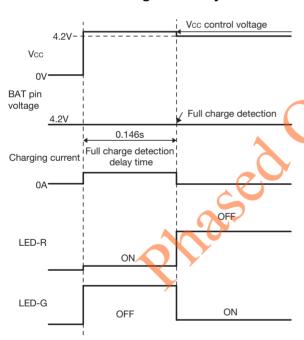
### Case of time-up for Pre charging



### Case of time-up for full charging

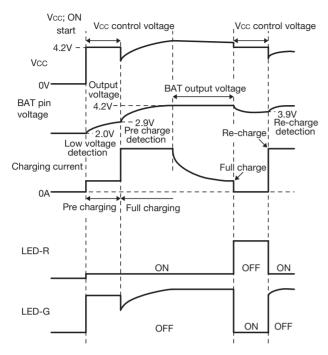


### Case of Full charged battery

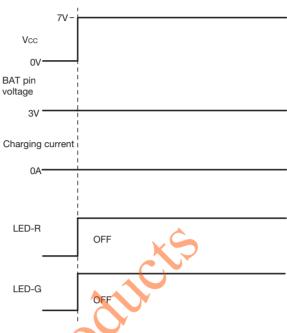


#### MM1707CV

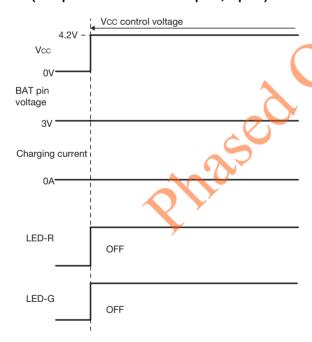
### Case of normal charging



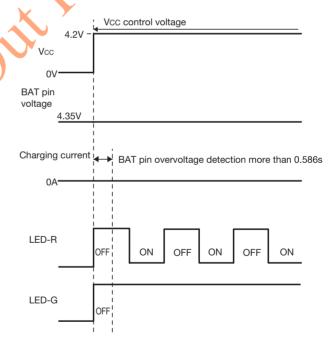
### Case of connecting abnormal adapter



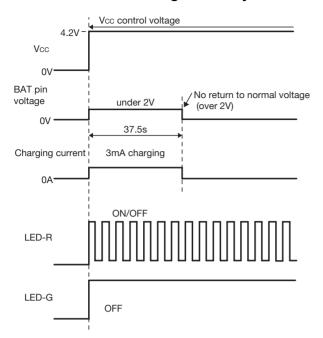
## ■ Case of setting battery error (temparature detection pin; open)



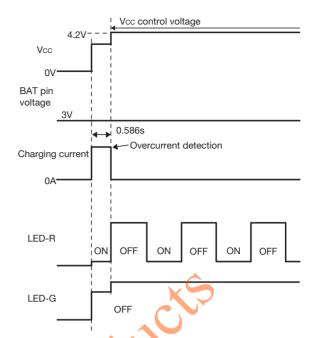
### ■ Case of overcharged battery



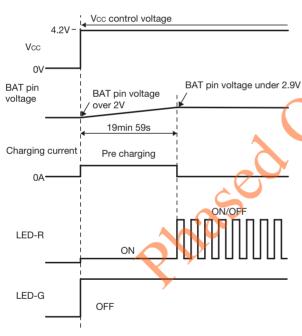
### Case of overdischarged battery



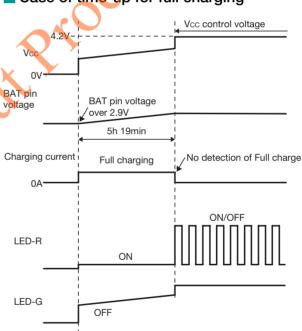
### Case of overcurrent detection



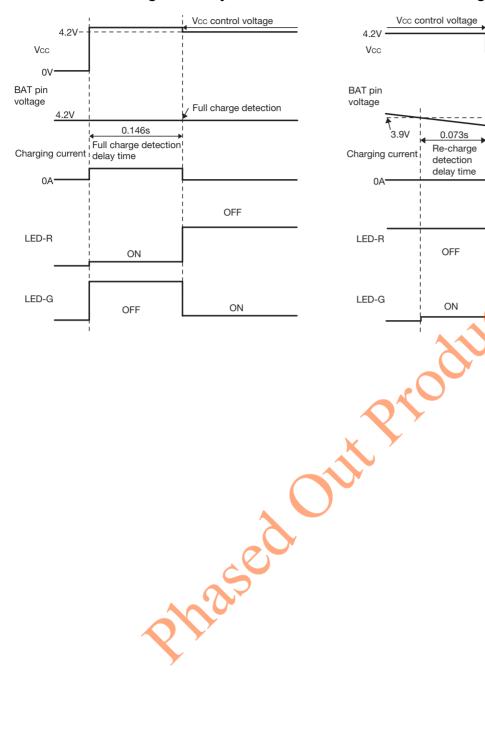
### Case of time-up for Pre charging



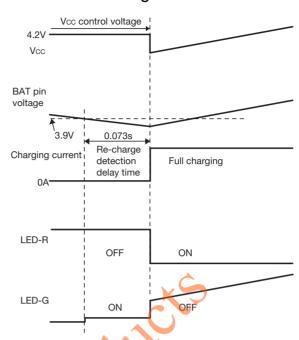
### Case of time-up for full charging



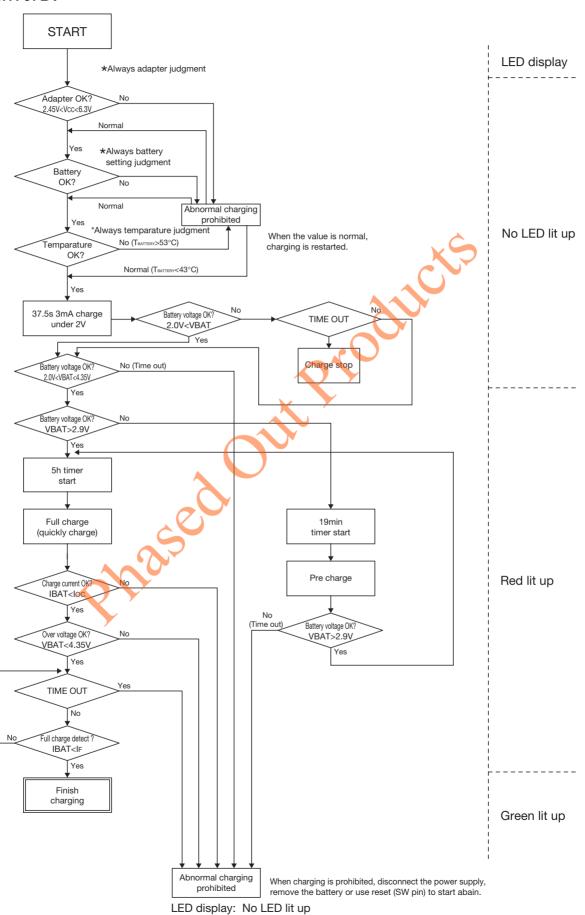
### Case of Full charged battery

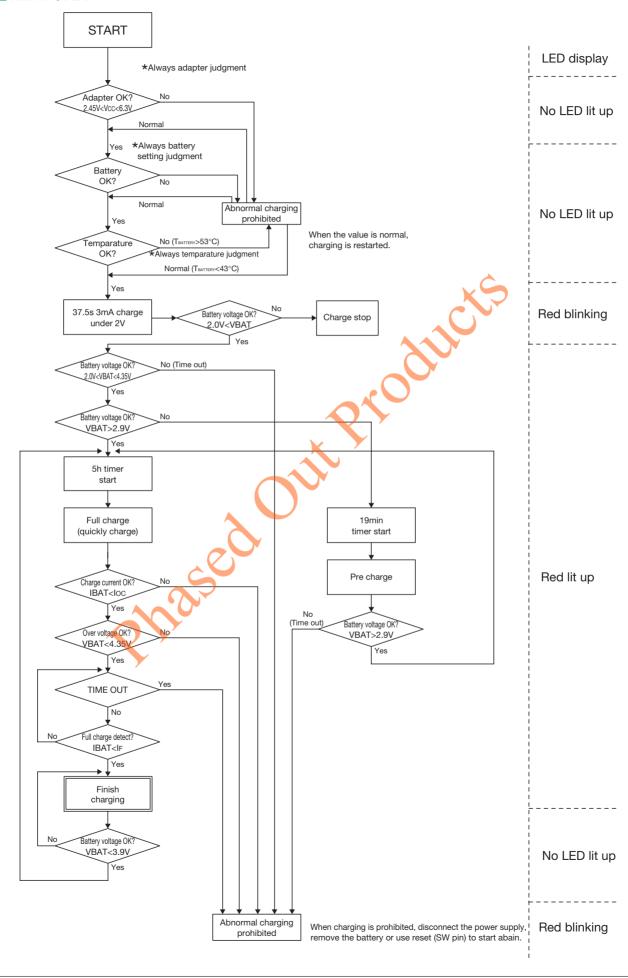


### Case of Re-charge detection

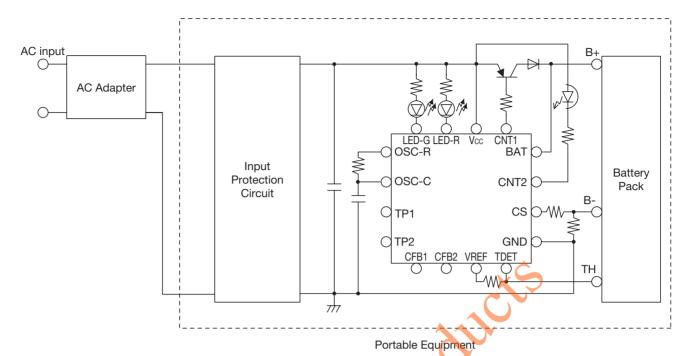


### Flow Chart





### **Application Circuit**



These circuits are typical examples provided for reference purposes, so in actual applications, the circuit constants, conditions and operations should be thoroughly studied.

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