



Linear charge control IC (2-channel built-in charge flag)

MM3758

Outline

This IC is a linear charge control IC for 1-cell lithium-ion and lithium polymer batteries. It has a built-in power MOSFET and reverse current protection circuit, eliminating the need for external sense resistors and reverse current protection diodes, thus reducing the number of components and heat generation. The IC also supports temperature profiles to ensure battery safety by limiting charge current and battery voltage at low and high battery temperatures. 2-channel charge flag is built in to enable detailed confirmation of charge status.

Application

Mobile devices, etc.

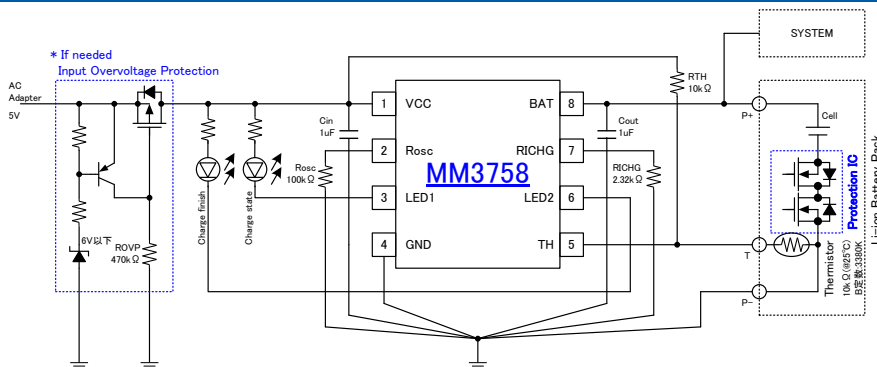
Feature

1. built-in power MOSFET
2. Built-in reverse current prevention circuit
3. compatible with JEITA recommended charging temperature profile
4. Built-in thermal regulation function
5. Built-in 2-channel charge status flag (Nch open drain)
6. Operating temperature range Ta=-40~85°C

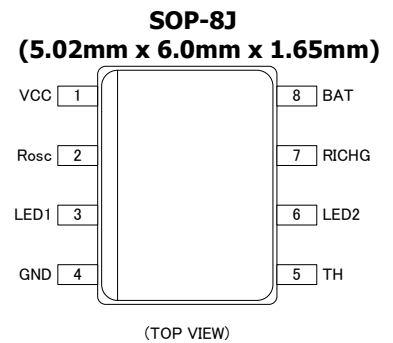
Major Specification

Parameter	Specification	Units
Rating voltage (VCC)	6	V
Operating voltage range (VCC)	4.3~5.8	V
Charge voltage	4.2	V
Fast charge current	~ 1500	mA
Pre-charge current	Fast charge current x0.1	mA
Fast charge starting voltage	2.9	V
Full charge current	Fast charge current x0.1	mA
BAT leakage current (max.)	100	nA
Thermal regulation temperature	122	°C
Charging timer	Pre:1, Fast:5	Hour

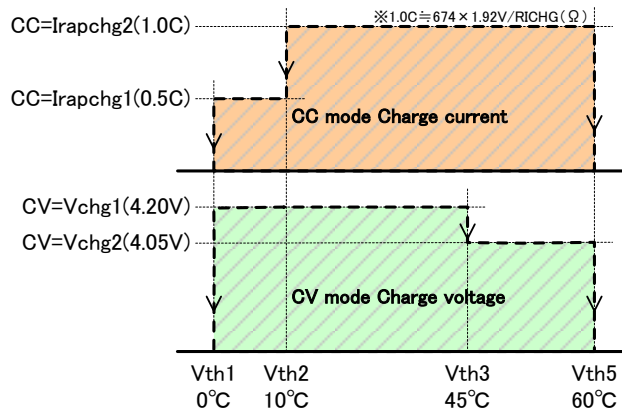
Application Circuit



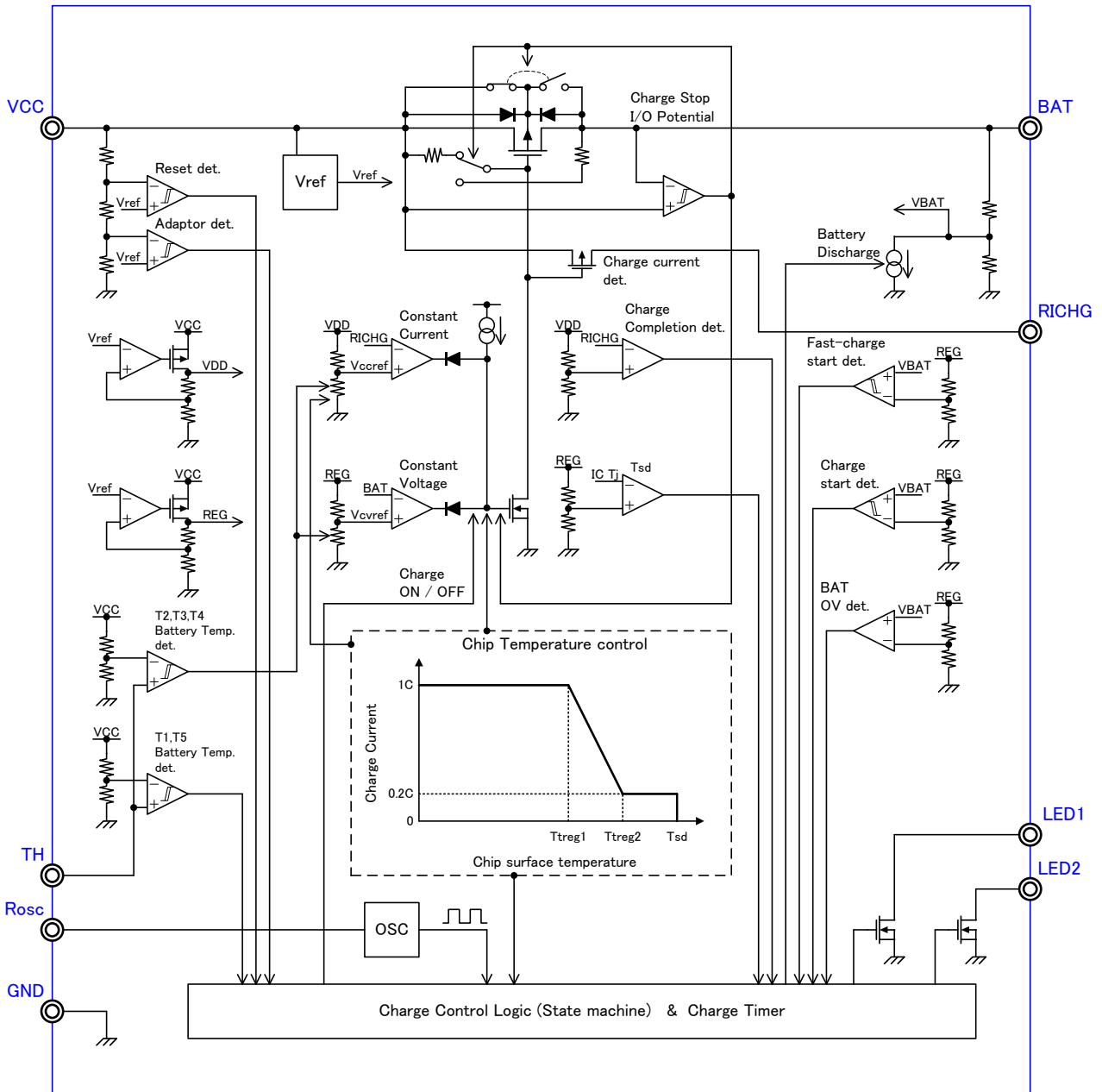
Package



Battery Temperature Profile

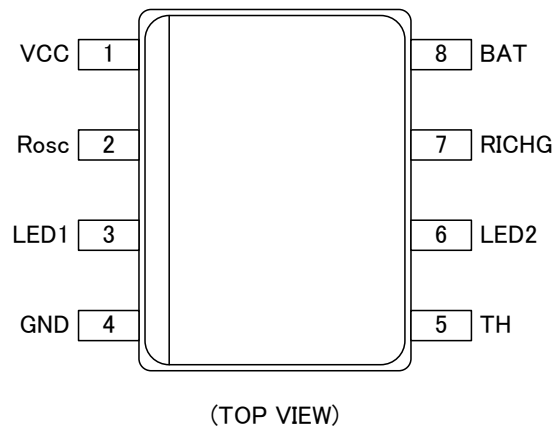


1. BLOCK DIAGRAM





2. PIN CONFIGURATION



3. TERMINAL EXPLANATIONS

PIN No.	PIN NAME	TERMINAL EXPLANATIONS
1	VCC	External power supply connection pin, Input pin of the charge control FET
2	Rosc	Oscillation frequency setting resistance connection pin $f_{osc} = 0.912V / (1.425E-10 * R_{osc})$ * Approximate. The fosc value for each Rosc value is specified on Page 14.
3	LED1	Charging status flag pin (Nch open drain output) During charging "L", Abnormal "open", Charging complete "open"
4	GND	Ground Pin
5	TH	Battery temperature detection input pin (connecting a thermistor)
6	LED2	Charge completion flag pin (Nch open drain output) During charging "open", Abnormal "open", Charging complete "L"
7	RICHG	Charge current setting resistance connection pin $I_{CHG} = 674 * 1.92V / R_{ICHG}$ * Approximate. The Charge current value for each RICHG value is specified on Page 14.
8	BAT	Charge control FET output pin (Connect to the positive side of a battery pack)



4. ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MIN.	MAX.	UNIT
Storage Temperature	Tstg	-55	150	°C
Operating Temperature	Topr	-40	85	°C
VCC, Rosc, LED1, TH, LED2, RICHG, BAT Pin Voltage	Vin	-0.3	6.0	V
BAT Pin Output Current	IBAT	-	1.5	A
LED1,2 Pin Sink Current	ILED	-	20	mA
Power Dissipation(*1)	Pd	-	1.424	W

*1 : When mounted on a 76.2×114.3×1.6tmm(epoxy glass ,double-sided, copper layer 60%) PC bord.

5. RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	MIN.	MAX.	UNIT
Operating Temperature	Topr	0	45	°C
VCC Operating Voltage(*2)	Vop	4.3	5.8	V
BAT pin Voltage	Vbop	0	4.35	V

*2 : Operation is possible from more than 4.0V, but in order to full charge up to 4.2V is required than 4.3V



6. ELECTRICAL CHARACTERISTICS

(Unless otherwise specified, Ta=0~45°C, VCC=5.0V)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Supply Current	I _{cc}	During fast charge (I _{rapchg} = 558mA setting)	-	3.0	4.5	mA
BAT Leak Current 1	I _{leak1}	BAT(=VSENSE)=4.2V, AC adaptor is unconnected	-	-	0.1	μA
BAT Leak Current 2	I _{leak2}	VCC=4.8V, charging is completed			2	μA
Reset Detection Voltage	V _{por}	Reset when VCC<V _{por}	2.3	2.5	2.7	V
ADP Detection Voltage	V _{adp}	Charging stops when VCC<V _{adp}	3.6	3.8	4.0	V
BAT Pin Discharge Current 1	I _{dischg1}	BAT=3.2V, in charge error mode	-	10	20	μA
BAT Pin Discharge Current 2	I _{dischg2}	BAT=3.2V	100	200	300	μA
Charge Start Detection Voltage	V _{start}	BAT=L→H	1.0	1.1	1.2	V
Charge Start Detection Voltage Hysteresis	V _{starthys}	BAT=H→L	50	100	150	mV
Fast-charge Start Detection Voltage	V _{qchg}	BAT=L→H	2.8	2.9	3.0	V
Fast-charge Start Detection Voltage Hysteresis	V _{qchghys}	BAT=H→L	40	80	120	mV
BAT Regulation Voltage	V _{chg1}	V _{th1} ≤ V _{TH} < V _{th3}	4.17	4.20	4.23	V
	V _{chg2}	V _{th3} ≤ V _{TH} < V _{th4}	4.00	4.05	4.10	V
Charge Stop I/O Potential Difference 1	V _{def1}	Charge stops when VCC – BAT < V _{def1} VCC=H→L	5	30	65	mV
Charge Stop I/O Potential Difference 2	V _{def2}	Charge stops when VCC – BAT < V _{def2} VCC=L→H	5	45	65	mV
Battery Overvoltage Detection Voltage	V _{ov}	VCC ≥ V _{ov} +100mV	4.27	4.35	4.43	V



(Unless otherwise specified, Ta=0~45°C, VCC=5.0V)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Forced Charge Current	Istart	RICHG=2.32kΩ, 0.3C (1.0C=Irapchg2)	116	167	219	mA
Trickle-charge Current	Iprechg	RICHG=2.32kΩ, 0.17C (1.0C=Irapchg2) BAT=2.6V	50	95	140	mA
Fast-charge Current	Irapchg1	RICHG=2.32kΩ, BAT=3.6V Vth1 ≤ VTH < Vth2	251	279	307	mA
	Irapchg2	RICHG=2.32kΩ, BAT=3.6V Vth2 ≤ VTH < Vth4	530	558	586	mA
Charge Completion Current	Ifc	RICHG=2.32kΩ, BAT > Vqchgon	50	95	140	mA
LED1,2 Output pin Low-Level Voltage	VledL	Iled=10mA	-	-	0.4	V
LED1,2 Output pin Leak Current	Iledleak	LED=5V	-1	-	1	μA
Series Pass Tr On Resistance	Ron	Io=200mA	-	0.38	0.6	Ω
Chip Temperature Detection1(*3)	Ttreg1	Applied to Tj (chip temperature)	112	122	132	°C
Chip Temperature Detection2(*3)	Ttreg2	Applied to Tj (chip temperature)	-	132	-	°C
Chip Temperature Detection Difference (*3)	Tdtreg	Applied to Tj (chip temperature) Ttreg2-Ttreg1	5	10	15	°C
Thermal Shutdown Temperature (*3)	Tsd	Applied to Tj (chip temperature)	143	153	163	°C

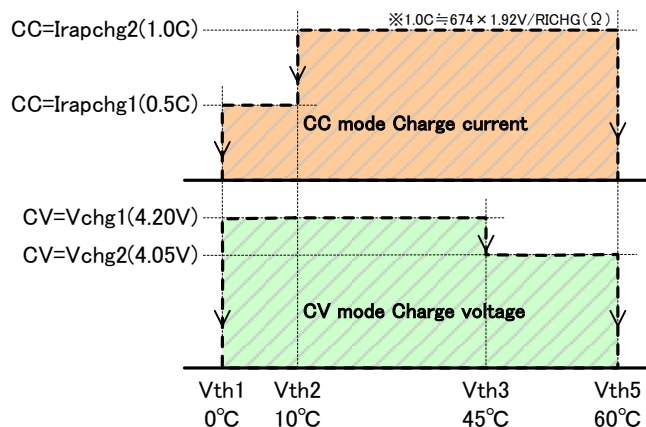
*3 : The parameter is guaranteed by design.



(Unless otherwise specified, Ta=0~45°C, VCC=5.0V)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Battery Connection Detection T0	Vth0	TH=L→H		VCC* 0.8096		
Cold Temp. Charge Stop Battery Temperature Detection Voltage T1	Vth1	TH=L→H, Charge stop threshold(0°C)	VCC* 0.7142	VCC* 0.7313	VCC* 0.7480	V
Cold Temp. Charge Recovery Battery Temperature Detection Voltage T1R(*4)	Vth1R	TH=H→L, Charge recovery threshold(3°C)	-	VCC* 0.7055	-	V
Battery Temperature Detection Voltage T2	Vth2	TH=L→H, CC=0.5C Charge threshold(10°C)	VCC* 0.6231	VCC* 0.6419	VCC* 0.6604	V
Battery Temperature Detection Voltage T2R	Vth2R	TH=H→L, CC=1C Charge threshold(13°C)	-	VCC* 0.6137	-	V
Battery Temperature Detection Voltage T3	Vth3	TH=H→L, CV=4.05V Charge threshold(45°C)	VCC* 0.3149	VCC* 0.3296	VCC* 0.3448	V
Battery Temperature Detection Voltage T3R	Vth3R	TH=L→H, CV=4.20V Charge threshold(41°C)	-	VCC* 0.3604	-	V
Hot Temp. Charge Stop Battery Temperature Detection Voltage T4	Vth4	TH=H→L, Charge stop threshold(60°C)	VCC* 0.2208	VCC* 0.2316	VCC* 0.2431	V
Hot Temp. Charge Recovery Battery Temperature Detection Voltage T4R(*4)	Vth4R	TH=L→H, Charge recovery threshold(55°C)	-	VCC* 0.2612	-	V

*4 : When the first charge and recharge, to become T1 = T1R = 0 degree, and T5 = T5R = 60 degree, please be careful about the noise and GND wiring.



*Thermistor : 10kohm@25°C, B-Constant : 3380K (Recommended : NCP15XH103F03RC)

MM3758AF JEITA Profile



(Unless otherwise specified, Ta=0~45°C, VCC=5.0V)

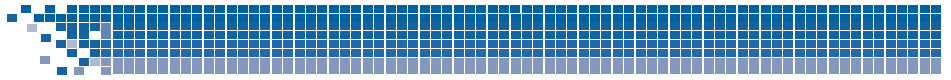
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Oscillator Frequency (*3)	fosc	Rosc=100kΩ	57.6	64	70.4	kHz
BAT Pin Discharge Time (*3)	Tdischg	Rosc=100kΩ	58	64	70	ms
AC Adaptor Connection Detection Time (*3,5)	Tadp	Applied when VCC≥Vadp detection	24	-	32	ms
	Tadp2	Applied when VCC≤Vpor detection	32	64	96	us
Forced Charge Time (*3)	Tistart	Rosc=100kΩ	480	512	544	ms
Forced Charge OFF Time (*3)	Toff	Rosc=100kΩ	115	128	141	ms
Battery Voltage Detection Time(*3,6)	Tcon	Rosc=100kΩ	96	-	128	ms
Fast Charge Start Voltage Detection Time(*3,6)	Tqstart	Rosc=100kΩ	96	-	128	ms
Charge Completion Current Detection Time(*3,7)	Tifc	Rosc=100kΩ	192	-	256	ms
Trickle-charge Timer (*3)	Tdchg	Rosc=100kΩ	54	60	66	min
Fast-charge Timer(*3)	Tchg	Rosc=100kΩ	270	300	330	min
Battery Overvoltage Detection Time(*3,6)	Tov	Rosc=100kΩ	96	-	128	ms
Charge Stop Battery Temperature Detection Time(*3,6)	Tpro	Rosc=100kΩ Vth1 or Vth4	96	-	128	ms
Charge Recovery Battery Temperature Detection Time(*3,6)	TproR	Rosc=100kΩ Vth1R or Vth4R	96	-	128	ms

*3 : The parameter is guaranteed by design.

*5 : The detection time varies depending on the timing of detection for approximately one clock(8ms) due to the mode transition system operated when matched 4 times in 8ms.

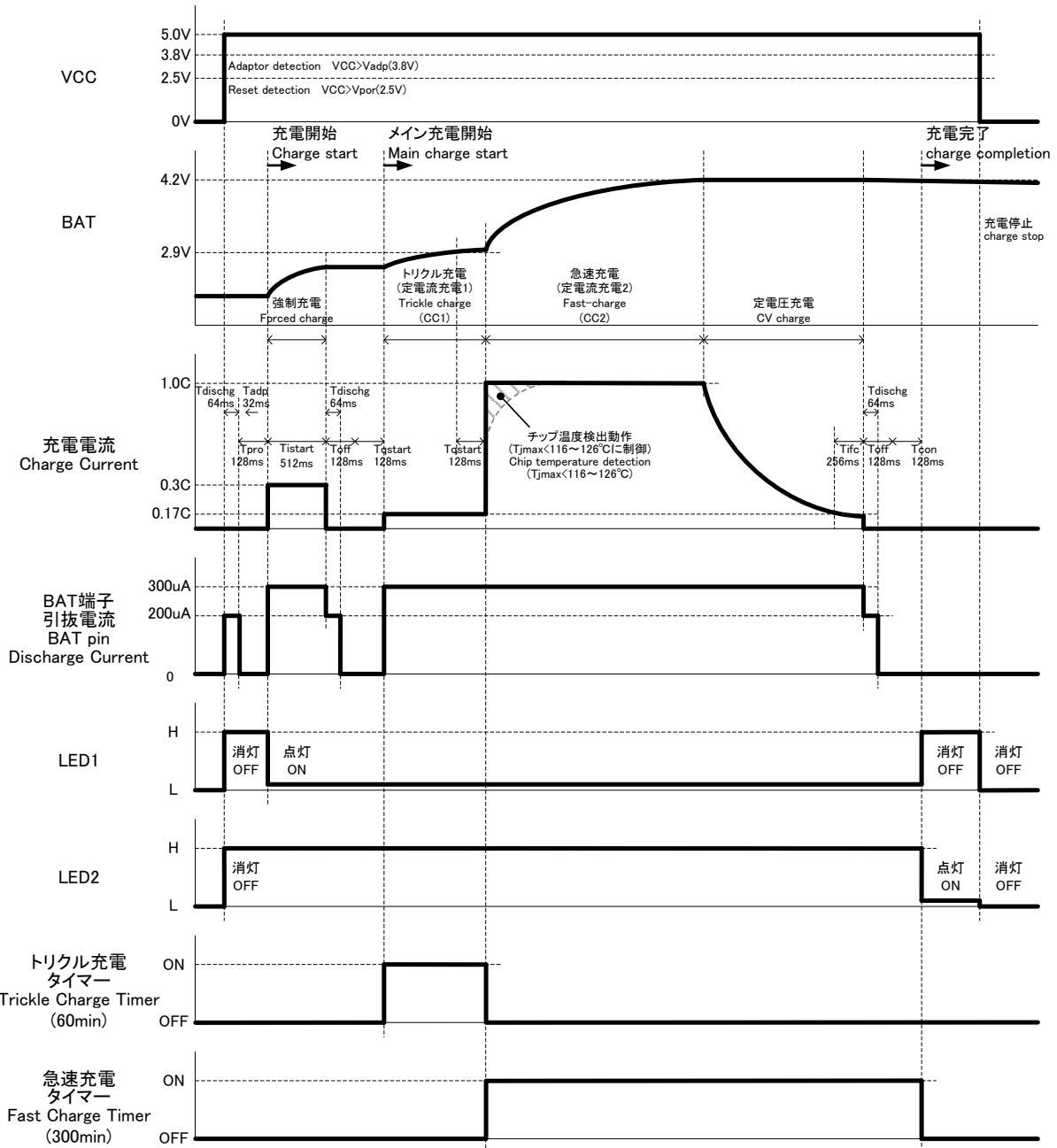
*6 : The detection time varies depending on the timing of detection for approximately one clock(32ms) due to the mode transition system operated when matched 4 times in 32ms.

*7 : The detection time varies depending on the timing of detection for approximately one clock(64ms) due to the mode transition system operated when matched 4 times in 64ms.

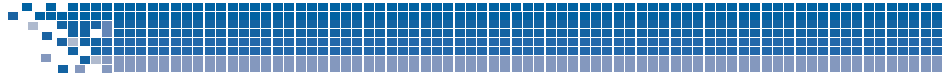


7. TIMING CHART

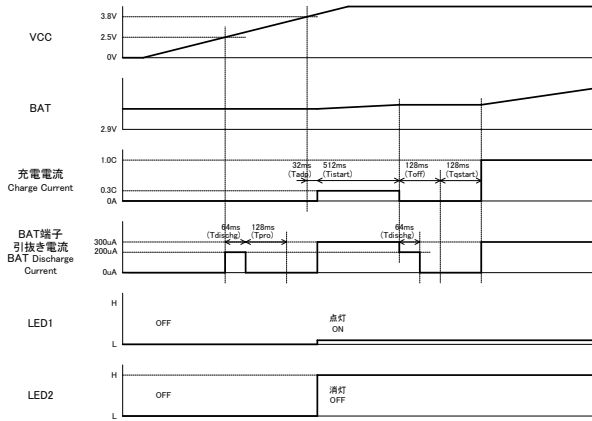
- Charging Timing chart (Normal charge)



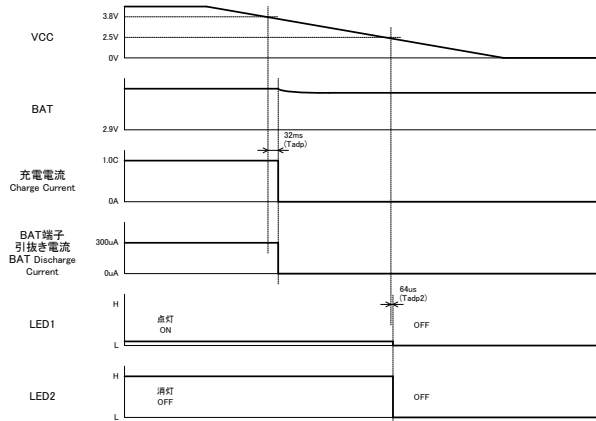
*8 : All typ numeric value.



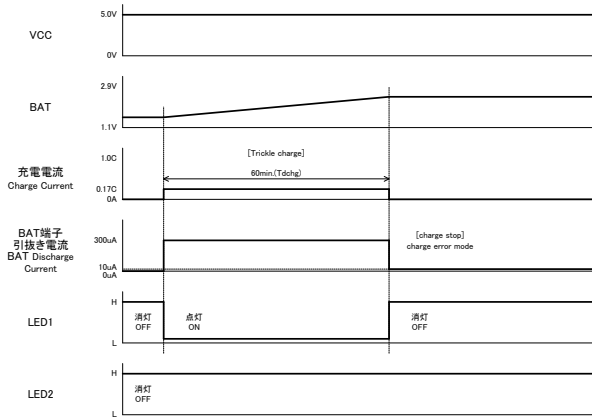
● Input Adaptor



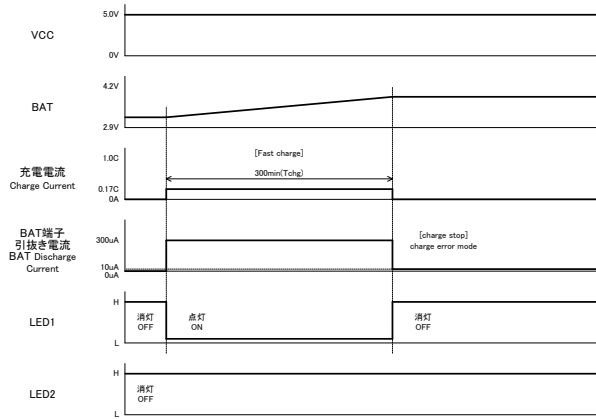
● Release Adaptor



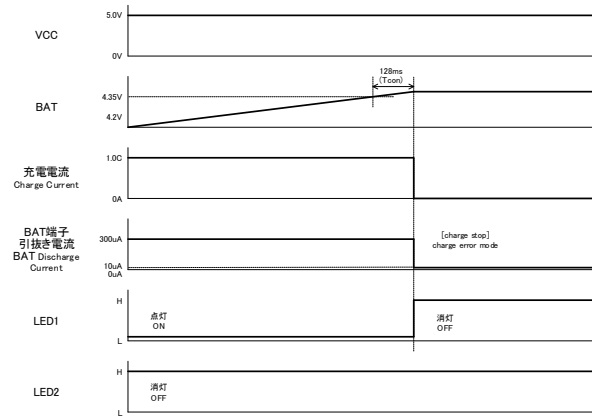
● Trickle charge time-up



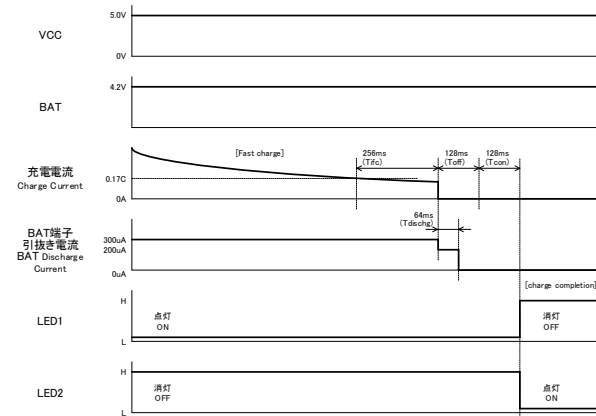
● Fast charge time-up



● Battery overvoltage detection (BAT > 4.35V)



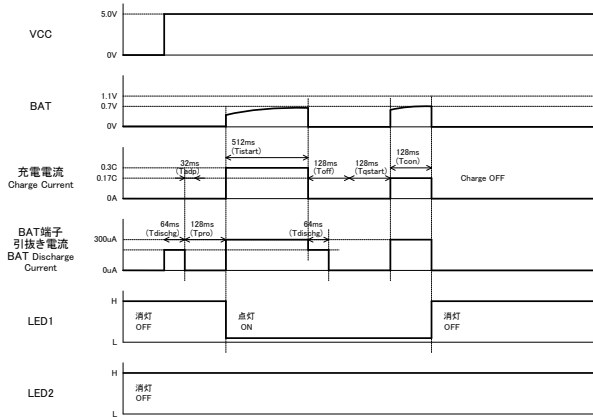
● Full charge detection



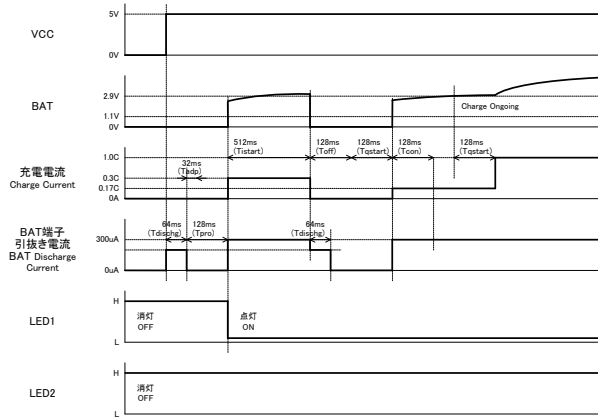
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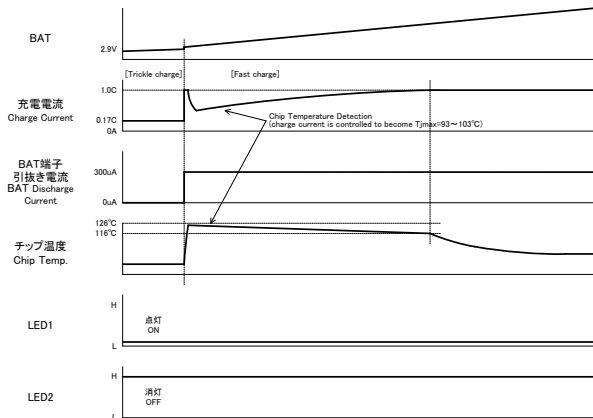
● Deep discharge battery charging start



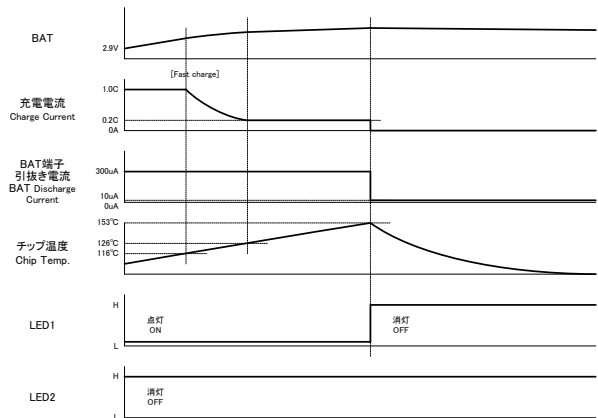
● Over-discharge protection pack charging start



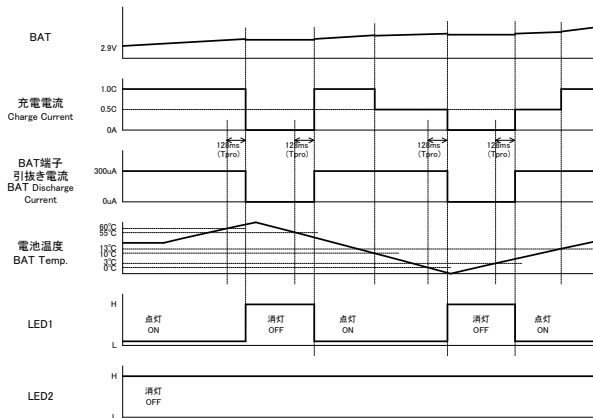
● Chip temperature detection



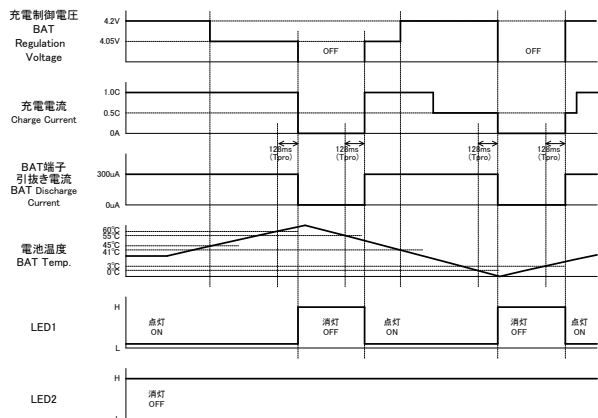
● Thermal shutdown



● Battery temperature detection (Constant Current Mode)



● Battery temperature detection (Constant Voltage Mode)

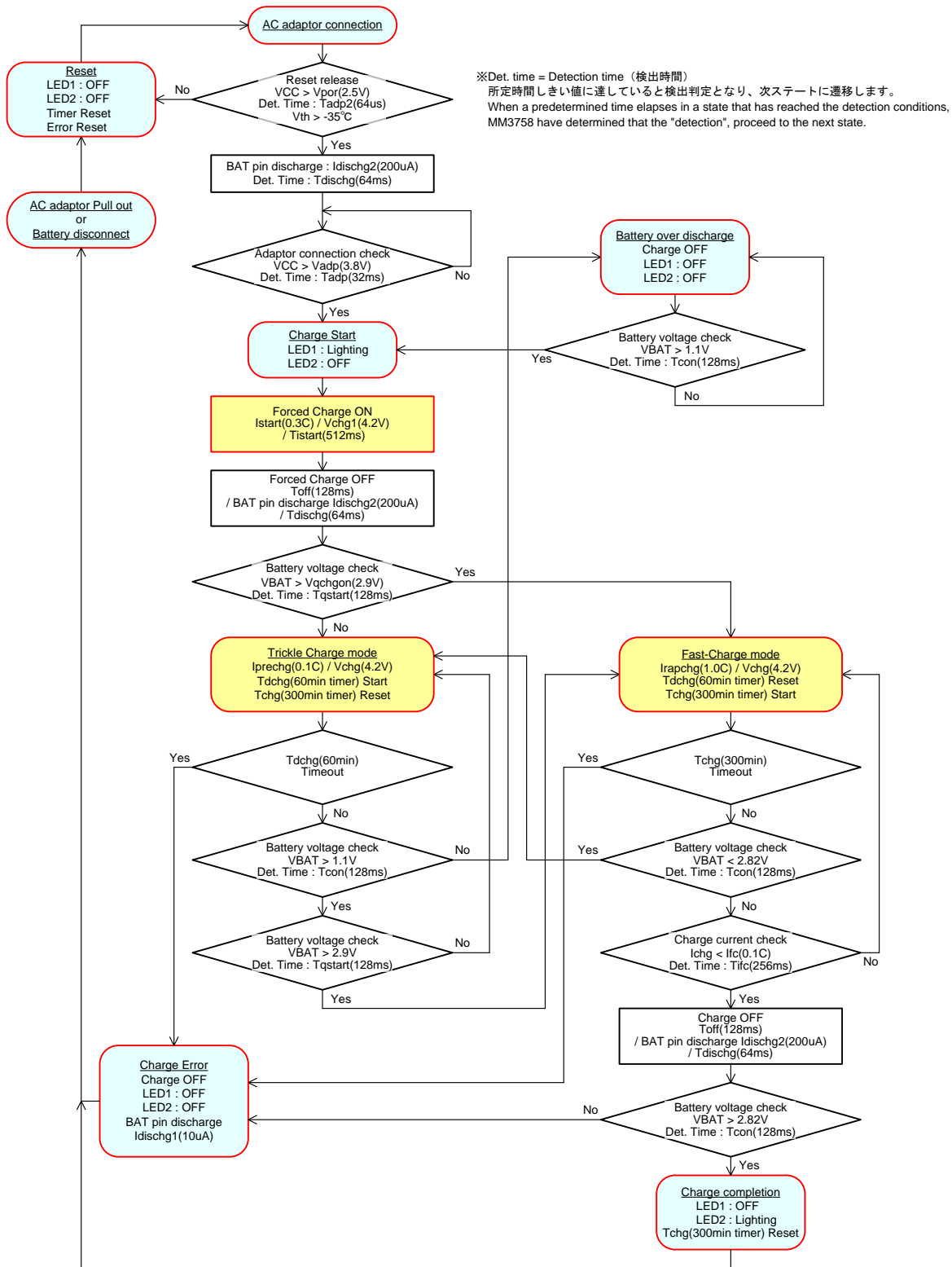


*8 : All typ numeric value.



8. FLOW CHART

• Main flow chart

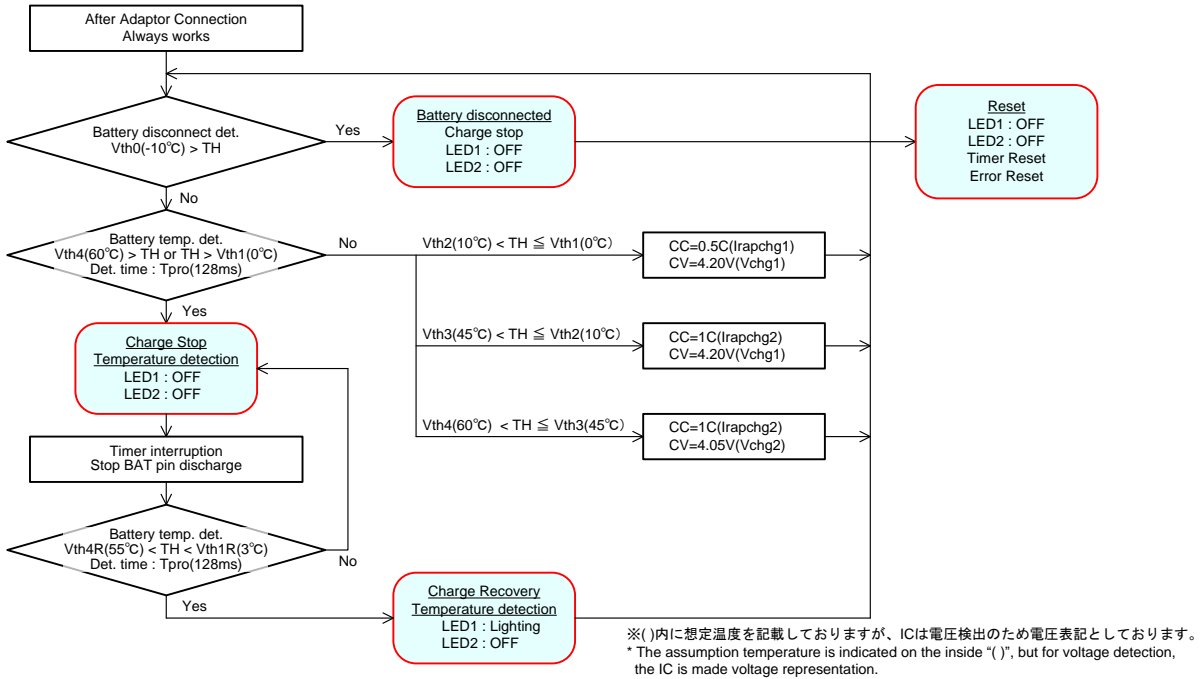


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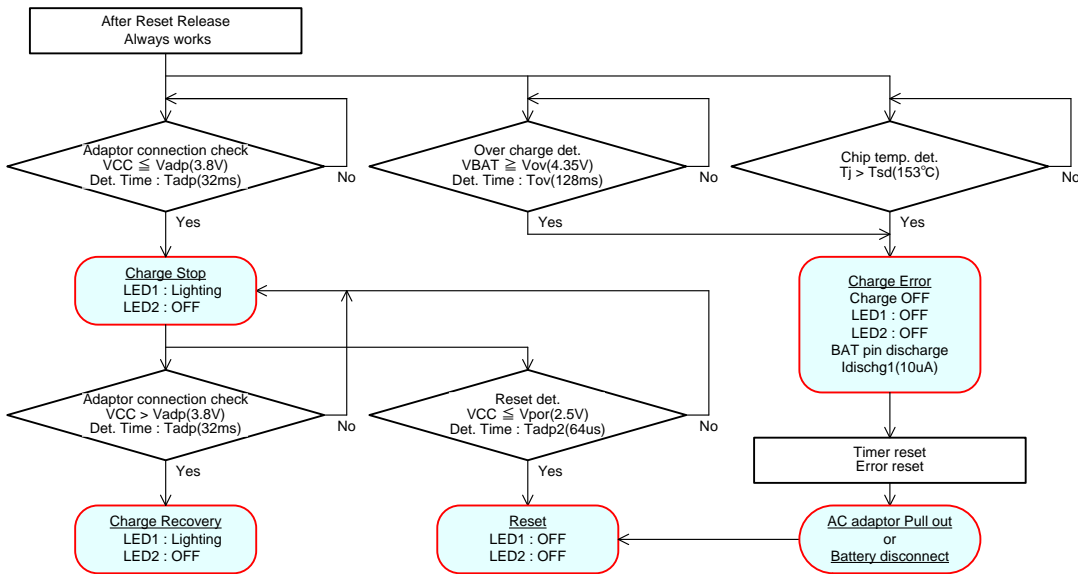
*9 : Charge current, charge completion current and each detection time for RICHG=2.32kΩ, Rosc=100kΩ.



• Battery temperature profile



• Protection function (Always detected)



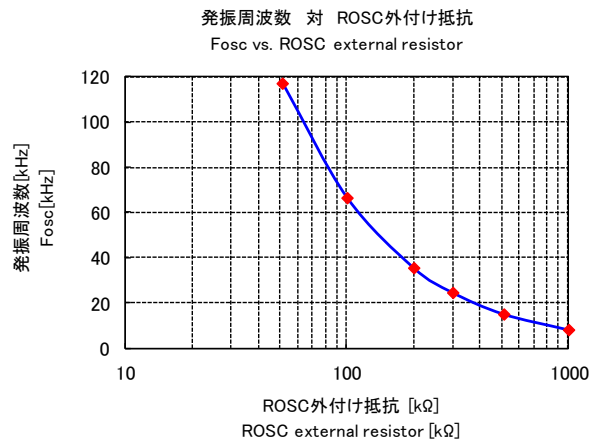
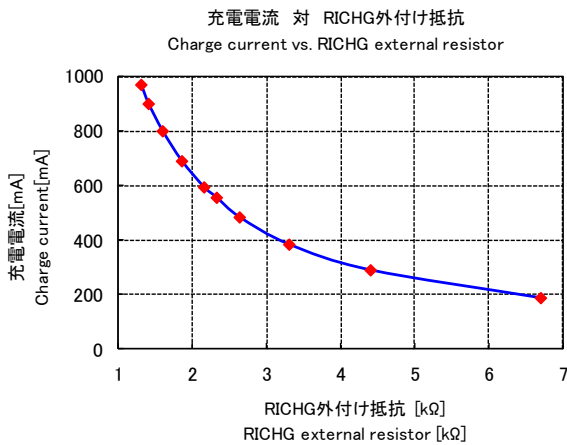
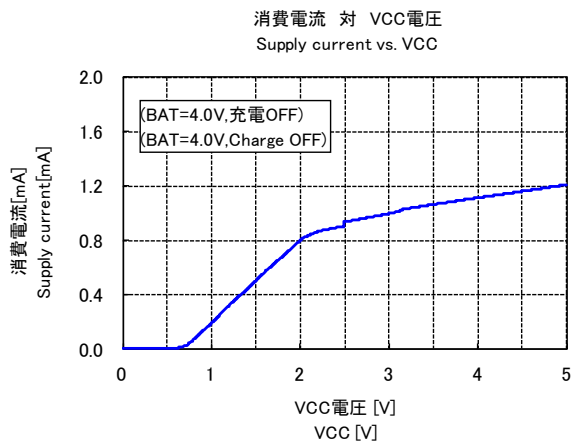
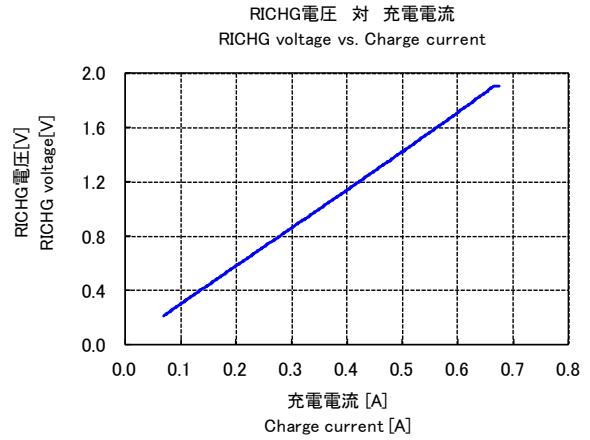
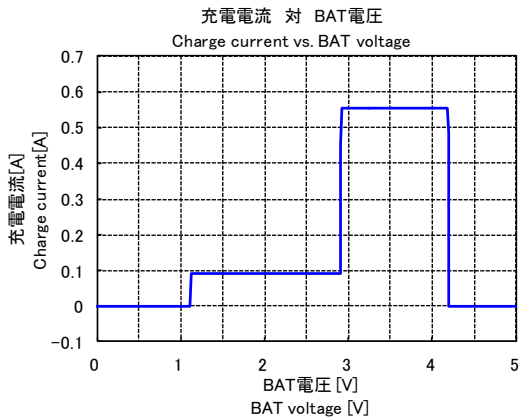
*8 : All typ. numeric value.

*9 : Charge current, charge completion current and each detection time for $R_{ICHG}=2.32\text{k}\Omega$, $R_{osc}=100\text{k}\Omega$.

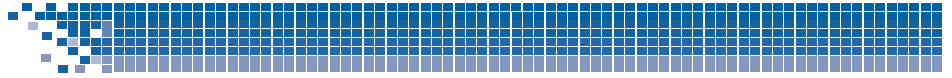


9. TYPICAL PERFORMANCE CHARACTERISTICS

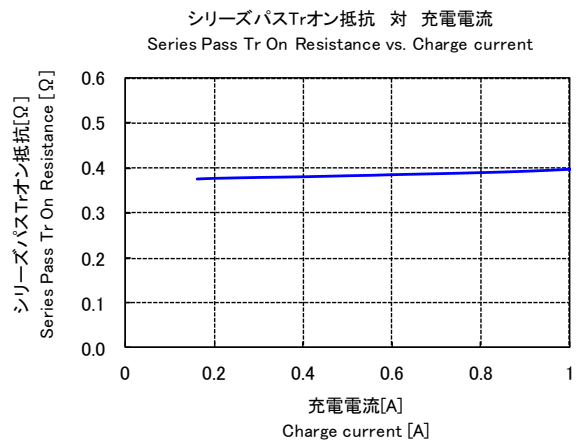
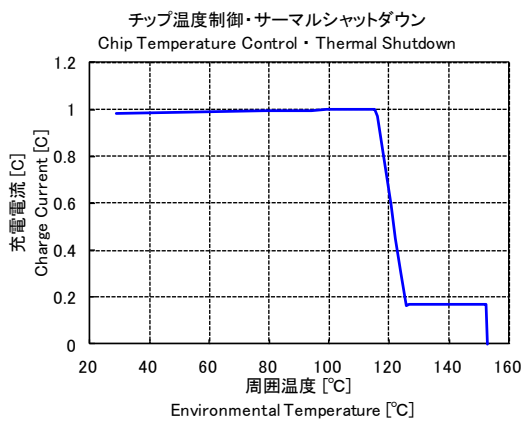
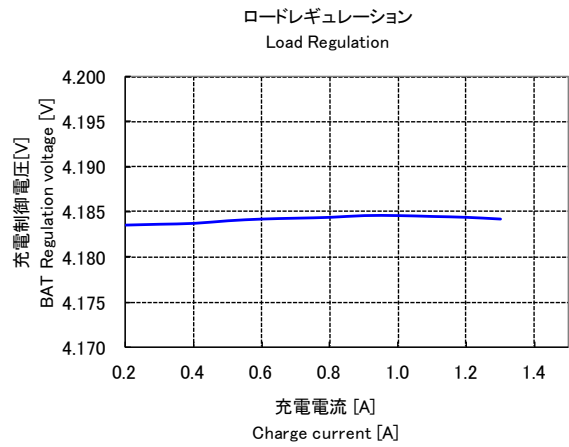
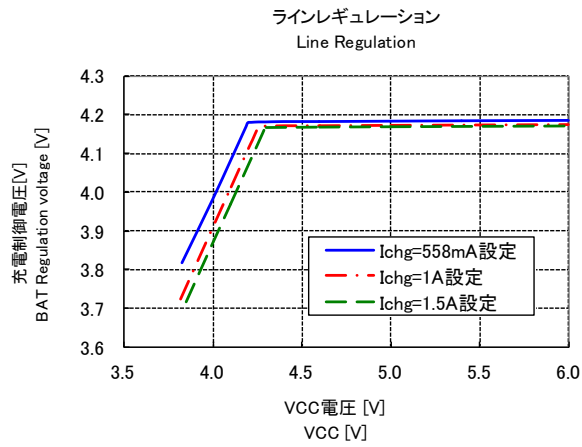
(Unless otherwise specified, VCC=5.0V, RICHG=2.32kΩ, Rosc=100kΩ, Ta=25°C)



*10 : These are typical characteristics.



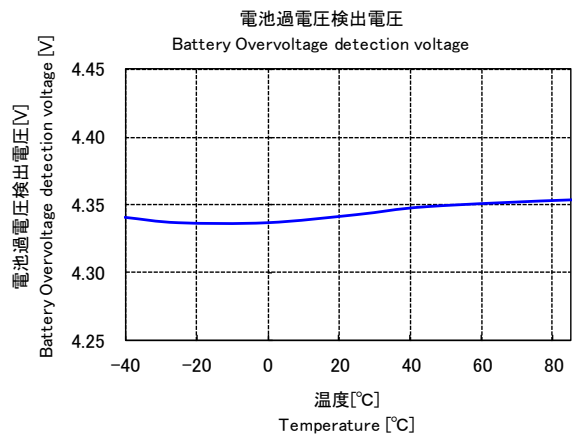
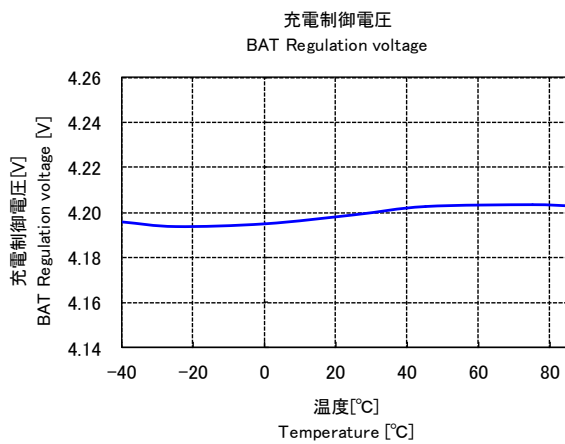
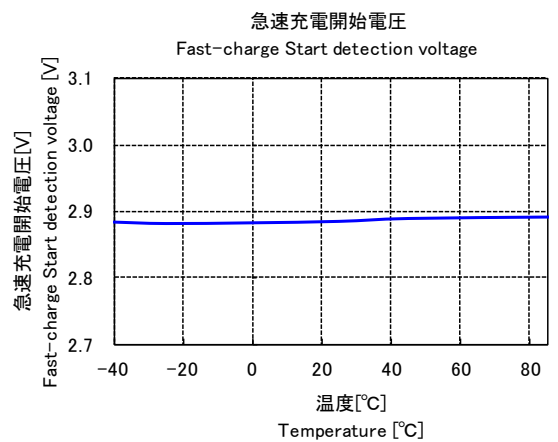
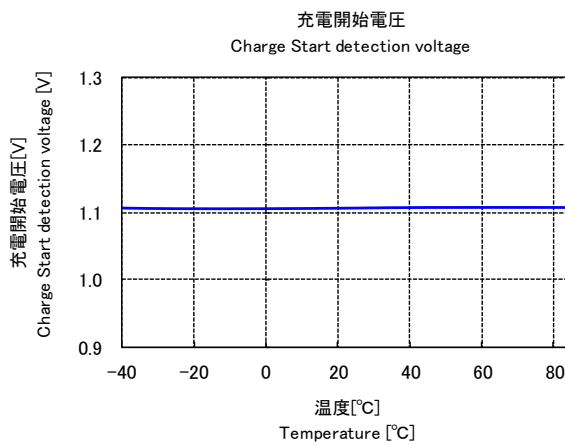
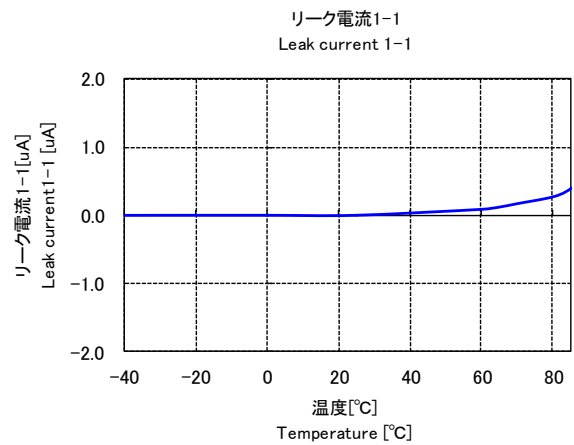
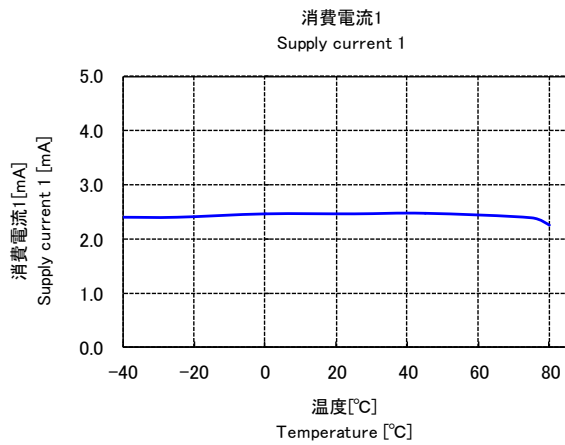
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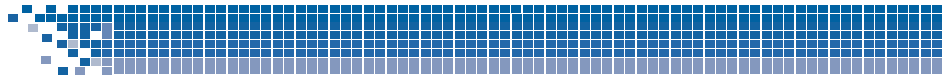
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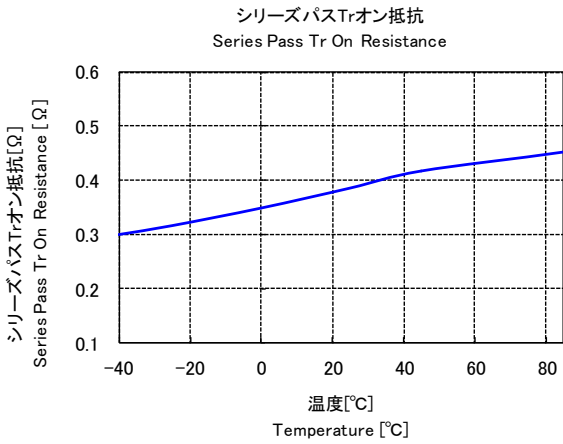
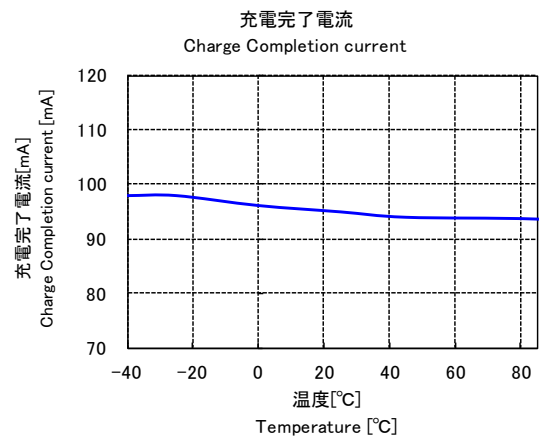
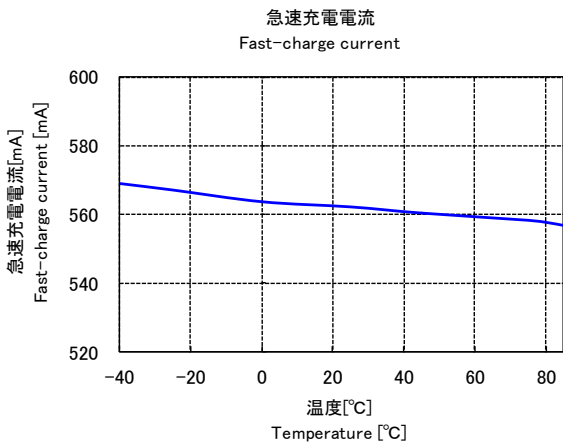
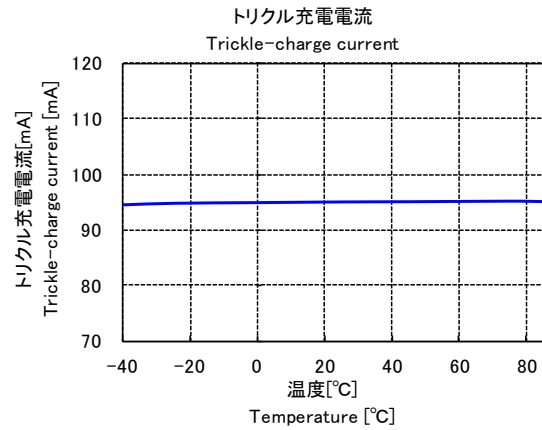
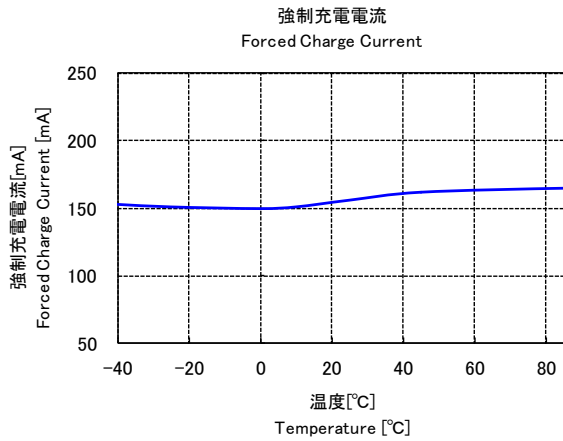
● TEMPERATURE DEPENDENCY



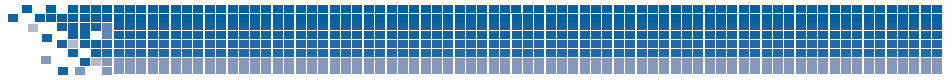
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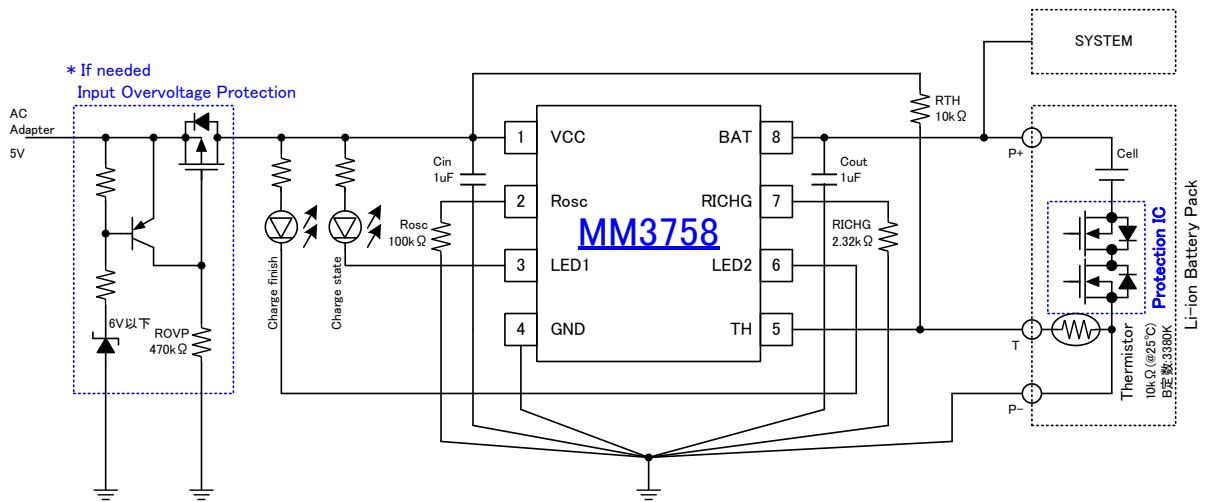
● TEMPERATURE DEPENDENCY



*10 : These are typical characteristics.



10. TYPICAL APPLICATION CIRCUIT



- Mitsumi Electric Co., Ltd. shall not be liable for any trouble or damage caused by using this circuit.
- In the event a problem which may affect industrial property or any other rights of us or a third party is encountered during the use of information described in these circuit, Mitsumi Electric Co., Ltd. shall not be liable for any such problem, nor grant.