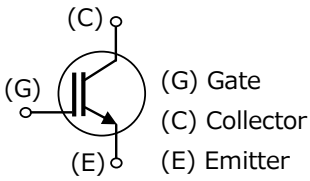
MITSUMI

PRELIMINARY

Power Semiconductor IGBT (Insulated Gate Bipolar Transistor) MR-Series 650V / 100A

MMJ65A0A00**



Outline

IGBT (Bare chip) utilizes various technologies that we cultivated by analog semiconductor device production and is the product which prepared a lineup of the wide high voltage, high current which can contribute to high efficiency and saving energy.

Applications

・Industrial Motor Drivers
・Inverter
・Welding
・UPS

Features

① Field Stop Trench gate IGBT
② Low Collector-Emitter saturation voltage
③ High short circuit capability
④ Low swiching losses

Absolute Maximum Ratings

Tj=25deg unless otherwise noted.

Parameter	Symbol	Rating	Unit
Collector-Emitter voltage	VCES	650	V
Gate-Emitter voltage	VGES	±30	V
Collector current *1)	IC	100	A
Junction temperature	Tj	-40～+175	℃

*1)Collector current is limited by Tj(max) and thermal properties of assembly.

Die Specification

Item	Value	Unit
Die thickness	90	μm
Die size	7.8x7.8(60.8)	mm
Front metal(AlSi)	6.5	μm
Backside metal(AlSi/Ti/Ni/Au)	1.25	μm

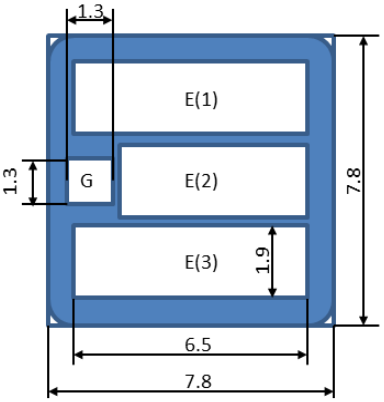
Electrical Characteristics

Tj=25deg unless otherwise noted.

Parameter		Symbol	Specification Min	Typ	Max	Unit	condition
Zero gate voltage collector current		ICES	-	-	1	μA	Vce=650V,Vge=0V
Gate-Emitter leakage current		IGES	-	-	±500	nA	Vge=±30V,Vce=0V
Gate-emitter threshold voltage		VGE(th)	5.00	-	6.80	V	Vce=10V,Ic=2.0mA
Collecter-Emitter saturation voltage	Tj=25℃	VCE (sat)	-	1.5	1.8	V	Ic=100A,Vge=15V
	Tj=175℃		-	1.7	-		
Input capacitance		Cies	-	8000	-	pF	VCE=25V,VGE=0V, f=1MHz
Reverse transfer capcitance		Cres	-	300	-	pF	
Switching time *Reference characteristics		td(on)	-	50	-	ns	Vcc=300V,Ic=100A VGE=-15/+15V, Rg=10.0Ω, Inductive load, Ls≒100nH
		tr	-	50	-	ns	
		td(off)	-	300	-	ns	
		tf	-	60	-	ns	
Short circuit withstand time		Tsc	10	-	-	μs	Vcc=360V,Vge=15V,Tj=150℃

This characteristic is when it is incorporated in a mold package or evaluation board.
Depending on the assembly conditions etc., it may not be satisfied. Please note that it is not a guaranteed value.

Die Dimension



Mitsumi Electric CO.,LTD.
Semiconductor Business Division
Strategy Engineering Department
tel:+81-46-230-3470

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