

MM3866

2023/07/20

Overview

is a Capacitor-less/Ultralow quiescent current 200mA LDO.

The IC can be stable behavior without Input/Output capacitor.

Therefore the number of external capacitor is reduced.

The IC can be better low quiescent current and load transient by bias boost circuit.

The package is a small PLP-4C (1mm x 1mm), ideal for mobile devices.

Application

- Portable communication device
- Photographing / Imaging device
- Wearable device
- Health care device

Features

Cap less, Low current consumption

Main specifications

Output current [mA]	200
Absolute maximum rating [V]	7.0
Recommended operating voltage	1.70

MM3866

2023/07/20

Min. [V]	1.70
Recommended operating voltage Max. [V]	6.0
Output voltage Min. [V]	1.20
Output voltage Max. [V]	5.00
Output voltage accuracy [%]	±1.0
No-Load Input Current [μA]	0.9
Dropout Voltage [V]	0.35
PSRR [dB]	50
Circuit structure	1ch LDO
Operating ambient temperature Min. [deg.C]	-40
Operating ambient temperature Max. [deg.C]	85
OFF input current Typ. [μA]	0.10
Protection function	OCP
Additional function	ON/OFF control, Auto discharge

Package

PLP-4C

Case Studies



No amplifier or software design required. Development of an LDO for automobiles with open load/short circuit detection function. [Power Supply IC]