



Soft start function 1A LDO

# MM3702/MM3703 Series

## Overview

This IC is a 1A LDO with soft start. The soft start function can optionally set output rise time by an external capacitor Cs. The package can be selected from SOT89-5 (Standard), HSOP-8E(High heat radiation), SSON-6A (Small leadless) for application.

## Features

- Soft start
- Over current protection
- Thermal shutdown

## Main specifications

( $V_{DD}=V_{OUT}(Typ.)+1V$ ,  $V_{CE}=V_{DD}$ ,  $T_a=25^{\circ}C$  unless otherwise specified)

- Maximum rating supply voltage : -0.3V to 7.0V
- Operating voltage range : 1.7V to 6.5V
- Operating ambient temperature : -40°C to 85°C
- Output current : 1A
- Input current (OFF) : Typ. 0.1uA
- No-load input current : Typ. 60uA
- Output voltage range : 1V to 5V (0.1V step)
- Output voltage accuracy :  $\pm 1\%$  ( $1.5V \leq V_{OUT}(Typ.)$ )  
 $\pm 15mV$  ( $V_{OUT}(Typ.) < 1.5V$ )
- Line regulation : Typ. 0.05%/V ( $2.0V \leq V_{OUT}(Typ.)$ ,  $V_{DD}=V_{OUT}(Typ.)+0.5V$  to 6.5V)  
Typ. 0.05%/V ( $V_{OUT}(Typ.) < 2.0V$ ,  $V_{DD}=2.5V$  to 6.5V)
- Load regulation : Typ. 50mV ( $I_{OUT}=1mA$  to 1A)
- Dropout voltage : Typ. 0.46V ( $I_{OUT}=1A$ ,  $V_{OUT}(Typ.)=3V$ )
- PSRR : Typ. 70dB ( $V_{OUT}(Typ.) < 1.3V$ ,  $f=1kHz$ )  
Typ. 65dB ( $1.3V \leq V_{OUT}(Typ.) < 3.4V$ ,  $f=1kHz$ )  
Typ. 60dB ( $3.4V \leq V_{OUT}(Typ.)$ ,  $f=1kHz$ )
- Output capacitor : 1.0uF (Ceramic capacitor)
- Output Rise Time : Typ. 1.5ms ( $C_s=0.01uF$ ,  $V_{OUT}>1.5V$ )
- Protection function : Over current protection, Thermal shutdown
- Additional function : ON/OFF control, Auto discharge, Soft start

## Packages

MM3702

- SOT89-5A

MM3703

- SOT89-5A
- HSOP-8E
- SSON-6A

## Application

- Audio visual equipment
- Photographing / Imaging device
- Office equipment / Printer
- Home appliance equipment
- In-vehicle infotainment device



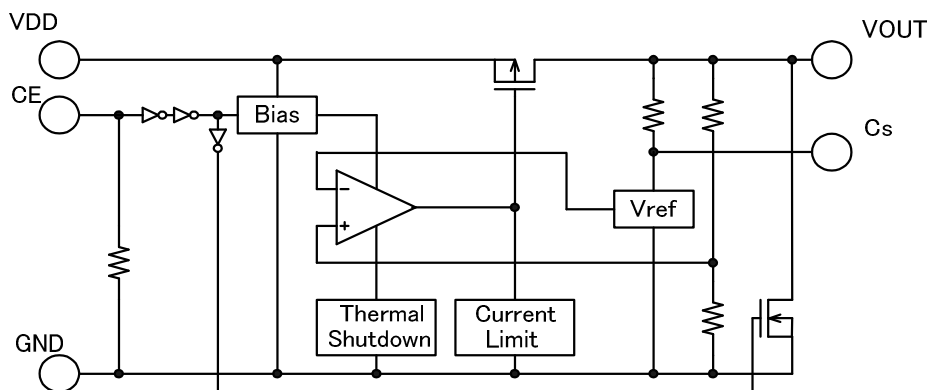


## Model Name

M	M	3	7	0	2	X	X	X	X	X	X
M	M	3	7	0	3	X	X	X	X	X	X
Series name						(A)	(B)	(C)	(D)	(E)	

(A)	Function Type	A	CE=H active, with discharge function (SOT89-5A, SSON-6A)
		F	CE=H active, with discharge function (HSOP-8E)
(B)	Output voltage rank	10	The output voltage can be designated in the range from 1.0V(10) to 5.0V(50) in 0.1V steps.
		?	
		50	
(C)	Package	P	SOT89-5A
		H	HSOP-8E (MM3703 only)
		R	SSON-6A (MM3703 only)
(D)	Packing specifications 1	R	R housing (SOT89-5A, SSON-6A standard)
		B	B housing (HSOP-8E standard)
(E)	Packing specifications 2	E	Embos tape / Halogen free (HSOP-8E, SSON-6A)
		H	Embos tape / Halogen free (SOT89-5A)

## Block Diagram

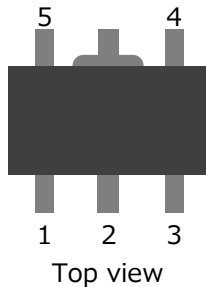




## Pin Configuration

### ◆ MM3702 series

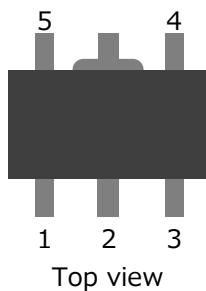
- SOT89-5A



Pin No.	Pin name	Function
1	CE	ON/OFF-control pin Connect CE pin with VDD pin, when it is not used.
2	GND	GND pin
3	Cs	Soft-start pin Must be connect capacitor to Soft-start pin.
4	V <sub>DD</sub>	Voltage supply pin
5	V <sub>OUT</sub>	Output pin

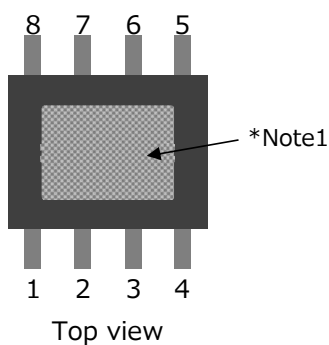
### ◆ MM3703 series

- SOT89-5A



Pin No.	Pin name	Function
1	Cs	Soft-start pin Must be connect capacitor to Soft-start pin.
2	GND	GND pin
3	CE	ON/OFF-control pin Connect CE pin with VDD pin, when it is not used.
4	V <sub>DD</sub>	Voltage supply pin
5	V <sub>OUT</sub>	Output pin

- HSOP-8E



Pin No.	Pin name	Function
1	V <sub>OUT</sub>	Voltage supply pin
2	NC	No connection
3	Cs	Soft-start pin Must be connect capacitor to Soft-start pin.
4	GND	GND pin
5	CE	ON/OFF-control pin Connect CE pin with VDD pin, when it is not used.
6	NC	No connection
7	NC	No connection
8	V <sub>DD</sub>	Voltage supply pin

\*Note1: Heat spreader bottom with GND.

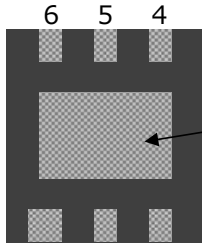




## Pin Configuration

◆ MM3703 series

■ SSON-6A



1 2 3  
Top view

Pin No.	Pin name	Function
1	V <sub>OUT</sub>	Voltage supply pin
2	C <sub>s</sub>	Soft-start pin Must be connect capacitor to Soft-start pin.
3	GND	GND pin
4	CE	ON/OFF-control pin Connect CE pin with VDD pin, when it is not used.
5	NC	No connection
6	V <sub>DD</sub>	Voltage supply pin

\*Note1:Heat spreader bottom with GND.





## Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit	
Storage temperature	Tstg	-55	150	°C	
Junction temperature	TjMAX	-	150	°C	
Supply voltage	V <sub>DD</sub>	-0.3	7.0	V	
CE input voltage	V <sub>CE</sub>	-0.3	7.0	V	
Output voltage	V <sub>OUT</sub>	-0.3	V <sub>DD</sub> +0.3V	V	
Cs Voltage	V <sub>CS</sub>	-0.3	V <sub>DD</sub> +0.3V	V	
Output current	I <sub>omax</sub>	0	1.2	A	
Power dissipation *Note2	SOT89-5A	Pd	-	1780	mW
	HSOP-8E		-	3500	mW
	SSON-6A		-	1250	mW

\*Note2:JEDEC51-7 standard

\*Note3:In consideration of product life, please examine the use in less than 80%.

## Recommended Operating Conditions

Item	Symbol	Min.	Max.	Unit
Operating junction temperature	Tjopr	-40	125	°C
Operating ambient temperature	Topr	-40	85	°C
Operating voltage	Vop	1.6	6.5	V
Output current	I <sub>OUT</sub>	0	1	A

## Electrical Characteristics

(V<sub>DD</sub>=V<sub>OUT</sub>(Typ.)+1V, V<sub>CE</sub>=V<sub>DD</sub>, Ta=25°C unless otherwise specified)

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Input current(OFF)	I <sub>DDOFF</sub>	V <sub>CE</sub> =0V	-	0.1	1.0	μA
No-Load Input Current	I <sub>DD</sub>	I <sub>OUT</sub> =0mA	-	60	80	μA
Output voltage	V <sub>OUT</sub>	I <sub>OUT</sub> =10mA, 1.5V≤V <sub>OUT</sub>	×0.99	-	×1.01	V
		I <sub>OUT</sub> =10mA, V <sub>OUT</sub> <1.5V	-0.015	-	0.015	
Line regulation	V <sub>LINE</sub>	V <sub>OUT</sub> (Typ.)+0.5V≤V <sub>DD</sub> ≤6.5V I <sub>OUT</sub> =10mA, 2.0V≤V <sub>OUT</sub>	-	0.05	0.20	%V
		2.5V≤V <sub>DD</sub> ≤6.5V I <sub>OUT</sub> =10mA, V <sub>OUT</sub> <2.0V				
Load regulation	V <sub>LOAD</sub>	1mA≤I <sub>OUT</sub> ≤1000mA	-	50	130	mV
Dropout voltage	V <sub>IO</sub>	Please refer to another page.	-	-	-	V
Ripple rejection *Note4	RR	f=1kHz, V <sub>ripple</sub> =0.5V, I <sub>OUT</sub> =10mA V <sub>OUT</sub> <1.3V	-	70	-	dB
		f=1kHz, V <sub>ripple</sub> =0.5V, I <sub>OUT</sub> =10mA 1.3V≤V <sub>OUT</sub> <3.4V	-	65	-	
		f=1kHz, V <sub>ripple</sub> =0.5V, I <sub>OUT</sub> =10mA 3.4V≤V <sub>OUT</sub> <5.0V	-	60	-	

\*Note4:The parameter is guaranteed by design.





## Electrical Characteristics

( $V_{DD}=V_{OUT(Typ.)}+1V$ ,  $V_{CE}=V_{DD}$ ,  $T_a=25^{\circ}C$  unless otherwise specified)

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Vout temperature coefficient *Note4	$\Delta V_{OUT} / \Delta T_{OP}$	$I_{OUT}=10mA$ $-40 \leq T_{OP} \leq 85^{\circ}C$	-	±100	-	ppm/°C
Output short-circuit current *Note4	$I_{short}$	$V_{OUT}=0V$	-	150	-	mA
Thermal shutdown detect temperature *Note4	$T_{SD}$		-	150	-	°C
Thermal shutdown release temperature *Note4	$T_{SR}$		-	115	-	°C
Output Rise Time *Note4	tr	$V_{OUT} \leq 1.5V$ , $C_s=0.01\mu F$	-	2.0	-	ms
		$1.5V < V_{OUT}$ , $C_s=0.01\mu F$	-	1.5	-	
CE High threshold voltage	$V_{CEH}$		1.2	-	6.5	V
CE Low threshold voltage	$V_{CEL}$		0	-	0.3	V
CE pin current	$I_{CE}$	$V_{CE}=2.0V$	-	0.1	-	μA
Output NMOS ON resistance *Note4	$R_{DON}$	$V_{CE}=0V$ , $V_{DD}=4V$	-	15	-	Ω

\*Note4: The parameter is guaranteed by design.





### Electrical Characteristics

( $V_{DD}=V_{OUT}(Typ.)+1V$ ,  $V_{CE}=V_{DD}$ ,  $T_a=25^{\circ}C$  unless otherwise specified)

Model name	Item								
	Output voltage				Dropout voltage				
	$V_{OUT}$ (V)				$V_{IO}$ (V)				
	Conditions	Min.	Typ.	Max.	Conditions	Min.	Typ.	Max.	
MM3702A10	$I_{OUT}=10mA$	0.985	1.000	1.015	$I_{OUT}=300mA$ , $V_{OUT}<2.0V$ *Note5	-	0.40	0.60	
MM3702A11		1.085	1.100	1.115		-	0.30	0.40	
MM3702A12		1.185	1.200	1.215		-	0.21	0.27	
MM3702A13		1.285	1.300	1.315		-	0.21	0.27	
MM3702A14		1.385	1.400	1.415		-	0.21	0.27	
MM3702A15		1.485	1.500	1.515		-	0.21	0.27	
MM3702A16		1.584	1.600	1.616		-	0.21	0.27	
MM3702A17		1.683	1.700	1.717		-	0.21	0.27	
MM3702A18		1.782	1.800	1.818		-	0.21	0.27	
MM3702A19		1.881	1.900	1.919		-	0.21	0.27	
MM3702A20		1.980	2.000	2.020		$I_{OUT}=300mA$ , $2.0V \leq V_{OUT}$ , $V_{DD}=V_{OUT}(Typ.)-0.2V$	-	0.21	0.27
MM3702A21		2.079	2.100	2.121			-	0.21	0.27
MM3702A22		2.178	2.200	2.222			-	0.21	0.27
MM3702A23		2.277	2.300	2.323			-	0.21	0.27
MM3702A24		2.376	2.400	2.424			-	0.21	0.27
MM3702A25		2.475	2.500	2.525			-	0.21	0.27
MM3702A26		2.574	2.600	2.626			-	0.21	0.27
MM3702A27		2.673	2.700	2.727			-	0.21	0.27
MM3702A28		2.772	2.800	2.828			-	0.21	0.27
MM3702A29	2.871	2.900	2.929	-	0.21		0.27		
MM3702A30	2.970	3.000	3.030	-	0.21		0.27		
MM3702A31	3.069	3.100	3.131	-	0.21		0.27		
MM3702A32	3.168	3.200	3.232	-	0.21		0.27		
MM3702A33	3.267	3.300	3.333	-	0.21		0.27		
MM3702A34	3.366	3.400	3.434	-	0.21		0.27		
MM3702A35	3.465	3.500	3.535	-	0.21		0.27		
MM3702A36	3.564	3.600	3.636	-	0.21		0.27		
MM3702A37	3.663	3.700	3.737	-	0.21		0.27		
MM3702A38	3.762	3.800	3.838	-	0.21		0.27		
MM3702A39	3.861	3.900	3.939	-	0.15	0.21			
MM3702A40	3.960	4.000	4.040	-	0.15	0.21			
MM3702A41	4.059	4.100	4.141	-	0.15	0.21			
MM3702A42	4.158	4.200	4.242	-	0.15	0.21			
MM3702A43	4.257	4.300	4.343	-	0.15	0.21			
MM3702A44	4.356	4.400	4.444	-	0.15	0.21			
MM3702A45	4.455	4.500	4.545	-	0.15	0.21			
MM3702A46	4.554	4.600	4.646	-	0.15	0.21			
MM3702A47	4.653	4.700	4.747	-	0.15	0.21			
MM3702A48	4.752	4.800	4.848	-	0.15	0.21			
MM3702A49	4.851	4.900	4.949	-	0.15	0.21			
MM3702A50	4.950	5.000	5.050	-	0.15	0.21			

\*Note5:Dropout voltage maximum value in the input and it is confirmed that there is no output abnormal voltage impression the 300mA in the model less than  $V_{OUT}<2.0V$ .





### Electrical Characteristics

( $V_{DD}=V_{OUT}(Typ.)+1V$ ,  $V_{CE}=V_{DD}$ ,  $T_a=25^{\circ}C$  unless otherwise specified)

Model name	Item								
	Output voltage				Dropout voltage				
	$V_{OUT}$ (V)				$V_{io}$ (V)				
	Conditions	Min.	Typ.	Max.	Conditions	Min.	Typ.	Max.	
MM3702A10	$I_{OUT}=10mA$	0.985	1.000	1.015	$I_{OUT}=1A,$ $V_{OUT}<2.0V$ *Note6	-	1.00	1.30	
MM3702A11									
MM3702A12									
MM3702A13									
MM3702A14									
MM3702A15									
MM3702A16									
MM3702A17									
MM3702A18									
MM3702A19									
MM3702A20		$I_{OUT}=1A,$ $2.0V \leq V_{OUT},$ $V_{DD}=V_{OUT}(Typ.)-0.2V$	1.980	2.000		2.020	-	0.70	0.90
MM3702A21									
MM3702A22									
MM3702A23									
MM3702A24									
MM3702A25									
MM3702A26									
MM3702A27									
MM3702A28									
MM3702A29									
MM3702A30									
MM3702A31									
MM3702A32									
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MM3702A38									
MM3702A39									
MM3702A40									
MM3702A41									
MM3702A42									
MM3702A43									
MM3702A44									
MM3702A45									
MM3702A46									
MM3702A47									
MM3702A48									
MM3702A49									
MM3702A50									

\*Note6:Dropout voltage maximum value in the input and it is confirmed that there is no output abnormal voltage impression the 1A in the model less than  $V_{OUT}<2.0V$ .







### Electrical Characteristics

( $V_{DD}=V_{OUT}(Typ.)+1V$ ,  $V_{CE}=V_{DD}$ ,  $T_a=25^{\circ}C$  unless otherwise specified)

Model name	Item							
	Output voltage				Dropout voltage			
	$V_{OUT}$ (V)				$V_{io}$ (V)			
	Conditions	Min.	Typ.	Max.	Conditions	Min.	Typ.	Max.
MM3703A/F10	$I_{OUT}=10mA$	0.985	1.000	1.015	$I_{OUT}=300mA$ , $V_{OUT}<2.0V$ *Note5	-	0.40	0.60
MM3703A/F11		1.085	1.100	1.115				
MM3703A/F12		1.185	1.200	1.215				
MM3703A/F13		1.285	1.300	1.315				
MM3703A/F14		1.385	1.400	1.415				
MM3703A/F15		1.485	1.500	1.515				
MM3703A/F16		1.584	1.600	1.616				
MM3703A/F17		1.683	1.700	1.717				
MM3703A/F18		1.782	1.800	1.818				
MM3703A/F19		1.881	1.900	1.919				
MM3703A/F20		1.980	2.000	2.020				
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MM3703A/F25		2.475	2.500	2.525				
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MM3703A/F36	3.564	3.600	3.636					
MM3703A/F37	3.663	3.700	3.737					
MM3703A/F38	3.762	3.800	3.838					
MM3703A/F39	3.861	3.900	3.939					
MM3703A/F40	3.960	4.000	4.040					
MM3703A/F41	4.059	4.100	4.141					
MM3703A/F42	4.158	4.200	4.242					
MM3703A/F43	4.257	4.300	4.343					
MM3703A/F44	4.356	4.400	4.444					
MM3703A/F45	4.455	4.500	4.545					
MM3703A/F46	4.554	4.600	4.646					
MM3703A/F47	4.653	4.700	4.747					
MM3703A/F48	4.752	4.800	4.848					
MM3703A/F49	4.851	4.900	4.949					
MM3703A/F50	4.950	5.000	5.050					
					$I_{OUT}=300mA$ , $2.0V \leq V_{OUT}$ , $V_{DD}=V_{OUT}(Typ.)-0.2V$	-	0.21	0.27
						-	0.15	0.21

\*Note5: Dropout voltage maximum value in the input and it is confirmed that there is no output abnormal voltage impression the 300mA in the model less than  $V_{OUT}<2.0V$ .





### Electrical Characteristics

( $V_{DD}=V_{OUT}(Typ.)+1V$ ,  $V_{CE}=V_{DD}$ ,  $T_a=25^{\circ}C$  unless otherwise specified)

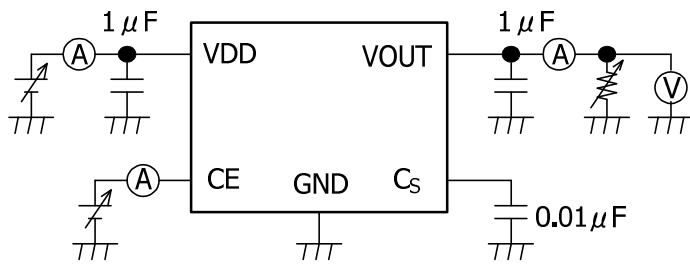
Model name	Item								
	Output voltage				Dropout voltage				
	$V_{OUT}$ (V)				$V_{io}$ (V)				
	Conditions	Min.	Typ.	Max.	Conditions	Min.	Typ.	Max.	
MM3703A/F10	$I_{OUT}=10mA$	0.985	1.000	1.015	$I_{OUT}=1A,$ $V_{OUT}<2.0V$ *Note6	-	1.00	1.30	
MM3703A/F11									
MM3703A/F12									
MM3703A/F13									
MM3703A/F14									
MM3703A/F15									
MM3703A/F16									
MM3703A/F17									
MM3703A/F18									
MM3703A/F19									
MM3703A/F20		$I_{OUT}=1A,$ $2.0V \leq V_{OUT},$ $V_{DD}=V_{OUT}(Typ.)-0.2V$	1.980	2.000		2.020	-	0.70	0.90
MM3703A/F21									
MM3703A/F22									
MM3703A/F23									
MM3703A/F24									
MM3703A/F25									
MM3703A/F26									
MM3703A/F27									
MM3703A/F28									
MM3703A/F29									
MM3703A/F30									
MM3703A/F31									
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MM3703A/F40									
MM3703A/F41									
MM3703A/F42									
MM3703A/F43									
MM3703A/F44									
MM3703A/F45									
MM3703A/F46									
MM3703A/F47									
MM3703A/F48									
MM3703A/F49									
MM3703A/F50									

\*Note6:Dropout voltage maximum value in the input and it is confirmed that there is no output abnormal voltage impression the 1A in the model less than  $V_{OUT}<2.0V$ .

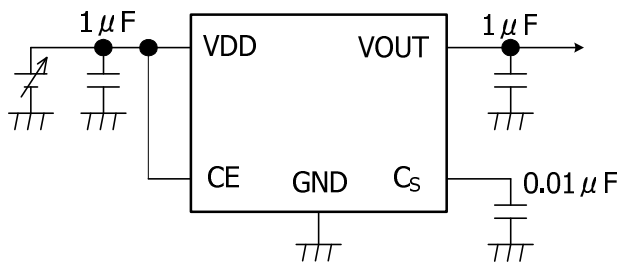




## Test Circuit



## Application Circuit



(Example of external parts)

- Output capacitor                      Ceramic capacitor 1.0μF
- Input capacitor                        Ceramic capacitor 1.0μF
- Soft start capacitor                  Ceramic capacitor 0.01μF                      \*Temperature characteristics : B

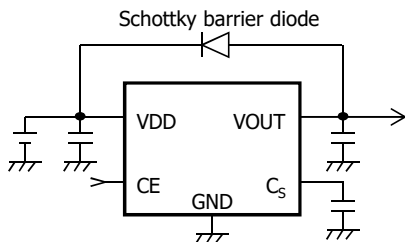
- In the event a problem which may affect industrial property or any other rights of us or a third party is encountered during the use of information described in these circuit, we shall not be liable for any such problem, nor grant a license therefore.





## Note

1. There is a possibility with deterioration and destruction of IC when using it exceeding the absolute maximum rating. The absolute maximum rating, never exceed it. The functional operation is not assured.
2. There is a possibility that it becomes impossible to maintain this performance and reliability IC original when using it exceeding recommended operation voltage. Please use it in recommended operation voltage.
3. Due to restrictions on the package power dissipation, the output current value may not be satisfied. Attention should be paid to the power dissipation of the package when the output current is large or the voltage between Input and Output is high.
4. The output capacitor is required between output and GND to prevent oscillation.
5. The ESR of capacitor must be defined in ESR stability area. It is possible to use a ceramic capacitor without ESR resistance for output. The ceramic capacitor must be used more than 1.0 $\mu$ F and B temperature characteristics.
6. The wire of VDD and GND is required to print full ground plane for noise and stability.
7. The input capacitor must be connected a distance of less than 1cm from input pin.
8. In case the output voltage is above the input voltage, the overcurrent flow by internal parasitic diode from output to input. In such application, the external bypass diode must be connected between output and input pin.



9. Please connect the soft-start capacitor( $C_s$ ) more than 0.001 $\mu$ F with the terminal  $C_s$ .
10. The output capacitor and the soft start capacitor must be connected it within the limits a rush current peak level 1A showed in the typical performance characteristics.
11. When rush current exceeds current limit characteristics, it is restricted with the current limit set up with the chip, an output rise time is uncontrollable by soft-start capacitor.
12. When use connecting VDD and CE, in the case of starting VDD in input rise time longer then the set-up soft-start time, an output rise time is decide by a VDD input rise time.
13. Please do not give the voltage to the terminal  $C_s$ .
14. When the voltage of the soft-start pin is higher than the voltage of VDD, it becomes test mode. In that case, there is a possibility that the output voltage becomes unstable.



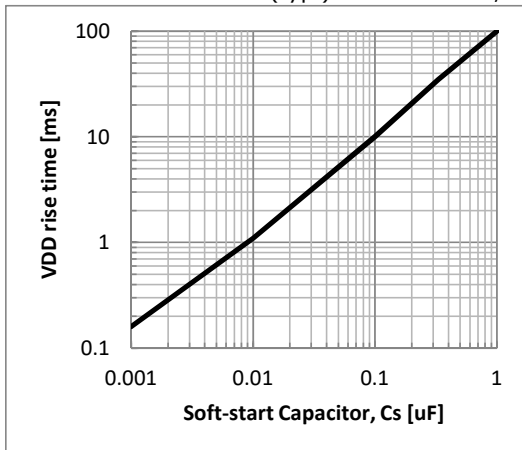


## Note

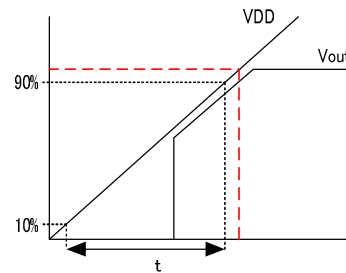
15. It is able to an unstable operation when you use the capacitor with intense capacitance change  
 The capacitor has the dependency at the power-supply voltage and the temperature.  
 The capacity value changes by the environment used. Please evaluate IC in the set.
16. The overcurrent protection circuit of foldback current limit type is built into this IC.
17. There is a possibility that IC generates heat when the output terminal is short-circuited.  
 However, the thermal shutdown circuit operates, and it will do operation that protects IC.  
 The thermal shutdown circuit is designed only to shut the IC off to prevent thermal runaway.  
 Do not continue to use the IC in an environment where the operation of this circuit is assumed.  
 The characteristic changes depending on the substrate condition.  
 Please evaluate IC in the set.
18. It returns automatically in temperature returned after it shuts down by self-generation of heat.  
 After it returns, it shuts down again by self-generation of heat.  
 It is necessary to change the environment used (IC consumption, temperature)  
 if it operates in upper cycle.
19. When Input rise time is longer than Vout soft start time, the output Voltage may happen  
 than the setting Voltage without a soft start function working normally.

Please set to soft-start capacitor for the VDD rise time in the slash area shown in Fig. 1.  
 Please choose to a capacitor in consideration of the dispersion .  
 Refer to Fig. 2 for a measurement circuit.

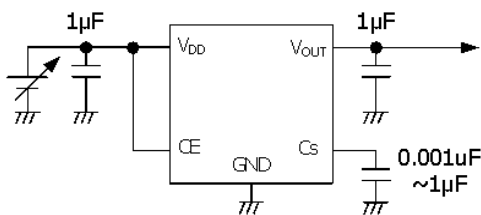
Condition :  $V_{DD} = V_{out}(typ.) + 1V$ ,  $C_E = V_{DD}$ ,  $T_a = -40^{\circ}C \sim 85^{\circ}C$



Fig, 1 Soft-start capacitor vs VDD rise time



\* VDD rise time (t) of VDD is judged in time (10%-90%) until VDD reaches Vout setting voltage.



Fig, 2 Test Circuit



## About Power Dissipation

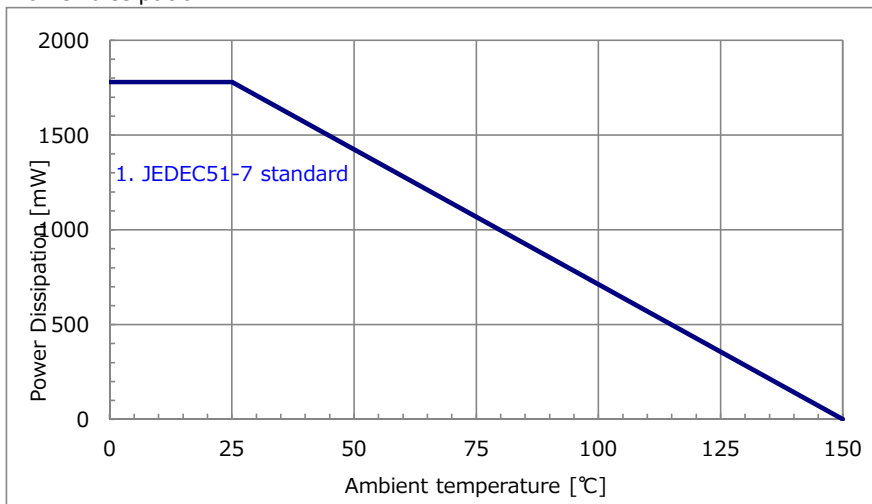
The Power dissipation change if board to mount IC change because radiative heat fix at board. It is reference data below, Evaluate IC in the set.

- SOT89-5A

1. JEDEC51-7 standard (4 layer FR-4 board)

Board size 114.3mm×76.2mm t=1.6mm Copper foil area 80%

Power dissipation 1780mW Ta=25°C

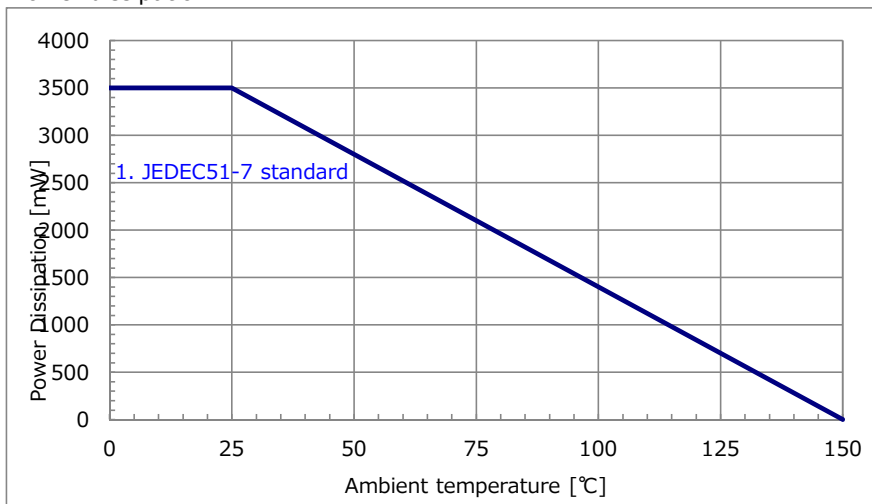


- HSOP-8E

1. JEDEC51-7 standard (4 layer FR-4 board)

Board size 114.3mm×76.2mm t=1.6mm Copper foil area 80%

Power dissipation 3500mW Ta=25°C



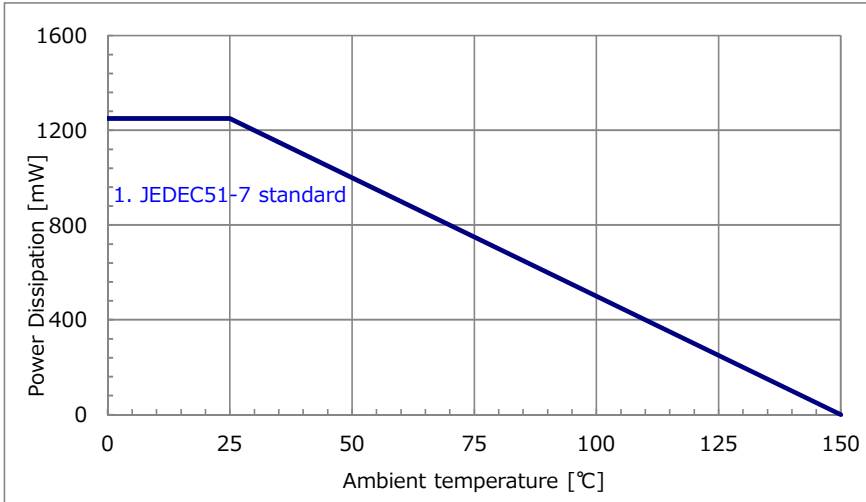


## About Power Dissipation

- SSON-6A

- JEDEC51-7 standard (4 layer FR-4 board)

Board size 114.3mm×76.2mm t=1.6mm Copper foil area 80%  
 Power dissipation 1250mW Ta=25°C



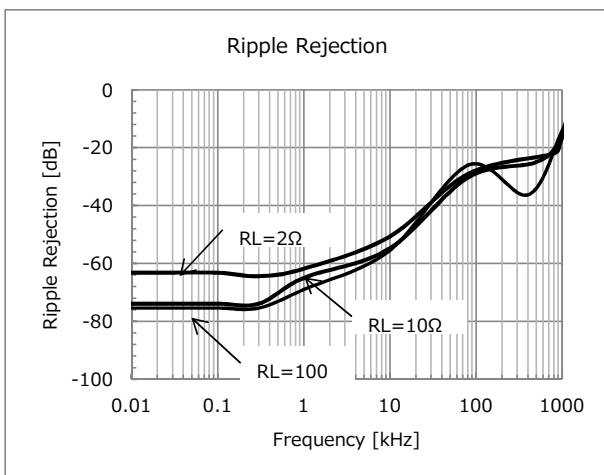
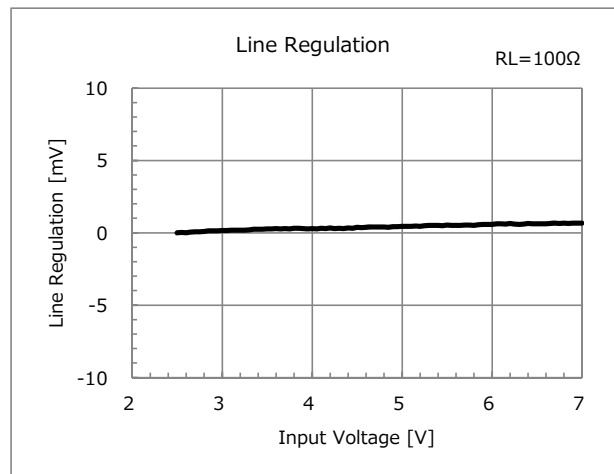
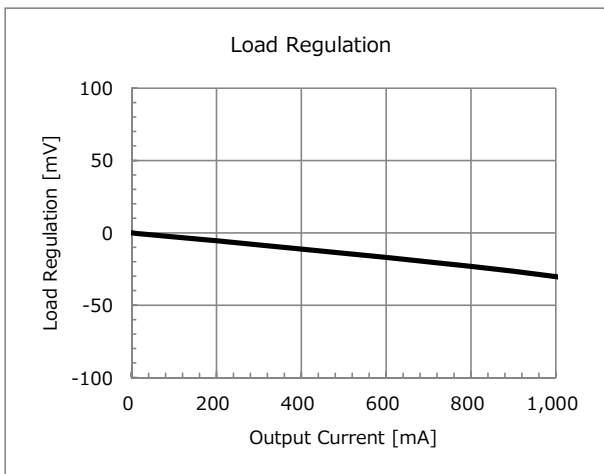
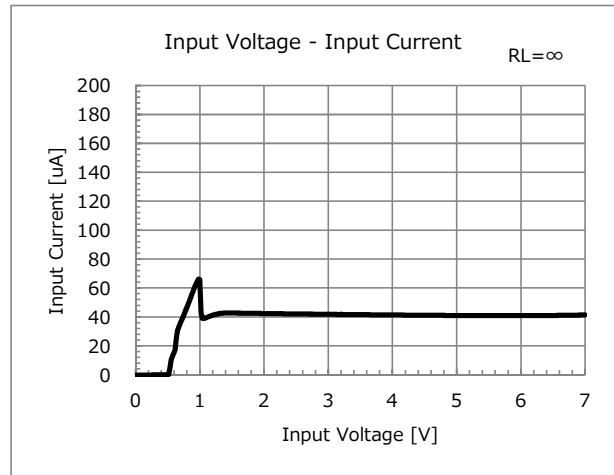
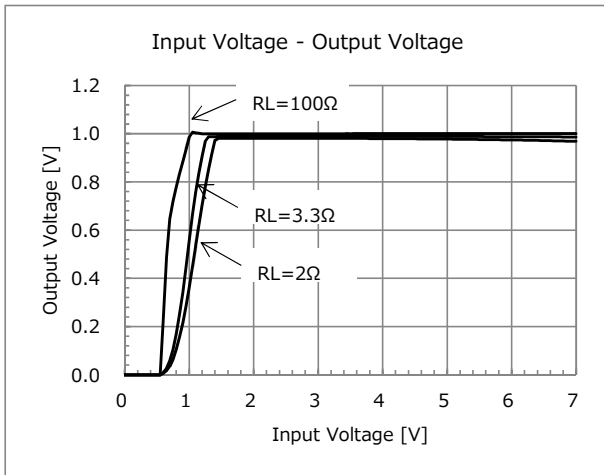
It is recommended to layout the VIA for heat radiation in the GND pattern of reverse (of IC) when there is the GND pattern in the inner layer (in using multilayer substrate). By increasing these copper foil pattern area of PCB, Power dissipation improves.





## Typical Performance Characteristics (1.0V)

( $V_{DD}=V_{OUT(Typ.)}+1V$ ,  $V_{CE}=V_{DD}$ ,  $T_a=25^\circ C$  unless otherwise specified)

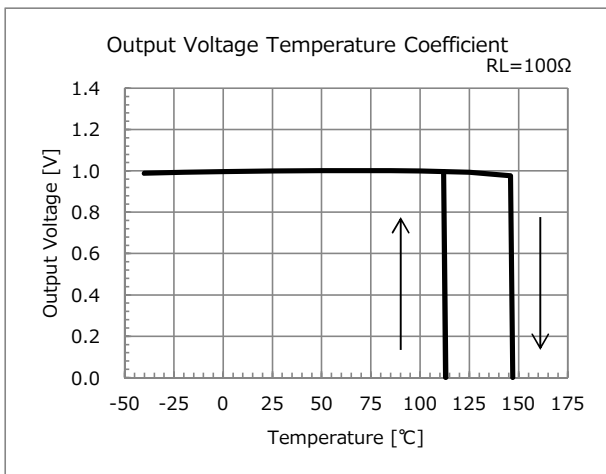
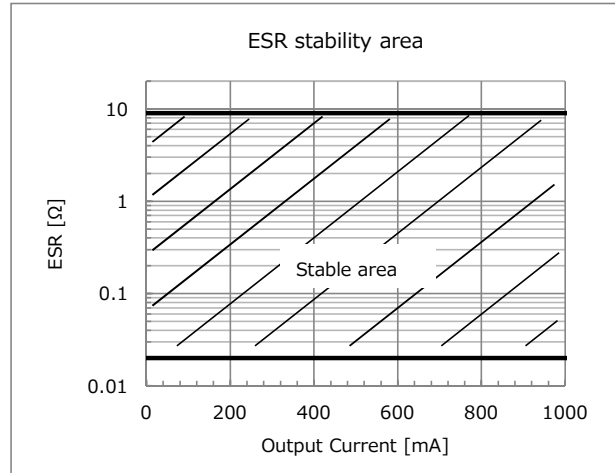
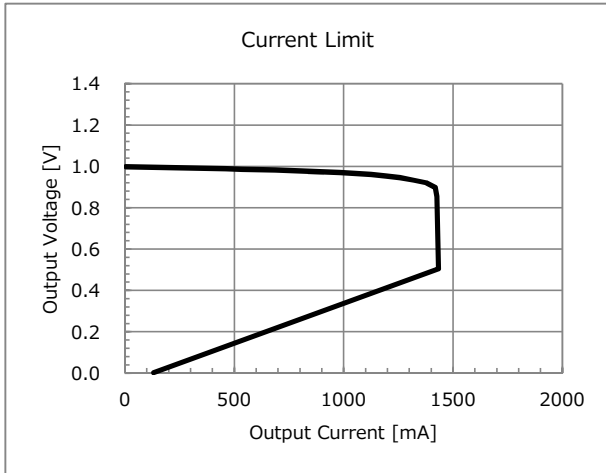






## Typical Performance Characteristics (1.0V)

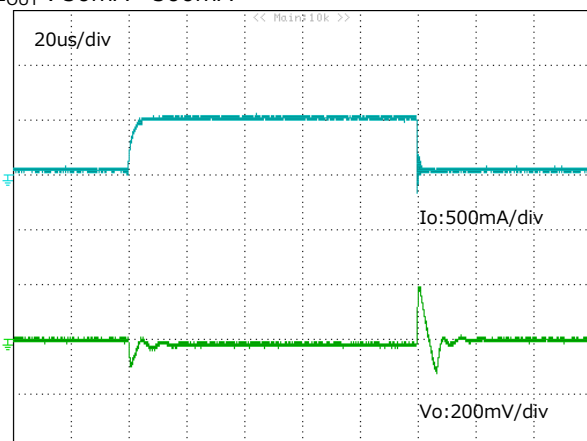
( $V_{DD}=V_{OUT(Typ.)}+1V$ ,  $V_{CE}=V_{DD}$ ,  $T_a=25^\circ C$  unless otherwise specified)



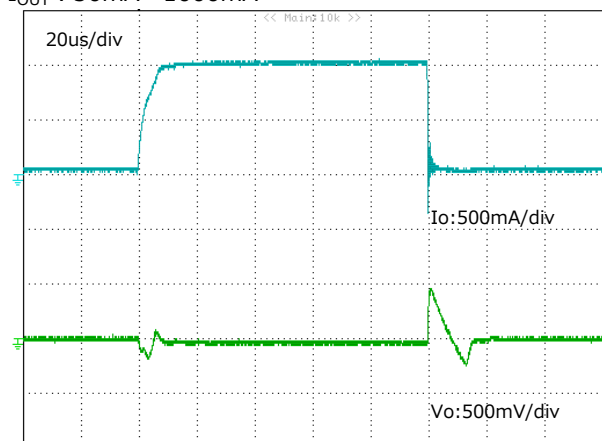
### ■ Load transient response

( $C_{in}=C_o=1\mu F$ )

$I_{OUT} : 50mA \Leftrightarrow 500mA$



$I_{OUT} : 50mA \Leftrightarrow 1000mA$



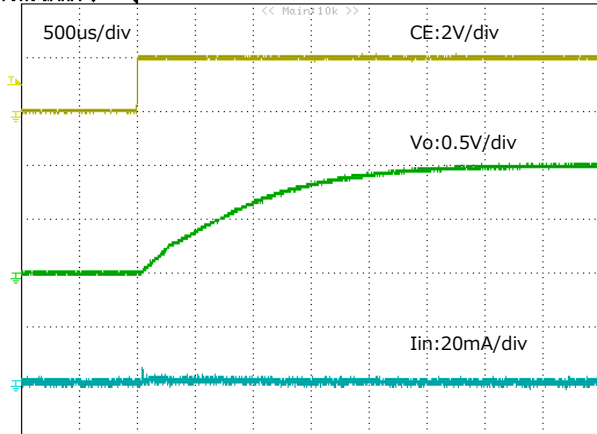


## Typical Performance Characteristics (1.0V)

( $V_{DD}=V_{OUT(Typ.)}+1V$ ,  $V_{CE}=V_{DD}$ ,  $T_a=25^\circ C$  unless otherwise specified)

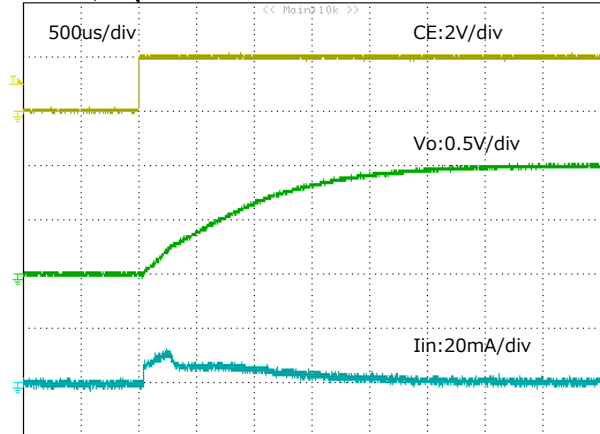
### CE rise characteristics1

( $V_{DD}=2V$ ,  $V_{CE}=0V \rightarrow 2V$ ,  $C_o=1\mu F$ )



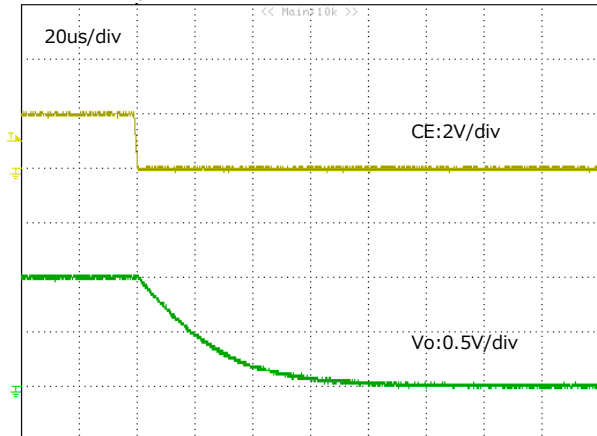
### CE rise characteristics2

( $V_{DD}=2V$ ,  $V_{CE}=0V \rightarrow 2V$ ,  $C_o=10\mu F$ )



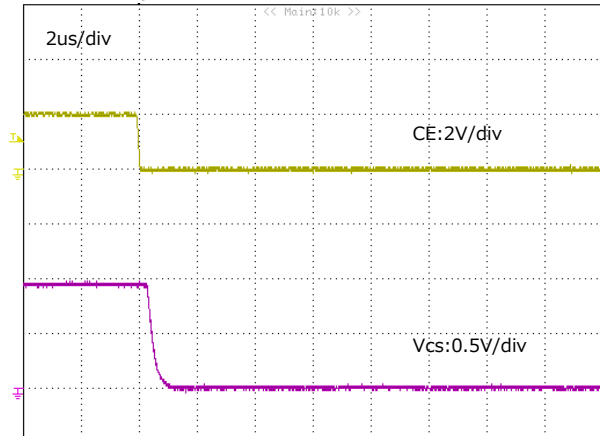
### $V_{OUT}$ discharge characteristics

( $V_{DD}=2V$ ,  $V_{CE}=2V \rightarrow 0V$ ,  $C_o=1\mu F$ )



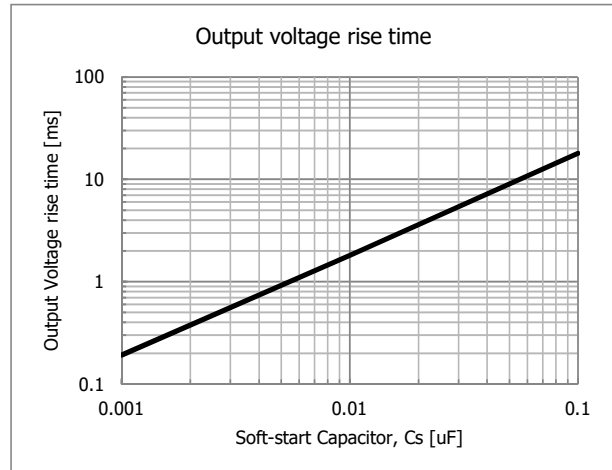
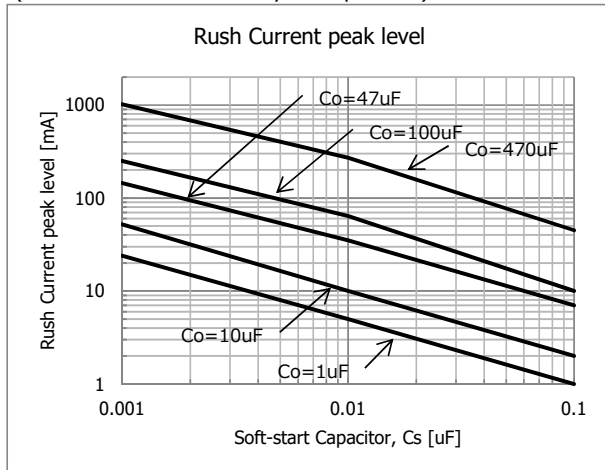
### Vcs discharge characteristics

( $V_{DD}=2V$ ,  $V_{CE}=2V \rightarrow 0V$ ,  $C_s=0.01\mu F$ )



### Rush Current characteristics

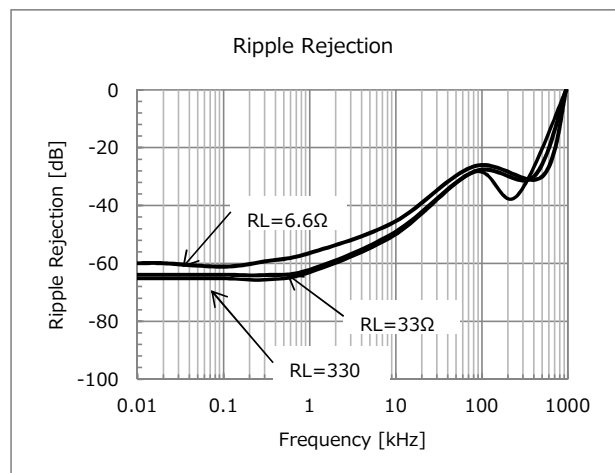
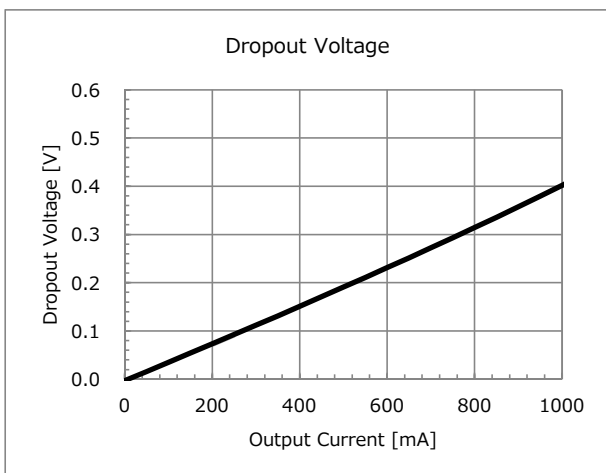
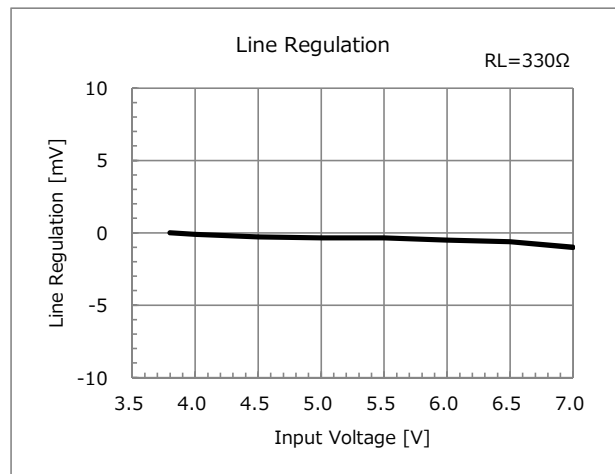
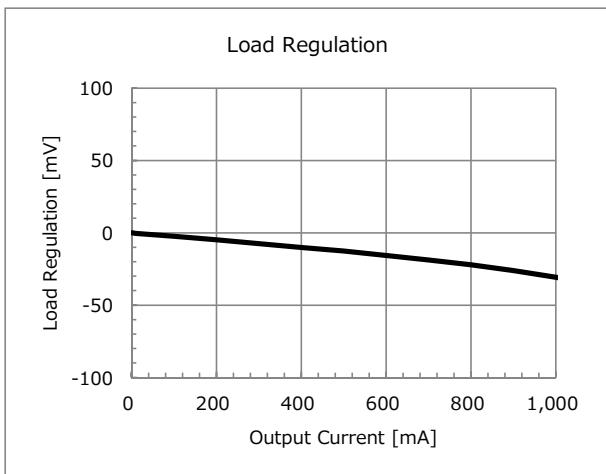
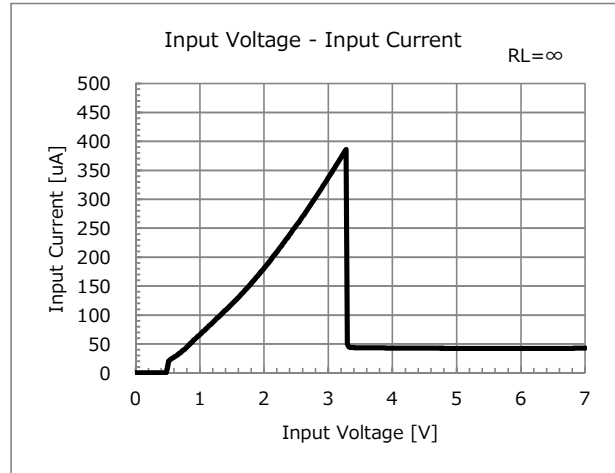
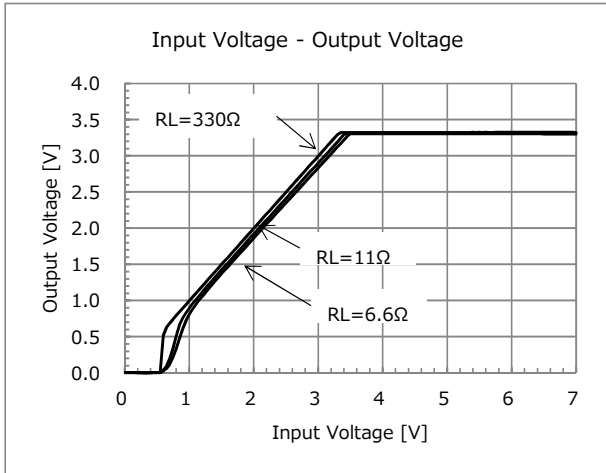
( $C_o$ : aluminum electrolytic capacitor)





## Typical Performance Characteristics (3.3V)

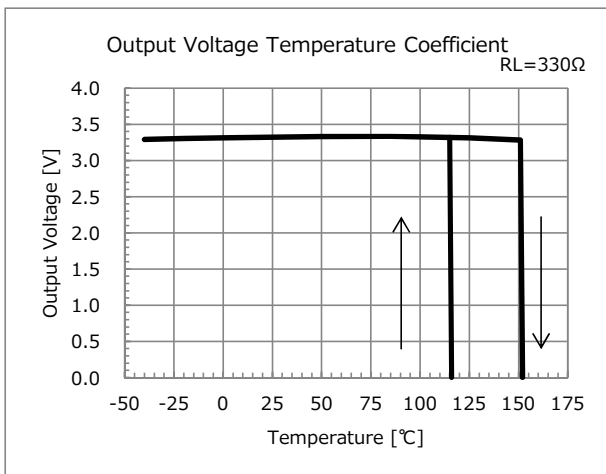
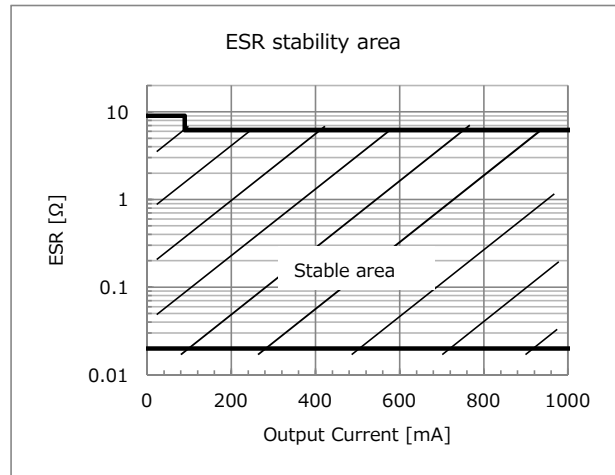
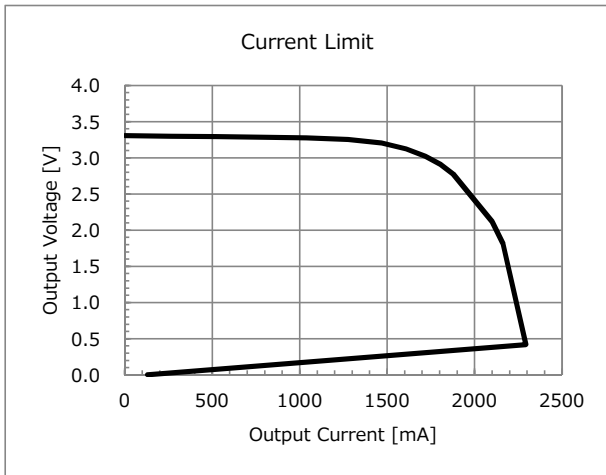
( $V_{DD}=V_{OUT(Typ.)}+1V$ ,  $V_{CE}=V_{DD}$ ,  $T_a=25^\circ C$  unless otherwise specified)





## Typical Performance Characteristics (3.3V)

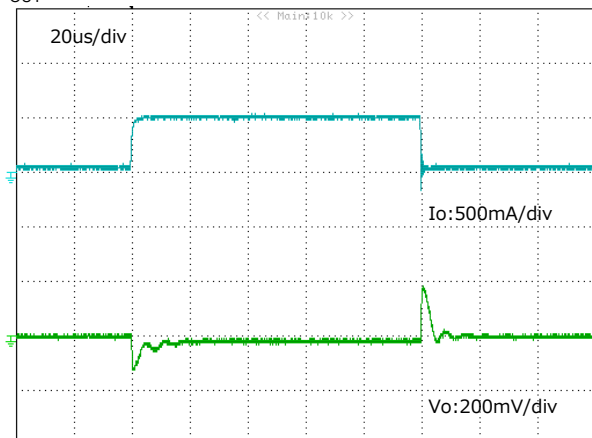
( $V_{DD}=V_{OUT(Typ.)}+1V$ ,  $V_{CE}=V_{DD}$ ,  $T_a=25^\circ C$  unless otherwise specified)



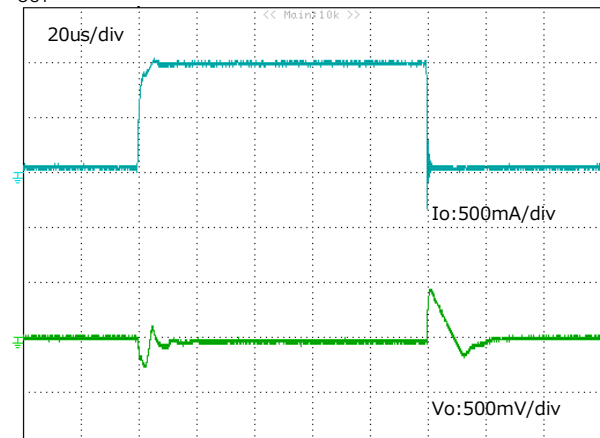
### ■ Load transient response

( $C_{in}=C_o=1\mu F$ )

$I_{OUT} : 50mA \leftrightarrow 500mA$



$I_{OUT} : 50mA \leftrightarrow 1000mA$



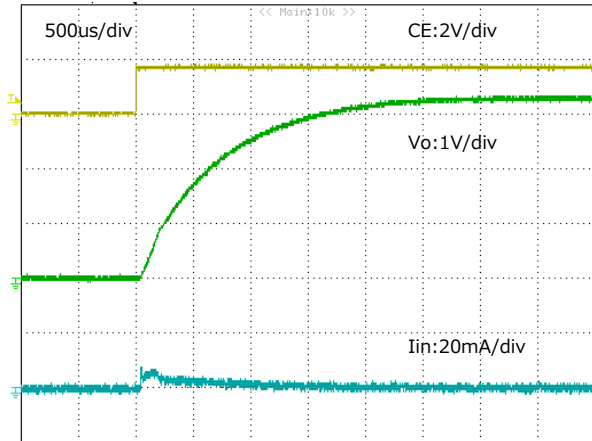


## Typical Performance Characteristics (3.3V)

( $V_{DD}=V_{OUT(Typ.)}+1V$ ,  $V_{CE}=V_{DD}$ ,  $T_a=25^\circ C$  unless otherwise specified)

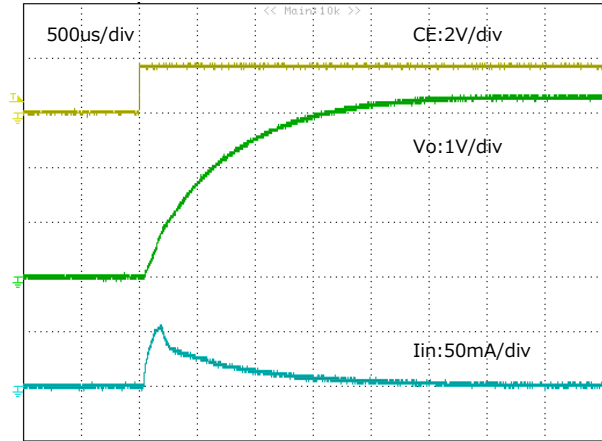
### CE rise characteristics1

( $V_{DD}=4.3V$ ,  $V_{CE}=0V \rightarrow 4.3V$ ,  $C_o=1\mu F$ )



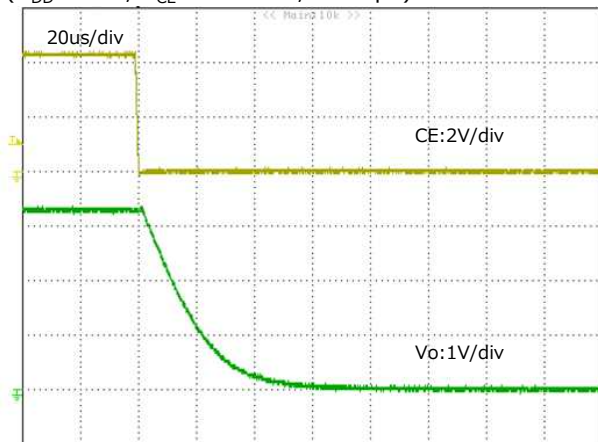
### CE rise characteristics2

( $V_{DD}=4.3V$ ,  $V_{CE}=0V \rightarrow 4.3V$ ,  $C_o=10\mu F$ )



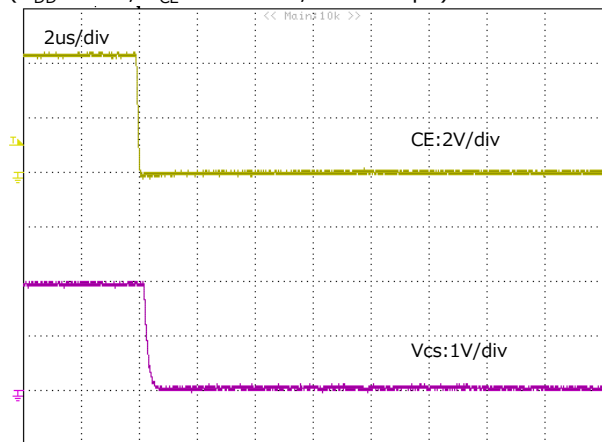
### $V_{OUT}$ discharge characteristics

( $V_{DD}=4.3V$ ,  $V_{CE}=4.3V \rightarrow 0V$ ,  $C_o=1\mu F$ )



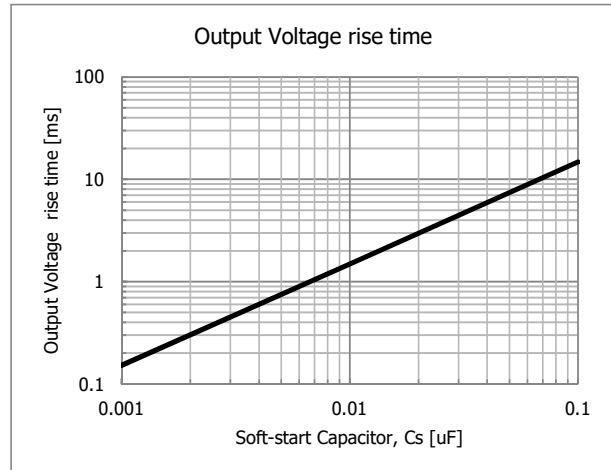
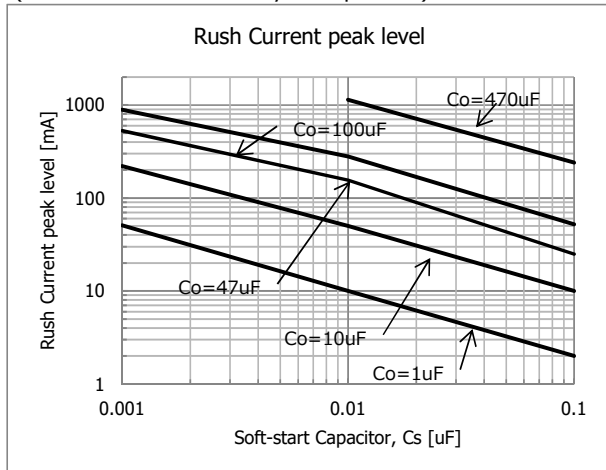
### Vcs discharge characteristics

( $V_{DD}=4.3V$ ,  $V_{CE}=4.3V \rightarrow 0V$ ,  $C_s=0.01\mu F$ )



### Rush Current characteristics

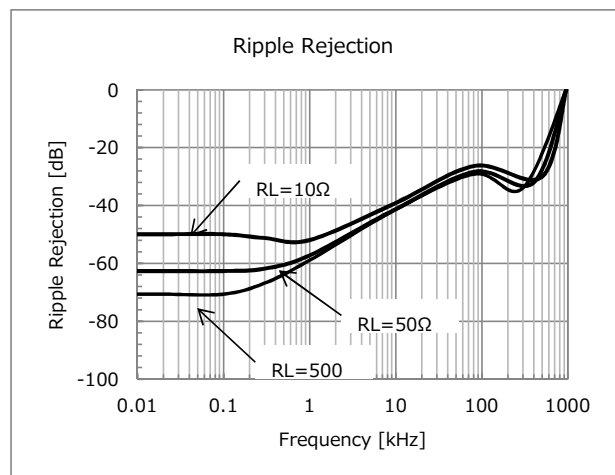
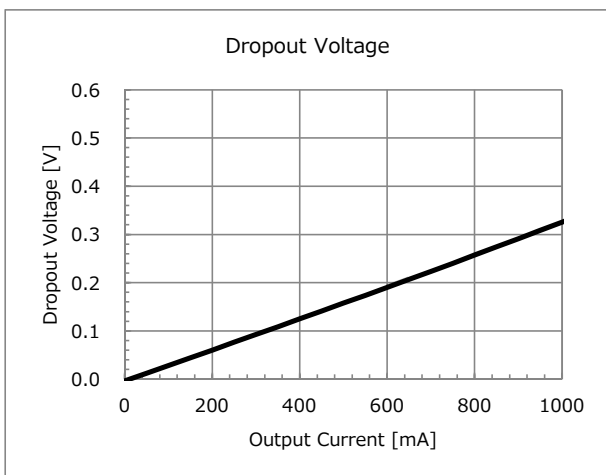
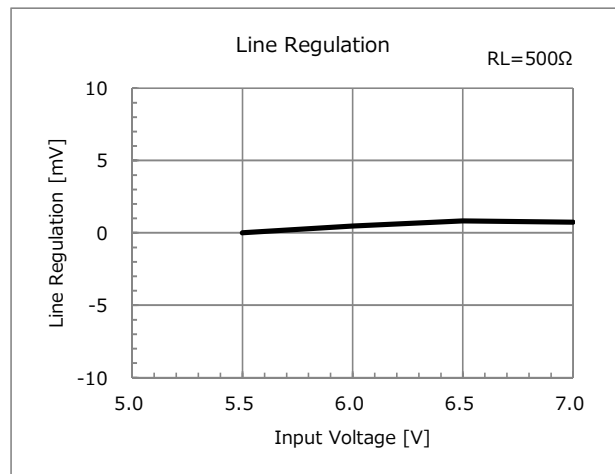
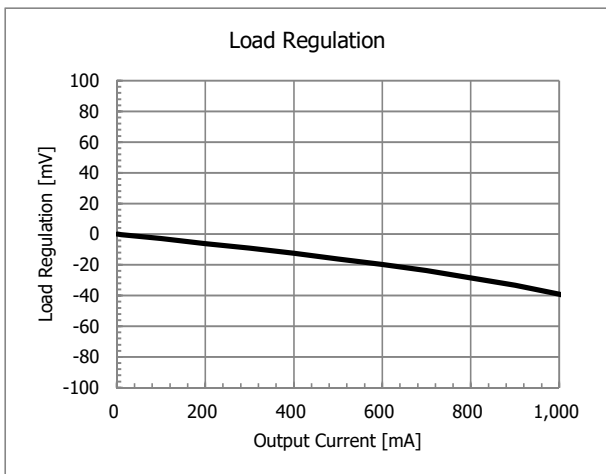
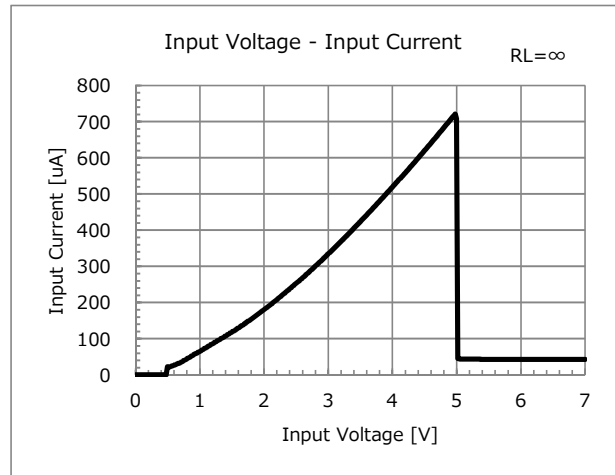
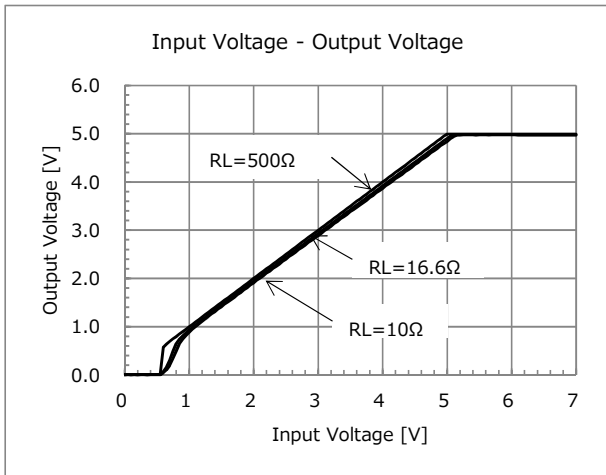
( $C_o$ : aluminum electrolytic capacitor)





## Typical Performance Characteristics (5.0V)

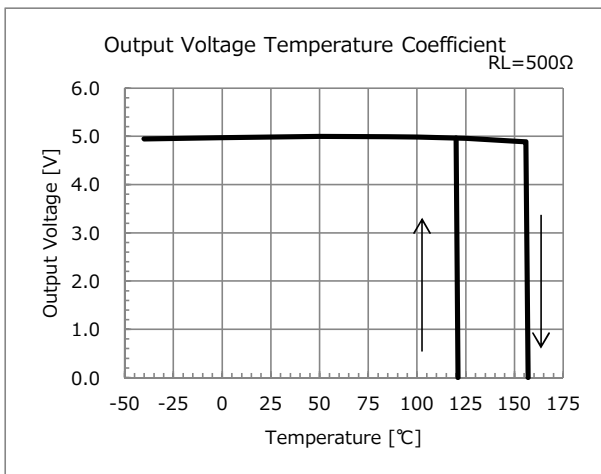
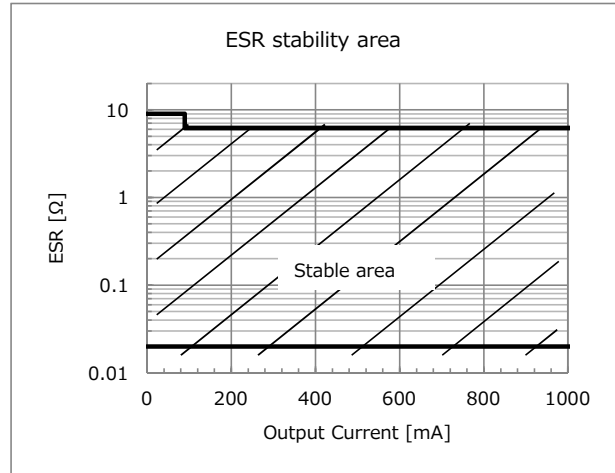
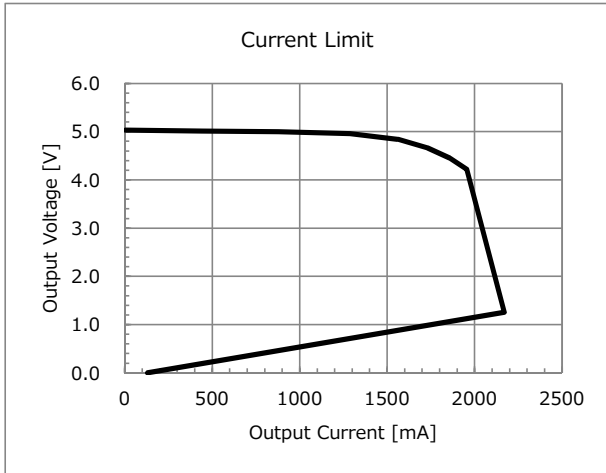
( $V_{DD}=V_{OUT(Typ.)}+1V$ ,  $V_{CE}=V_{DD}$ ,  $T_a=25^\circ C$  unless otherwise specified)





## Typical Performance Characteristics (5.0V)

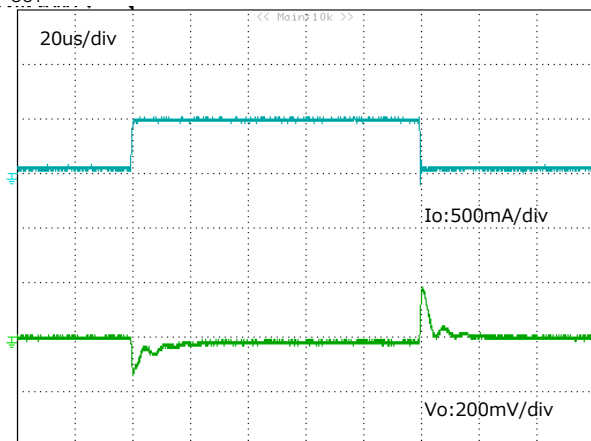
( $V_{DD}=V_{OUT(Typ.)}+1V$ ,  $V_{CE}=V_{DD}$ ,  $T_a=25^\circ\text{C}$  unless otherwise specified)



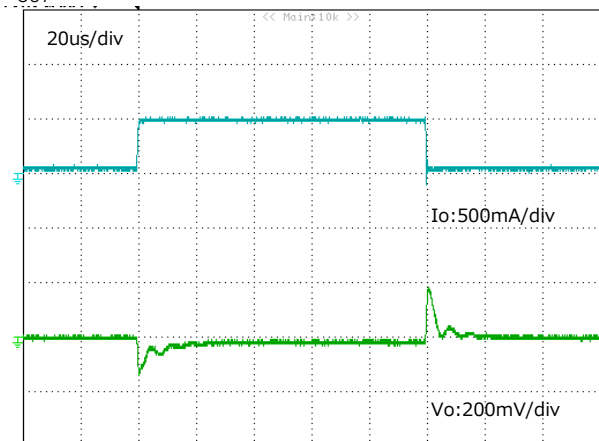
### ■ Load transient response

( $C_{in}=C_o=1\mu\text{F}$ )

$I_{OUT} : 50\text{mA} \Leftrightarrow 500\text{mA}$



$I_{OUT} : 50\text{mA} \Leftrightarrow 1000\text{mA}$



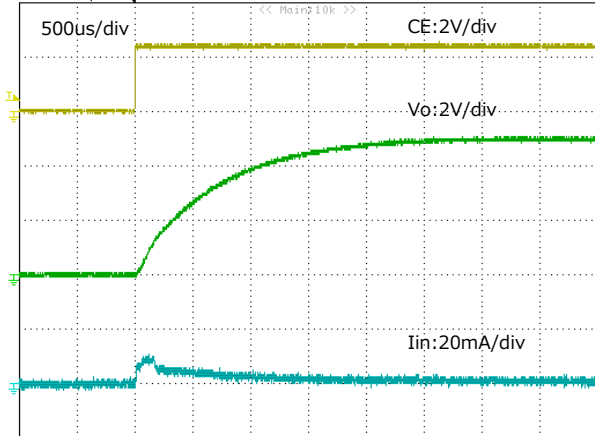


## Typical Performance Characteristics (5.0V)

( $V_{DD}=V_{OUT(Typ.)}+1V$ ,  $V_{CE}=V_{DD}$ ,  $T_a=25^\circ C$  unless otherwise specified)

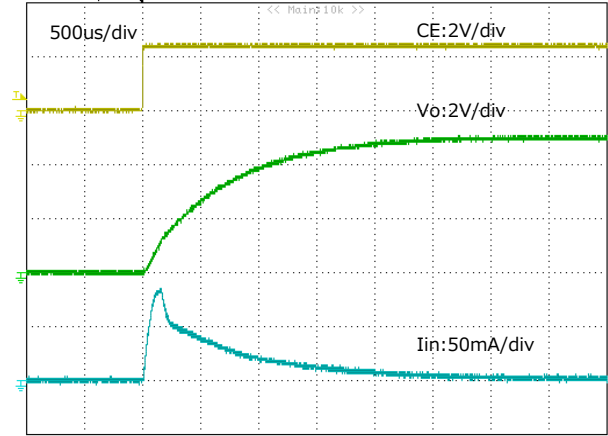
### CE rise characteristics1

( $V_{DD}=6.0V$ ,  $V_{CE}=0V \rightarrow 6.0V$ ,  $C_o=1\mu F$ )



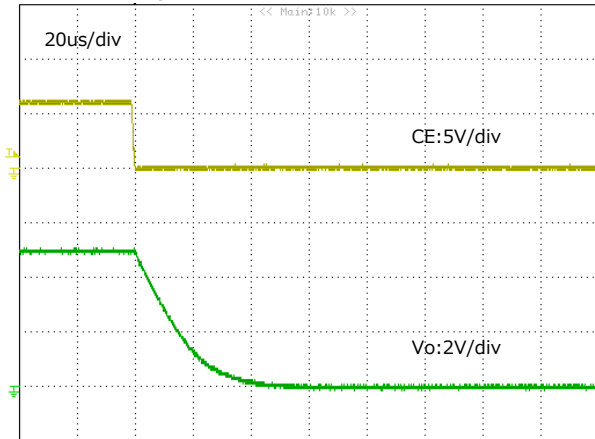
### CE rise characteristics2

( $V_{DD}=6.0V$ ,  $V_{CE}=0V \rightarrow 6.0V$ ,  $C_o=10\mu F$ )



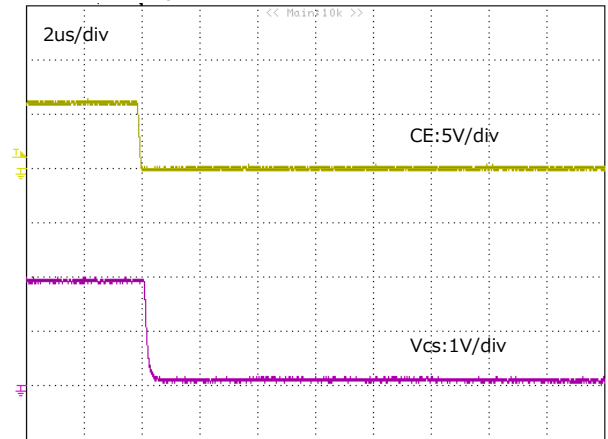
### $V_{OUT}$ discharge characteristics

( $V_{DD}=6.0V$ ,  $V_{CE}=6.0V \rightarrow 0V$ ,  $C_o=1\mu F$ )



### Vcs discharge characteristics

( $V_{DD}=6.0V$ ,  $V_{CE}=6.0V \rightarrow 0V$ ,  $C_s=0.01\mu F$ )



### Rush Current characteristics

( $C_o$ : aluminum electrolytic capacitor)

