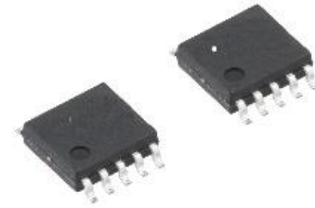


LED lighting power supply IC

MM3760 series



Outline

This IC is a control IC for LED lighting that supports phase dimming, PWM dimming, and DC dimming. Quasi-resonant switching is used to achieve low loss and low noise. In addition, the combination of peak current control and variable OFF time control realizes a non-linear dimming curve, enabling visually smooth dimming. In addition, a dedicated terminal (VOUT) is provided to supply the holding current required for normal operation of the phase dimmer, enabling a reduction in the number of parts.

Features

- Low loss and low noise by quasi-resonant switching
- Smooth dimming characteristics by combining peak current control and variable OFF time control
- Equipped with a phase detection output terminal for holding current control
- Minimum dimming current can be clamped
- Firefly switch compatible (up to 5 units can be connected in parallel)
- Various protection functions (OCP, SWP, UVLO, TSD, ISNS pin open protection)

Main specifications

- Operating voltage range : 10~25.5V
- Maximum current detection voltage : 06V(Typ.)
- Operating current consumption : 3.5mA(Typ.)
- Overcurrent detection voltage : 0.8V(Typ.)
- Firefly switch compatible load resistance : 10kΩ(Typ.)
- SWP short detection voltage : 2.5V(Typ.)

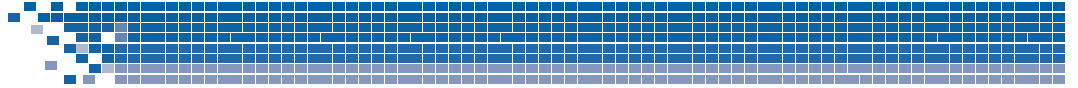
Applications

- Downlight
- ceiling light

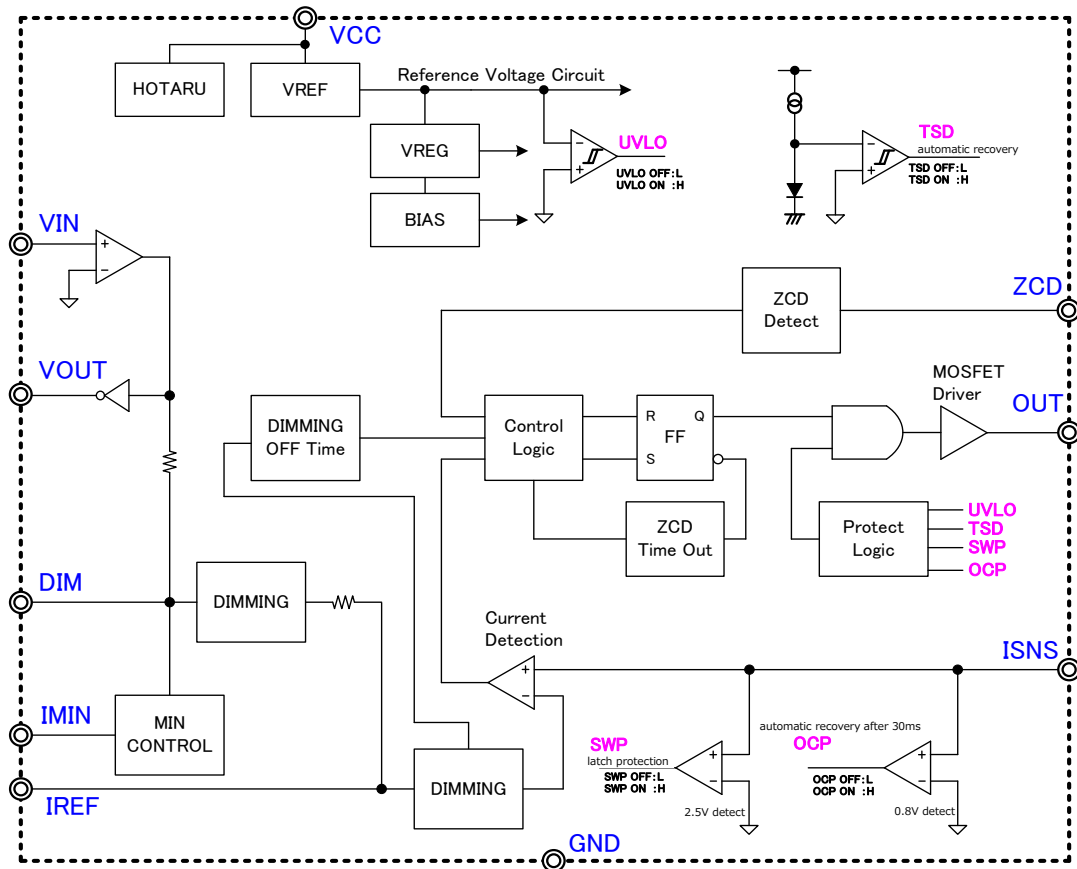
Package

- SOP-10A

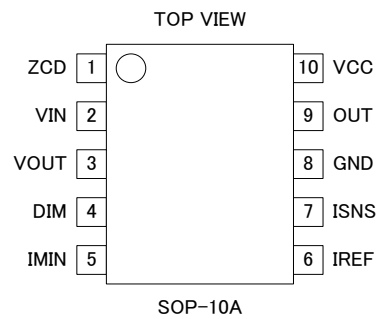


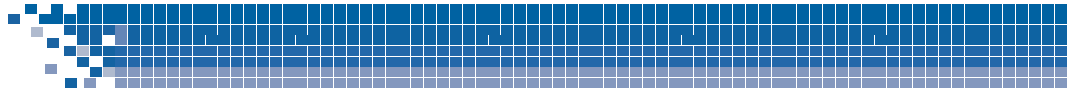


Equivalent Circuit Diagram



Pin configurations

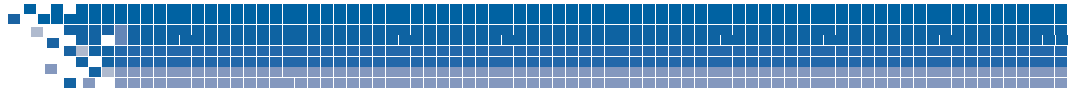




Pin explanations□

PIN No.	SYMBOL	FUNCTION
1	ZCD	Zero current detect terminal
2	VIN	Phase detect input terminal
3	VOUT	Phase detect output terminal
4	DIM	Light dimming terminal
5	IMIN	Minimum LED current control terminal
6	IREF	LED current sensing reference voltage terminal
7	ISNS	LED current sensing terminal
8	GND	GND terminal
9	OUT	Gate drive terminal
10	VCC	Power supply terminal





ABSOLUTE MAXIMUM RATINGS

(Ta=25°C / VCC=15V, unless otherwise specified)

ITEM	SYMBOL	MIN.	MAX.	UNIT
Supply Voltage	V _{CCMAX}	-0.3	30	V
VCC pin input current	I _{CCMAX}	-	10	mA
ZCD Pin Voltage	V _{ZCDMAX}	-0.3	5.5	V
VIN Pin Voltage	V _{VINMAX}	-0.3	5.5	V
VOUT Pin Voltage	V _{VOUTMAX}	-0.3	V _{VOUTH} +0.3	V
DIM Pin Voltage	V _{DIMMAX}	-0.3	5.5	V
IMIN Pin Voltage	V _{IMINMAX}	-0.3	5.5	V
IREF Pin Voltage	V _{IREFMAX}	-0.3	5.5	V
ISNS Pin Voltage	V _{ISNSMAX}	-0.3	5.5	V
OUT pin input current	I _{OUTMAX}	-600	700	mA
Power Dissipation (Alone)	P _d _{alone}	-	350(*1)	mW
Power Dissipation (Mounted)	P _d	-	800(*2)	mW
Thermal resistance junction to ambient temperature	R _{θJA}	-	360(*1)	°C/W
Storage temperature	T _{stg}	-55	150	°C
Junction temperature	T _{JMAX}	-	150	°C

Note : Absolute Maximum Ratings are those values beyond which the life of the device may be impaired

*1 : Result of measurement in only package

*2 : Use base condition : 114.3mm×76.2mm, t =1.5mm, Copper leaf 50% more than,
Material=Glass Epoxy





RECOMMENDED OPERATING CONDITIONS

(Ta=25°C / Ta=25°C, unless otherwise specified)

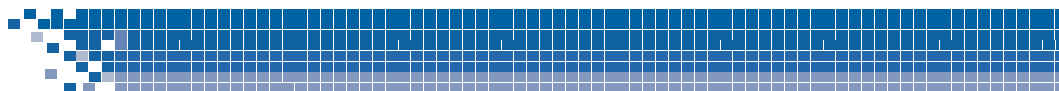
ITEM	SYMBOL	MIN.	MAX.	UNIT
Supply Voltage	V _{CC-OPR}	10	25.5	V
VOUT Pin Voltage	V _{VOUT-OPR}	0	V _{VOUTH}	V
VIN Pin Voltage	V _{VIN-OPR}	0	5	V
DIM Pin Voltage	V _{DIM-OPR}	0	5	V
IMIN Pin Voltage	V _{IMIN-OPR}	0	5	V
ZCD Pin Voltage	V _{ZCD-OPR}	0	5	V
IREF Pin Voltage	V _{IREF-OPR}	0	5	V
ISNS Pin Voltage	V _{ISNS-OPR}	0	5	V
Operating Junction temperature	T _{JMAX-OPR}	-40	125	°C

Note : Thermal Resistance (junction to ambient temperature) is dependent on the operating condition and the substrate to be used.

Maximum operating temperature range (T_{amax}) is calculated from the following equation from maximum operating junction temperature (T_{jmax-opr}), maximum power consumption (P_{d-max}) and thermal resistance(R_{θja}).

$$T_{amax} = T_{jmax-opr} - (R_{\theta ja} \times P_{d-max})$$

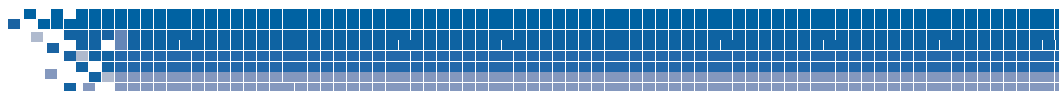




Electrical Characteristics

(Ta=25°C / VCC=15V, unless otherwise specified)

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNIT	*3
VCC circuit							
Start-up power supply current	I _{CCOFF}	V _{CC} =9V	0.51	0.86	1.37	mA	A
Power supply current	I _{CCON}	V _{CC} =15V	-	3.5	5.0	mA	A
Load resistance for Hotaru switch	R _{HOTARU}	V _{CC} =9V	6	10	14	kΩ	A
UVLO							
Under voltage lockout (UVLO)	V _{UVLO}	V _{CC} =H→L	8	9	10	V	A
UVLO release	V _{UVLOR}	V _{CC} =L→H	11.5	13.0	14.5	V	A
UVLO hysteresis voltage	V _{UVLOhys}	V _{CC} =H→L→H	3.3	4.0	4.7	V	A
Phase Detect							
Phase detect voltage H	V _{VINH}	V _{VIN} =L→H	0.58	0.65	0.72	V	B
Phase detect voltage L	V _{VINL}	V _{VIN} =H→L	0.40	0.45	0.50	V	B
Phase Output							
Output pulse voltage	V _{VOUTH}		4.5	5.0	5.5	V	A
VOUT sink current	I _{VOUTsink}	V _{VIN} =0V, V _{VOUT} =2.5V	-	5.7	-	mA	B
VOUT source current	I _{VOUTsource}	V _{VIN} =1V, V _{VOUT} =2.5V	-	4.0	-	mA	B
Dimming Input							
DIM output resistance	R _{DIM}		-	180	-	kΩ	B
Off blanking time increase start voltage	V _{DIMOFFbkstart}	Guaranteed by design	1.935	2.150	2.365	V	-
Isns detect voltage fall start voltage	V _{DIMISNSstart}		3.15	3.32	3.49	V	C
Isns detect voltage fall stop voltage	V _{DIMISNSstop}		0.532	0.560	0.588	V	C
Minimum Current Setting							
IMIN source current	I _{IMINsource}	V _{IMIN} =0V	4.5	5.0	5.5	uA	A
IREF Output							
IREF voltage	V _{IREF}	V _{DIM} =4.5V	2.70	2.87	3.04	V	C
IREF output resistance	R _{IREF}		90	140	190	kΩ	C



(Ta=25°C / VCC=15V, unless otherwise specified)

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNIT	*3
Zero Current Detect							
Zero current detect delay time	t_{dZCD}		-	100	-	ns	D
Zero current detect time-out	$t_{dZCD-TO}$	$V_{ZCD}=3.0V$	-	50	-	us	D
ISNS Input							
Maximun ISNS current detect voltage	$V_{ISNSmax}$	$V_{DIM}=4.5V$	576	600	624	mV	C
ISNS current detect blanking time	t_{ISNSb}		-	380	-	ns	E
Over current detect voltage	V_{OCP}	$V_{DIM}=4.5V$	720	800	880	mV	E
Over current detecting restart time	t_{OCP}		-	30	-	ms	E
Short winding protection detect voltage	V_{SWP}		2.25	2.50	2.75	V	E
ISNS source current	I_{ISNS}	$V_{ISNS}=0V$	-	1.0	-	uA	C
Dimming OFF Timer							
Off blanking time	t_{OFFbk}	$V_{IREF}=50mV$	58.5	65.0	84.5	us	E
Shortest Off blanking time	$t_{OFFbkmin}$	$V_{DIM}=4.5V$	-	-	4.0	us	E
Drive							
On resistance (sink)	R_{ONsink}		-	8	12	Ω	F
On resistance (source)	$R_{ONsource}$		-	10	15	Ω	F
Output rise time	t_{RISE}	$C_{out}=1000pF$ Guaranteed by design	-	60	120	ns	-
Output fall time	t_{FALL}	$C_{out}=1000pF$ Guaranteed by design	-	30	70	ns	-
Thermal Shut Down							
TSD detection temp.	T_{DET}	Guaranteed by design	-	170	-	$^{\circ}C$	-
TSD release temp.	$T_{RELEASE}$	Guaranteed by design	-	120	-	$^{\circ}C$	-

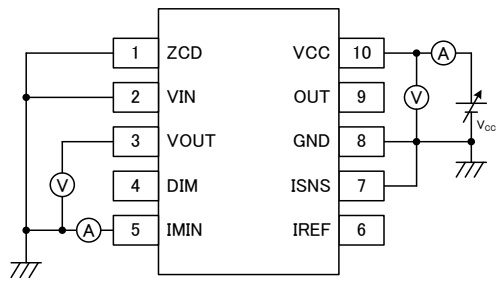
*3 The test circuit symbols.



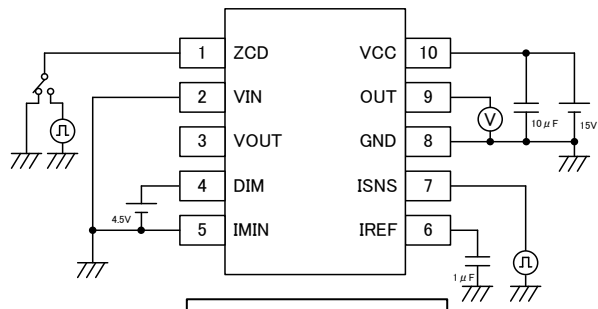


TEST CIRCUITS

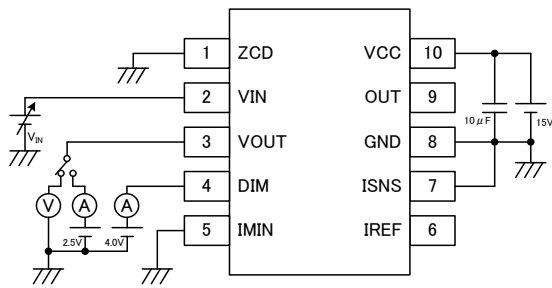
TEST CIRCUIT A



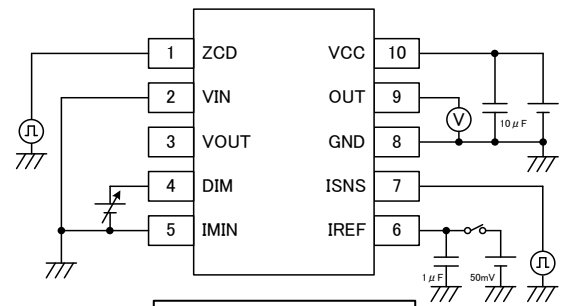
TEST CIRCUIT D



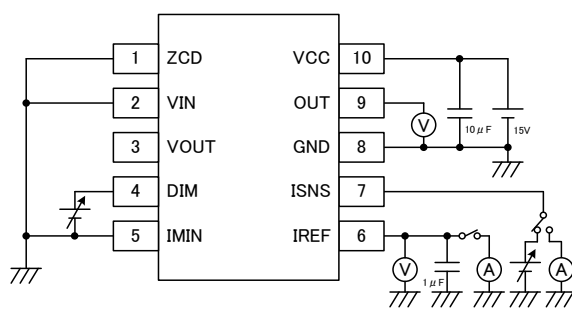
TEST CIRCUIT B



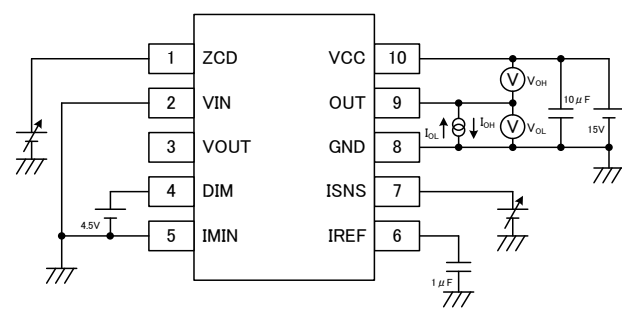
TEST CIRCUIT E



TEST CIRCUIT C



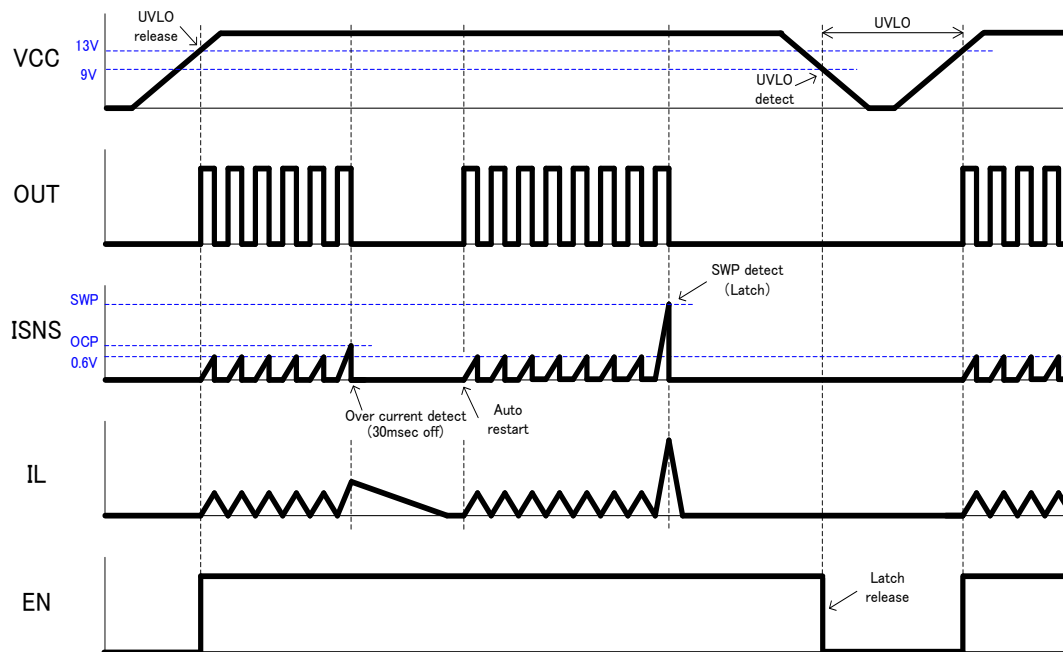
TEST CIRCUIT F



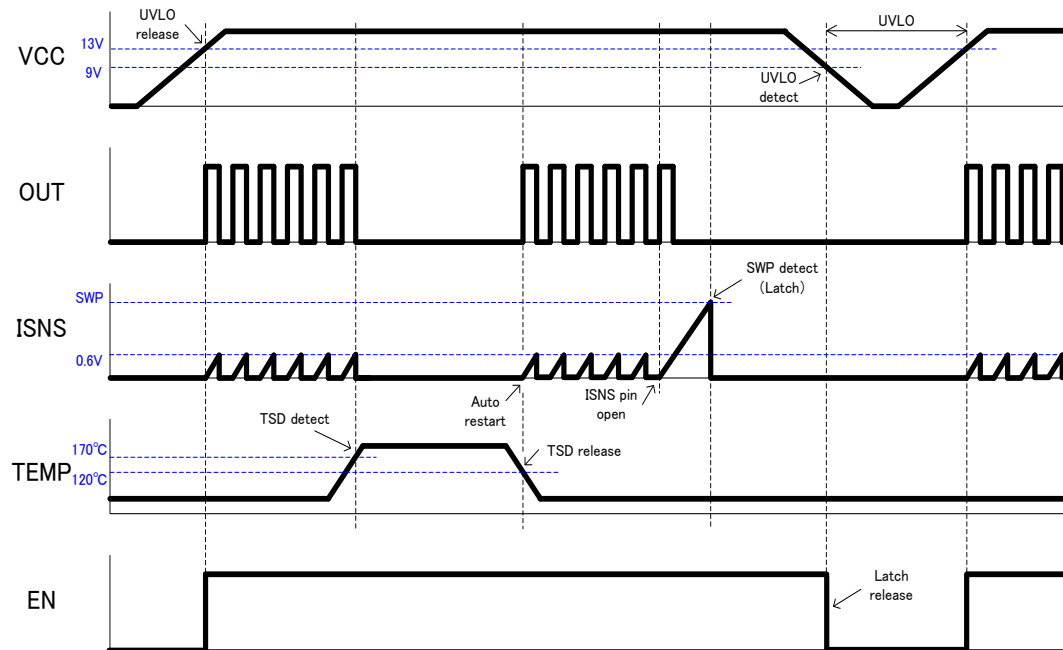


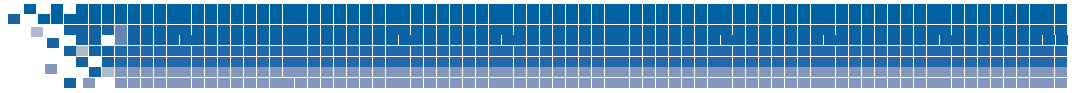
TIMING CHART

UVLO, Over current detect, and Short winding protection detect operations

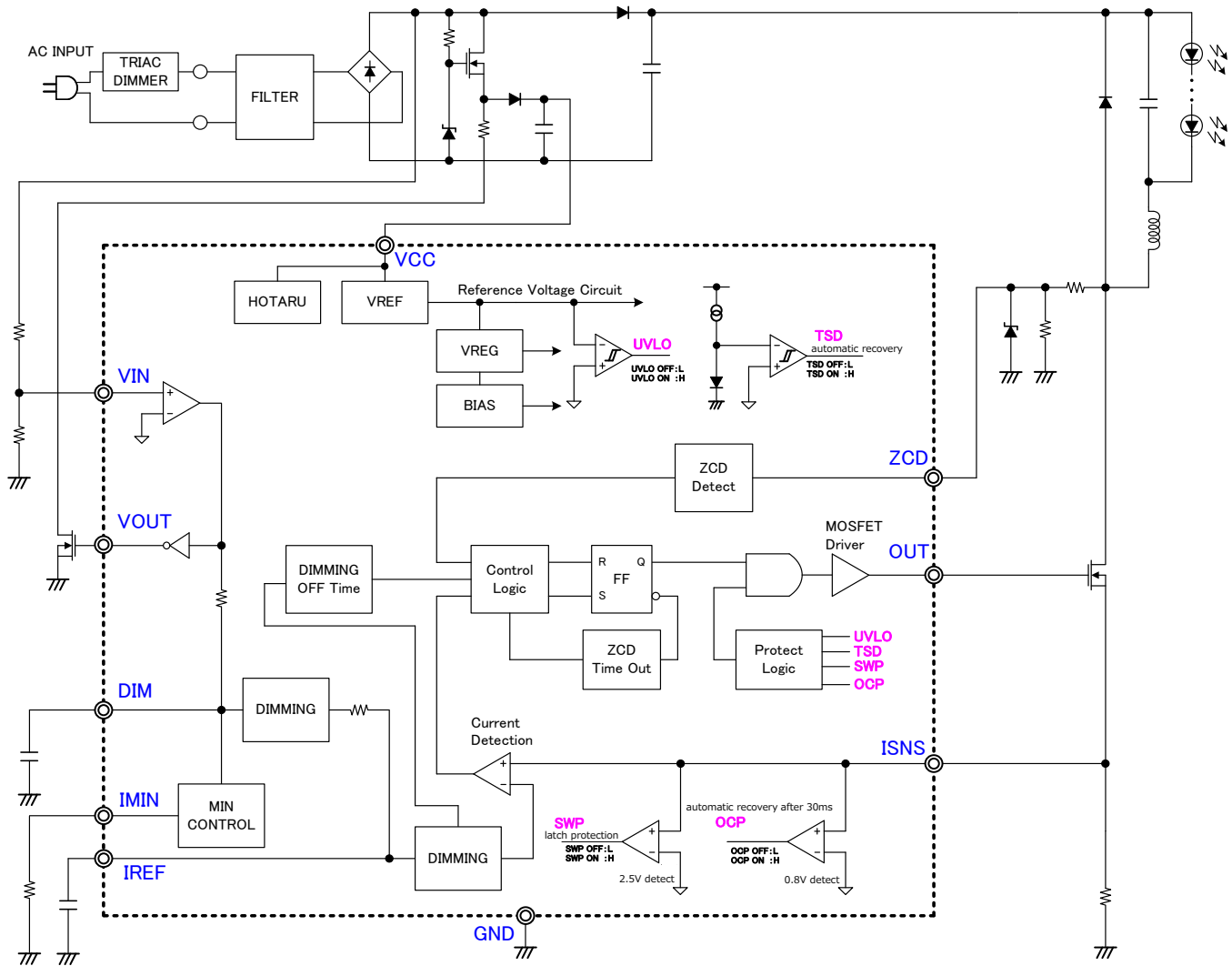


ISNS open detect and Thermal shut down operations





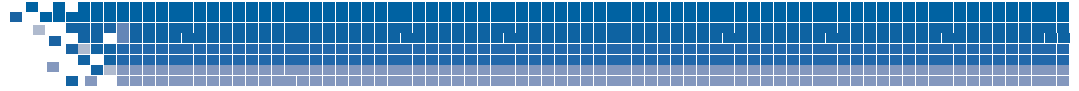
Application Circuits



caution : We shall not be liable for any trouble or damage caused by using this circuit .

In the event a problem which may affect industrial property or any other rights of us or a third party is encountered during the use of information described in these circuit, we shall not be liable for any such problem, nor grant a license therefore.

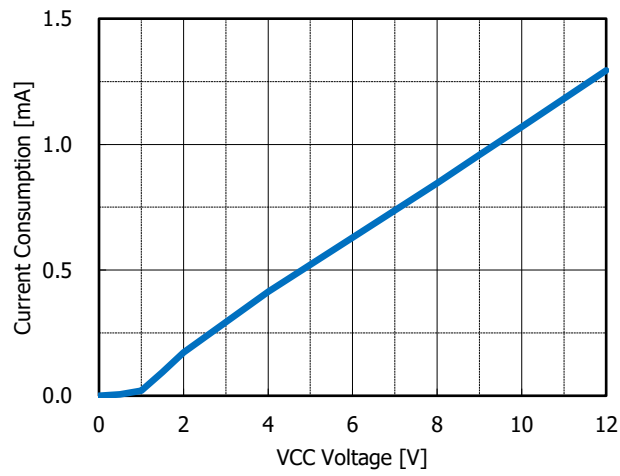




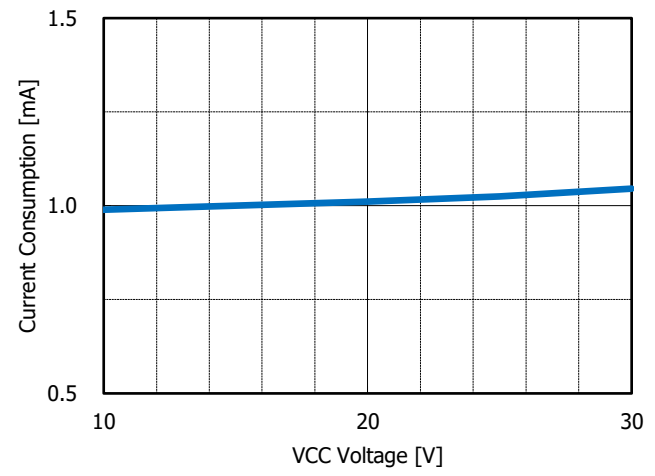
CHARACTERISTIC DATA (for example)

(Ta=25°C / Ta=25°C, unless otherwise specified)

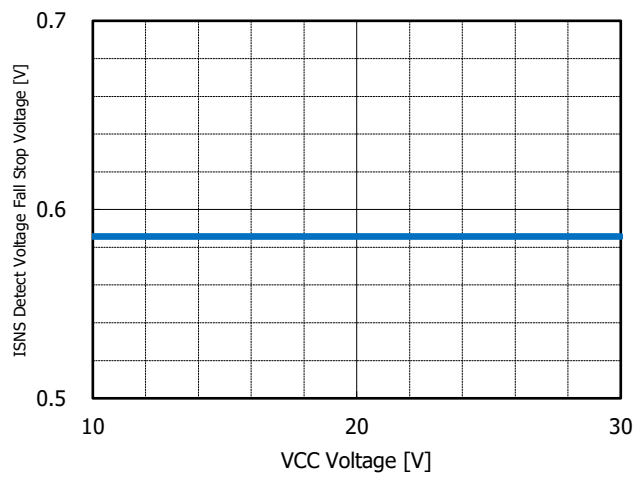
Start-up power supply current - VCC Voltage



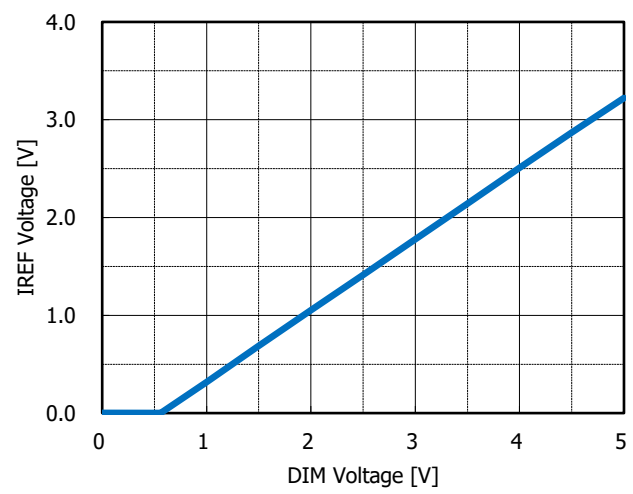
Power supply current - VCC Voltage



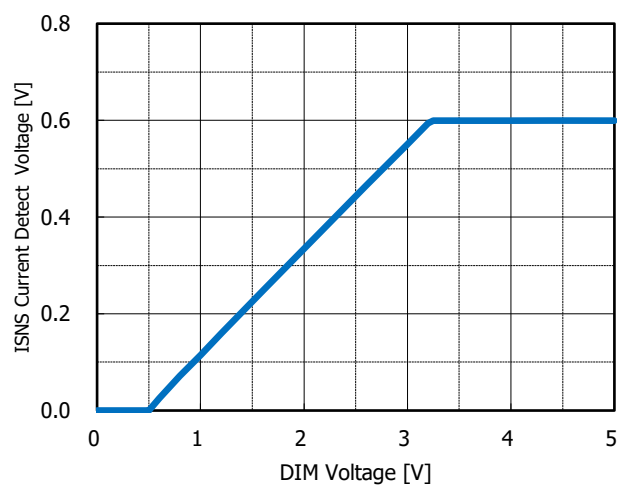
ISNS Detect Voltage Fall Stop Voltage - VCC Voltage



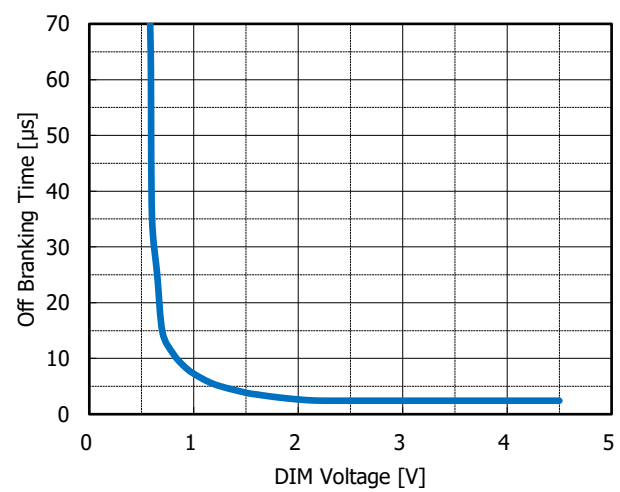
IREF Voltage - DIM Voltage



ISNS Current Detect Voltage - DIM Voltage



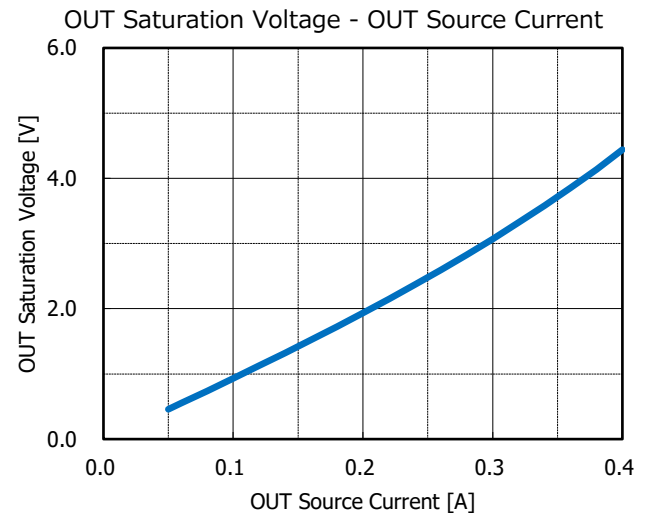
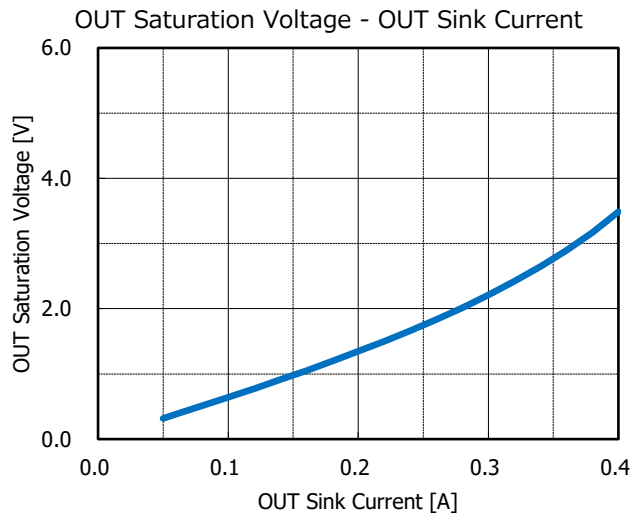
Off Branking Time - DIM Voltage



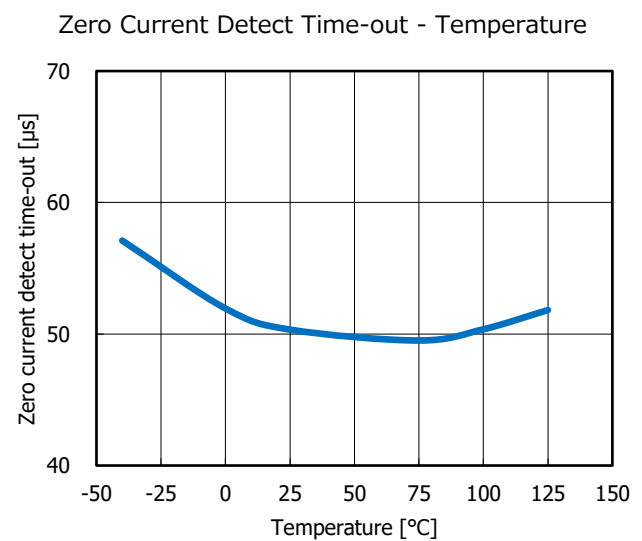
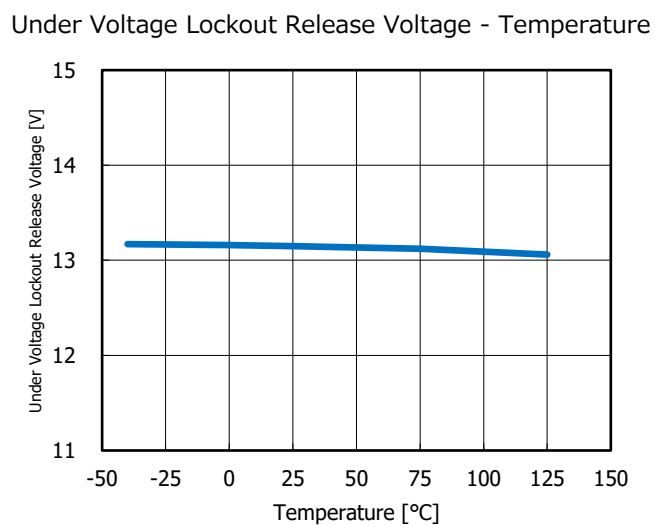
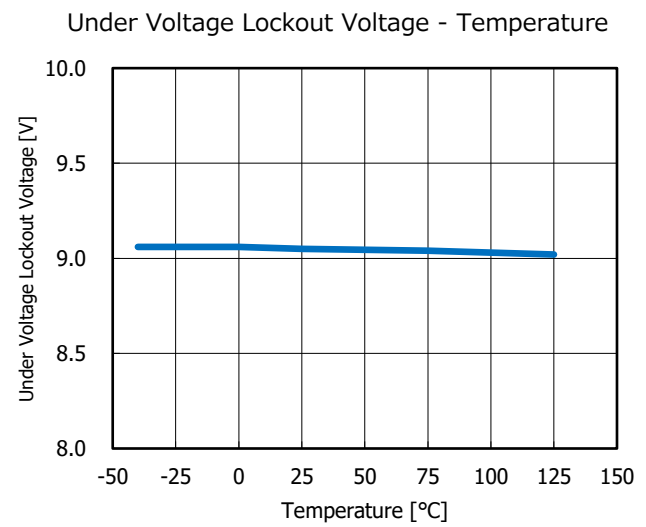
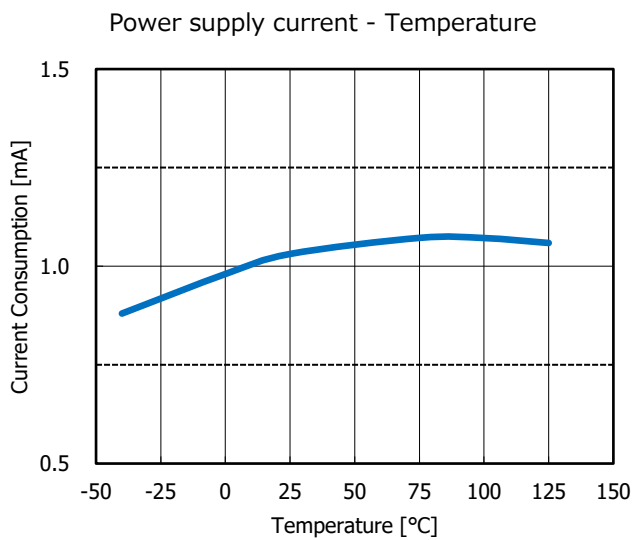


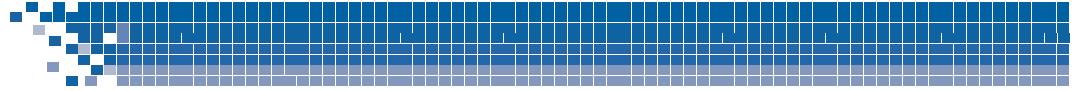
CHARACTERISTIC DATA (for example)

($T_a=25^\circ\text{C}$ / $T_a=25^\circ\text{C}$, unless otherwise specified)



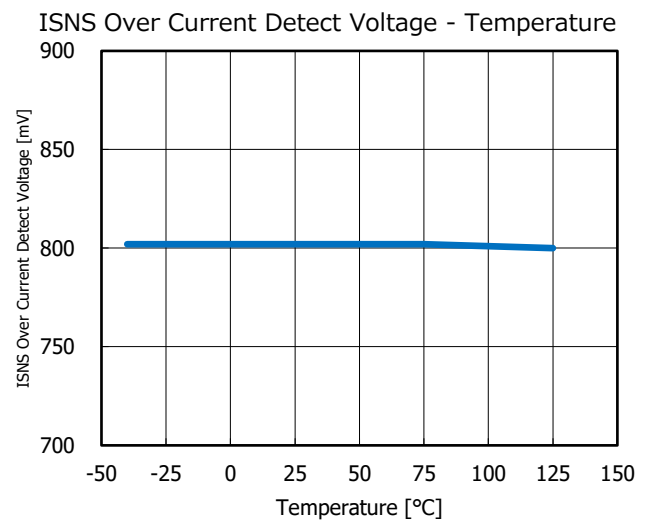
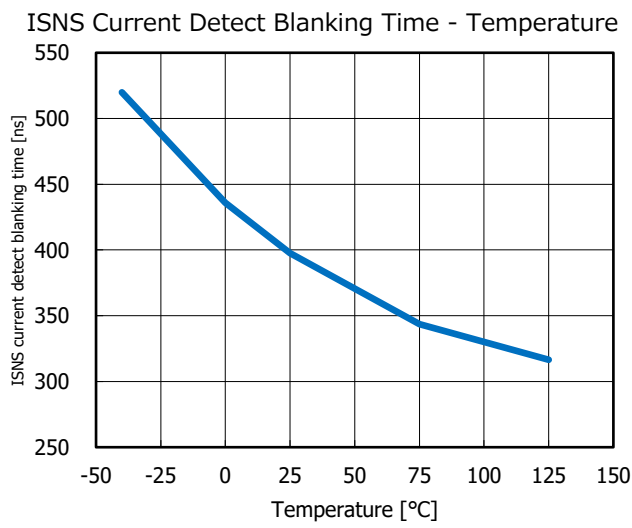
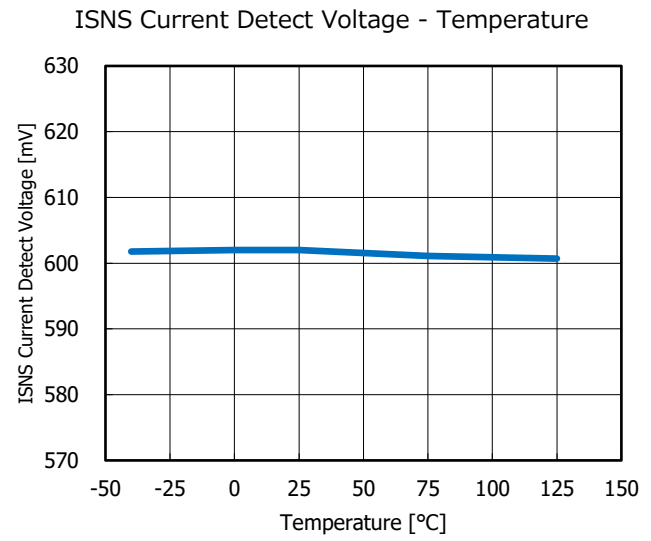
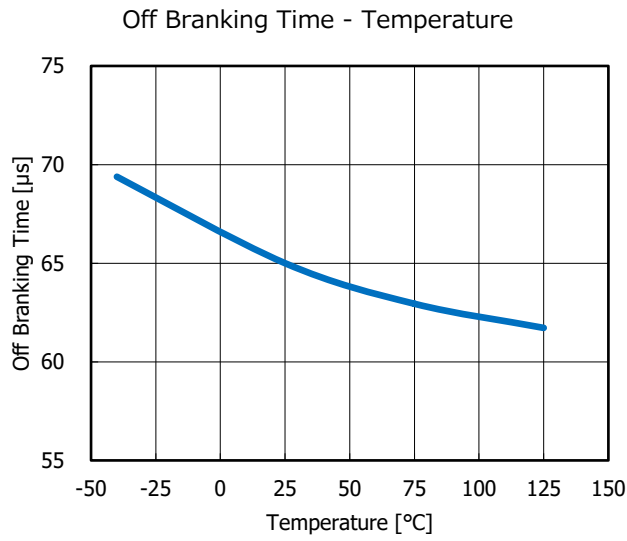
($V_{CC}=15\text{V}$ / $V_{CC}=15\text{V}$, unless otherwise specified)

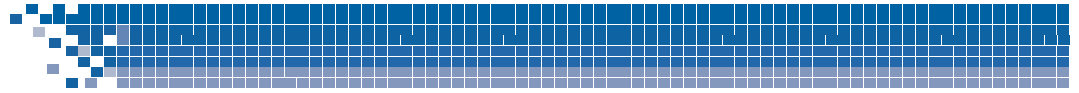




CHARACTERISTIC DATA (for example)

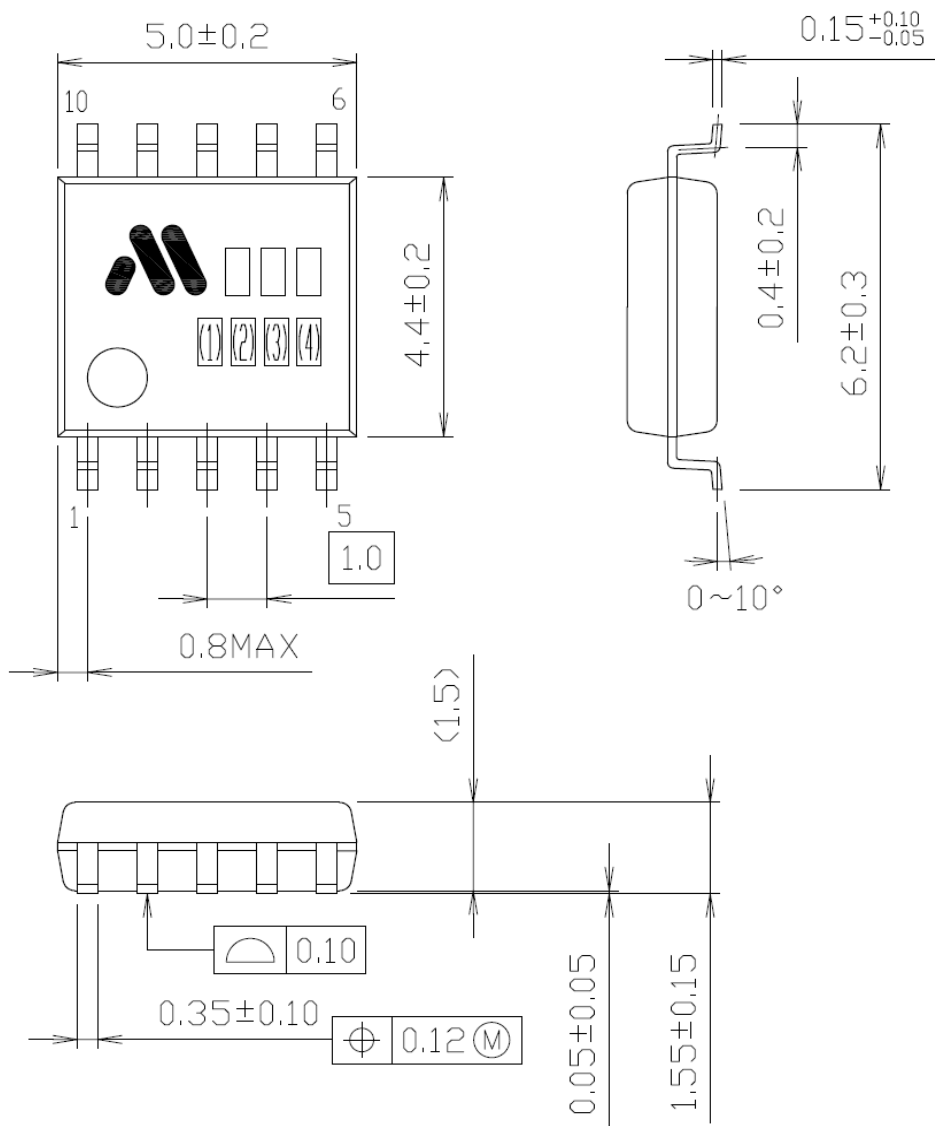
(VCC=15V / VCC=15V, unless otherwise specified)





Dimensions

UNIT mm



Marking Contents

