

PST852A

2024/02/28

Overview

This is a reset IC with an independent voltage detection monitor terminal VS and VDD terminal. The IC power supply is separate and so, even if the monitor voltage VS is low, the output does not become unstable at the operation limit like conventional reset ICs. Instead it maintains low-level operation. This IC is particularly suitable for low-voltage (1V type) power monitoring. It has an accuracy of $\pm 1.5\%$ and an ultralow current consumption of $0.35 \mu\text{A}$ typ. and otherwise offers characteristics resembling those of conventional IC reset ICs.

Application

- Reset circuits for microcomputers, CPUs and MPUs
- Reset circuits for logic circuits
- Battery voltage check circuits
- Back-up power supply switching circuits
- Level detection circuits

Features

High accuracy detection,
Low current consumption

Main specifications

Absolute maximum rating [V]	Recommended operating voltage Min. [V]	Recommended operating voltage Max. [V]	Detection voltage Min. [V]	Detection voltage Max. [V]	Detection voltage accuracy [%]	Consumption current [μA]
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12.0	0.70	10.00	0.8	6.0	±0.5	0.35
Output type	Output Logic	Separated sense pin	Manual reset	Circuit structure		
Open drain	Active L	Yes	No	1ch Reset		
Operating Ambient Temperature Min. [deg.C]		Operating Ambient Temperature Max. [deg.C]		Hysteresis voltage Typ. [V]		
-40		105		VTH(Typ.)×0.05		
Additional function						
Separated Sense pin						

Package

[SC-82ABB](#)[SOT-25A](#)[SSON-4B](#)

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Case Studies

No amplifier or software design required. Development of an LDO for automobiles with open load/short circuit detection function. [Power Supply IC]